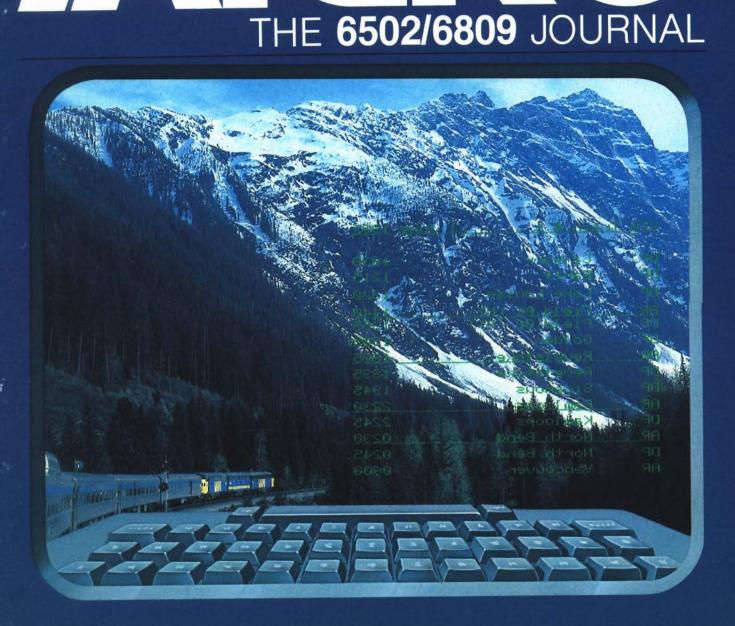
SEPTEMBER 1982

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68000 Feature

A/D Conversion on the Atari
Superimposing TV Pictures with the PET
Apple Program Compressor



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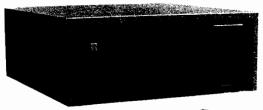
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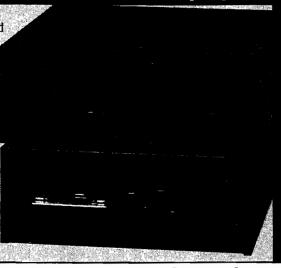
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September Highlights

68000 Feature

Once again the microcomputer industry has a new and exciting product to offer users: the 68000 microprocessor. Dozens of companies have already built computers around this chip, or developed compatible software and peripherals. The big news about the 68000 is that it can be used as a powerful add-on to your 6502.

Laurence Kepple, in "The 68000 and the Personal Computer" (p. 27), discusses the 68000 as a 6502 add-on. He offers some insights on the manufacturers and their products. If you're interested in adding a 68000 to your present system, take a look at this article.

We're providing a very useful and detailed series on the 68000 microprocessor itself, authored by Joe Hootman, a Professor of Electrical Engineering at the University of North Dakota. The first part (p. 41) contains a brief introduction to the 68000 and detailed treatment of the data movement commands. Future installments will offer tables on the 68000's instruction set.

Tom Whiteside and Joe Jelemensky of Motorola, manufacturer of the 68000, describe the registers, instruction set, and addressing modes of this microprocessor (p. 32).

Our data sheet this month will serve as a handy reference to 68000 programmers.

6809

Hal Clark presents an article (p. 57) for 6809-based machines to give a Pascal-like structure to your machine-language programs. This makes the program easier to read and debug while maintaining the execution speed and memory efficiency of assembly language.

A BASIC program to calculate market projections on industry and company sales, by Len Suckle, is presented for the Color Computer (p. 67). It could be translated to other BASICs fairly easily and is well documented so you could customize for a specific application.

Apple

For Apple users, Barton Bauers has written an extremely useful machine-language utility to reduce the length of Applesoft BASIC programs and increase the speed of loading and execution (p. 89). It allows you to fully document your program with remarks and single line statements for legibility, and then remove the remarks and concatenate all non-referenced lines for a streamlined version.

This month's "Apple Slices" column (p. 47) by Tim Osborn, presents a quick random-access technique called "hashing." If you want to speed up access to your file programs that do a lot of random accessing, here is the solution.

We welcome Dr. Richard C. Vile, Jr., as an Apple contributing editor. Richard will be working with our inhouse Apple specialist, Phil Daley, to expand our Apple coverage.

Commodore

There are two articles of special interest to PET/CBM owners. Peter Hiscocks (p. 11) describes a hardware technique to simultaneously display on the PET's screen the computer's output and the image from a closed-circuit TV camera. Although the PET does not have access to the image, it can be programmed to display fancy captions or to point out certain features. The picture could also be used to enhance an otherwise dull computer run.

"Auto SAVE for the PET" by Louis Sander (p. 83) is a BASIC subroutine you attach to your programs during development. It ensures that you make copies of your program at regular intervals, thereby giving you more protection from unanticipated disasters.

Loren Wright's "PET Vet" column (p. 78) discusses Eastern House Software's MAE macro assembler and some new Commodore-oriented publications.

We would like to welcome Dave Malmberg and Jim Strasma as contributing editors in the Commodore area.

Atari

We are happy to have Paul Swanson on board now as a contributing editor and Atari columnist. Paul's column will begin in November. He will also help us increase our base of Atari authors.

Straightforward Garbage Collection

This program by Cornelis Bongers appeared in our August issue, page 90 (also one of the utilities on disk). A few readers missed the important note that appeared at the end of Mr. Bongers' article. Here it is again, in case you missed it too:

The machine-language program starts at \$9000 and has a length of \$260 bytes. After assembling the text file and storing it to disk, the program can be installed by: BRUN programname. This command executes an initialization routine that sets HIMEM to \$9000 and installs the & vector. If you want to BRUN the routine from within an Applesoft program, the BRUN command should be inserted at the first line of the program, and must be followed by a CLEAR command. For example:

10 PRINT "BRUN program name": CLEAR: REM control D behind first quotes.

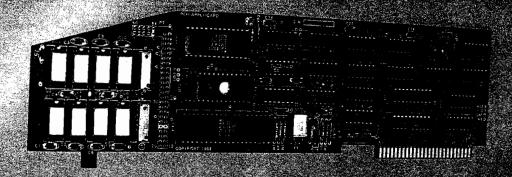
The program makes use of some Applesoft routines in ROM. If the RAM version of Applesoft is being used, the relevant subroutine calls have to be adjusted.

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VIA Train Number 1, "The Canadian," makes its way westward across the continental divide. Nearly three days ago it left Montreal and tomorrow morning it will be in Vancouver, British Columbia.

Computers are used by travel agencies to keep transportation schedules, such as this one, readily accessible. A microcomputer could be installed on board the train and tied into a radio communications network with other, larger computers. The Canadian Pacific Railroad (whose tracks VIA is using) runs long sulfur, coal, and grain trains with computer-controlled "robot" locomotives midway through the train. Every change in speed or gear made by the crew at the head of the train is duplicated by the robot locomotives.

Photo by Loren Wright, MICRO Editor

MCRO is published monthly by: MICRO INK, Chelmsford, MA 01824 Second Class postage paid at: Chelmsford, MA 01824 and additional mailing offices USPS Publication Number: 483470 ISSN: 0271-9002

Send subscriptions, change of address, USPS Form 3579, requests for back issues and all other fulfillment questions to

MICRO INK 34 Chelmsford Street P.O. Box 6502 Chelmsford, MA 01824 or call 617/256-5515 Telex: 955329 TLX SRVC 800-227-1617

Subscription Rates Per Year \$24.00 U.S. 2 yr. / \$42.00 Foreign surface mail \$27.00 Air mail: \$42.00 Europe Mexico, Central America. Middle East, North Africa, Central Africa \$48.00 South America, South Africa, Far East, Australasia, New Zealand \$72,00

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Editorial

When Worlds Collide

The 68000 microprocessor is one of a number of new chips that are making it increasingly difficult for minicomputer manufacturers to justify their existence as an enterprise distinct from the world of microcomputing. More than market share and terminology are at stake. The critical issue is: what role does the individual user play as part of a computer system? In a microcomputer system, the computer and the user are on equal terms, one-on-one partners in a problem-solving endeavor. In a minicomputer, multiuser environment, the individual user is simply less important. Many users must compete for the attention of the CPU. Waiting in line has never been popular, and some people are asking why, in this era of ever cheaper hardware, there are those who say we should go on waiting. For example Hal Hardenbergh, in his newsletter DTACK Grounded, speaks up for the individual user with some vigor: "Refusing to give each user his or her own individual, undivided processor chip is prima-facie evidence of utter contempt for the customer. And utter contempt is all that a minicomputer type has for us personal computer types!"

"Utter contempt"? Surely Mr. Hardenbergh goes too far. However, consider the following observation from Ms. Andrea Lewis in a recent issue of Computer Design: "Putting a minicomputer operating system on a toy computer will not result in a cheap minicomputer." By "toy computer," Ms. Lewis means to indicate "microcomputer." She goes on, "To support multiple users in a true timesharing environment, reliable memory protection must be provided to keep one errant user from disrupting the other users' work on the system." A "true timesharing environment"? Is there something intrinsically valuable about timesharing? Useful when hardware was fabulously expensive, timesharing still requires costly overhead, such as the "reliable memory protection" to which Ms. Lewis refers.

Other costs of timesharing are more subtle. For example, VisiCalc, a program that was conceived especially for ''toy computers,'' does not work well in a timesharing environment. As Tracy Licklider, Vice President of Software Arts, noted recently, "VisiCalc is a screen-intensive, highly interactive program. Timesharing systems simply cannot process VisiCalc's frequent demands for screen refreshes. In a minicomputer, multiuser environment, VisiCalc gets bogged down." Thus, one of the most powerful programs in history succeeded in part because its designers were thinking in terms of a one-on-one relationship between the computer and its user.

Interactivity is the key, because interactivity means maximum user involvement. What minicomputer manufacturers have yet to understand is that the more involvement users have in a computer system, the more powerful the computer system becomes. As Hal Hardenbergh puts it: "To a minicomputer type, a minicomputer which has seventeen applications programs available for it is described as 'heavily supported.' You tell the owner of a PET, Apple, or [TRS] 80 that a particular personal computer has a thousand application programs available and they will reply, 'Oh? What's wrong with it? Isn't it a popular machine?' " The fantastic support available for a micro like the Apple is a direct result of user involvement.

Even those with a minicomputer orientation are beginning to think in terms of "one person, one processor." Computer Business News recently quoted Motorola Inc.'s Tom Starnes as saying that, though everyone does not need distributed processing capabilities in their applications now, almost everyone will "run out of gas" in terms of processing capability using a single system bus. "Which is why applications are turning more frequently to distributed processing where a number of different processors each performs their own isolated programs," he contended. As for timesharing systems, Starnes said, "It is not much more expensive to have a [16-bit chip] and memory for user programs in the workstation itself. The benefit is the user has a high-performance processor all to himself." To which we at MICRO say, "Amen."

Letters/Updates

Atari DOS Bug

Dear Editor:

There is a bug in the Atari DOS (or BASIC's disk I/O) that can clobber up to half of RAM page 6, the "safe" ram. Since the operation of this bug isn't obvious, it can cause incredible amounts of trouble; I spent two days going over a USR subroutine to try to find the bug, before I discovered that my routine had nothing to do with it.

A bug in either DOS (DOS I, DOS 2.0's, and OS/A tested), the BASIC cartridge, or the 10K OS ROM (Version A tested), causes the location of the disks' I/O buffer to move from around location 7000-8000 (decimal) to location 1408, which is only 128 bytes below location 1536, the beginning of page six.

This means that an input or output of more than 128 bytes will destroy some or all of the first 127 bytes of page six!

David H. Simmons P.O. Box 7000-140 Redondo Beach, CA 90277

Othello Update

Phil Daley, MICRO's Technical Editor for the Apple, offers this update:

In the program Othello (MICRO on the Apple, Vol. 3), Charles Taylor presents a two-person game with the computer keeping track of the board position and legality of moves. With the minor modifications presented here, the computer will play a level 1 game of Othello for those who need an opponent. This would be especially useful in learning how to play the game.

Lines 16-135 contain adjustments to the Taylor program corresponding lines. Lines 173-183 are new material. There was a minor bug in the program as published. The only way the program knew that the game ended was when all 64 blocks were filled. It is possible to not have any moves left

Atari Bug Demo

```
TESTING INSTRUCTIONS:
A: Type in program.
B: SAVE to disk as "D:BUGTEST.BAS".
C: Type "RUN".
0100 REM :
                 BUG-DEMO
0110 REM : BY DAVID H. SIMMONS
0120 DIM IN$(256)
0130 DPEN #1,4,0,"D:BUGTEST.BAS"
0140 REM (OR OTHER 'TEXT RECORD' FILE)
0150 ? "IOCB BUFFER ADDRESS: ";PEEK(852)+256*PEEK(853)
0160 REM : *(IOCB #1 ICBAL/ICBAH)*
0170 INPUT #1; IN$: INPUT #1; IN$
0180 ? "AFTER INPUT: "; PEEK (852) +256*PEEK (853)
0190 ? " +128= 1536, START OF PAGE SIX!":? "I/O Of more than
128 bytes WIPES OUT anything in page six!"
0200 CLOSE #1:END
```

when there are still unfilled spaces. Line 135 allows a player to enter "DONE" when this condition occurs and thereby end the game.

If you wish to design a level 2 game, a subroutine to examine how many "captures" could occur on each possible move would improve the computer's chances.

Fan Improvement

Dear Editor:

I recently purchased a System Saver fan by Kensington Software. Although the fan works as advertised, I have made two improvements that your readers might be interested in.

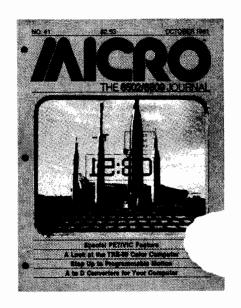
First I covered the vent slots on the right side of my Apple with a section of air-conditioning filter. This prevents dust from being drawn across the inside of the computer. The filter was attached with duct tape to the outside of the computer.

The second improvement was to place 1/8-inch thick foam rubber between the part of the fan case and computer that come in contact with each other. This minimizes vibration.

Robert Gershowitz 76-51 169 Street Flushing, NY 11366

```
Listing for Othello Line Changes
```

```
16 DIM BOARD(9,9): DIM COMPUTERMOVE$(61)
   FOR I = 1 TO 8: READ DY(I): NEXT I: FOR I = 1 TO 61: READ CO$(I): NEXT
56 TURN = 2: GOSUB 181
60 COLOR= CC(TURN): ZZ = 0
62 IF NOT Z OR TURN = 1 THEN PRINT PROMPT$ (TURN)
133 IF Z AND TURN = 2 THEN ZZ = ZZ + 1:MOVE$ = CO$(ZZ): GOTO 135: REM
                  SUBR GETMOVE
135 PASS = 0: IF MOVES = "DONE" THEN PASS = 1:Q = 100: RETURN
173
     DATA Al, A8, H1, H8, B1, B8, G1, G8
     DATA A2,A7,H2,H7,C1,C8,F1,F8
174
175 DATA A3, A6, H3, H6, D1, D8, E1, E8
176 DATA A4,A5,H4,H5,C4,C5,F4,F5
177
     DATA D3,D6,E3,E6,C3,C6,F3,F6
178
     DATA D2,D7,E2,E7,B4,B5,G4,G5
179
     DATA B3, B6, C2, C7, F2, F7, G3, G6
180
     DATA B2, B7, G2, G7, P
    HOME: PRINT "DO YOU WISH TO PLAY THE COMPUTER?" INPUT 2\$: IF LEFT$ (2\$,1) = "Y" THEN Z=1
181
182
183
     RETURN
                                                                          MICRO
```



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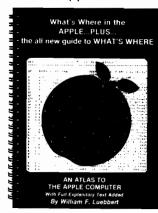
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Superimposing TV Pictures on PET Video

by Peter D. Hiscocks

The Commodore PET may be modified to superimpose the picture from a television camera on the PET video display. The resultant combined picture (PET video and television image) may also be fed to a remote monitor or video tape recorder.

I had to superimpose the picture from a television camera on the PET video display as part of an airborne camera control unit (see reference). The same concept could be useful in a variety of situations. For example, the composite video signal - television picture plus PET graphics and alphanumerics could be fed to an extension monitor or video tape recorder and the PET would become a very sophisticated video titling system. In another situation, the signal from a microscope-mounted television camera could be fed to the PET and the area of cells determined by tracing with a light pen.

Note that the television picture is not available to the PET; it is simply overlaid on the display, and the PET doesn't know of its existence. A block diagram of the system is shown in figure 1.

A first requirement of the system is that the television camera scan rate be synchronized to that of the PET display. Fortunately, the vertical and horizontal sync signals are available at the PET User Port. These are modified to a suitable form by the sync circuits shown in figure 2, and fed to the television camera. The television camera must be able to accept and lock on these signals, but this is a common requirement of cameras that are used in studio situations. I used the Sanyo 1620 X camera.

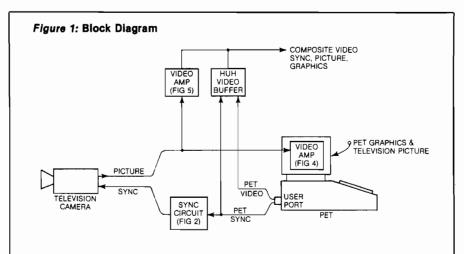
The next requirement of the system is that the display be able to show a continuous grey scale from full dark to

full brightness (or greenness, in the case of the new PETs). The PET display, of course, only has to deal with the two extremes of brightness. The grey scale is dealt with by the video amplifier (figure 4) and the modifications to the PET video display (figure 3).

In the existing PET display circuitry, the video amplifier switches the cathode of the CRT between 35 volts (dark level) and ground (white level).

The added video amplifier controls the power supply rail of the existing amplifier, thereby setting the maximum dark level and creating a continuous grey scale in response to the video signal. PET characters (white) are visible as long as the video picture is not completely white.

Modifying the PET video display is not for the faint-hearted or the novice electronic tinkerer. There are high



PET USER PORT
HORIZONTAL
SYNC

VERTICAL
SYNC

12

Vertical
SYNC

V

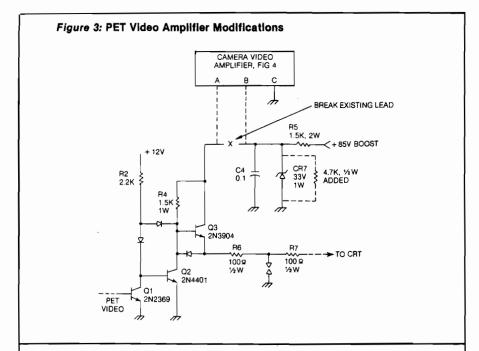


Figure 4: Camera Video Amplifier

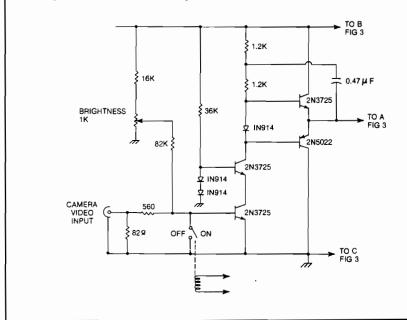
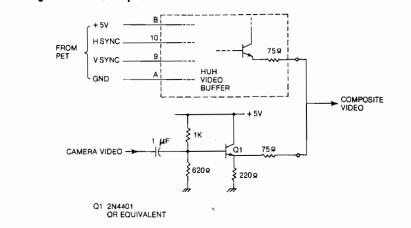


Figure 5: Video Amplifer



voltages inside the display, and a mistake could fry you and/or you PET. The circuit details that I show it figure 3 apply to the early PETs and you'd be wise to confirm the circuitry i you have a newer version.

The circuitry of the added video am plifier should be copied fairly closely especially in the choice of transistor: (the bandwidth requirement for the amplifier is in the order of 3.5 MHz). made up the circuit on Veroboard and mounted it inside the case of the PET display. In my installation, I used a relay to kill the television picture so the toggle switch could be placed or the front panel, without having to rur signal-carrying leads up to the from panel. Leads from the added video amplifier to the existing video amplifier should be kept as short as possible and well away from the display flyback transformer area.

In figure 3, the addition of the 4.7 Kohm resistor prevents excessive power dissipation in the 33-volt zene; diode.

Figure 1 also shows how the combined picture and graphics image may be sent to some external device such as an external video display or video tape recorder. The video amplifier of figure 5 buffers the video signal from the television camera and the HUH Video Buffer (HUH Electronics, 1429 San Mateo, CA 94402) develops a composite sync signal from the PET sync signals. At alternative suitable circuit for the latter is given in *The PET Revealed*, by Nick Hampshire, page 86.

Reference

"A Computer-Based Camera Control System," by A. Roberts and P. Hiscocks, *Photogrammetric Engineering and Remote Sensing*, Vol. 47, No. 1, Jan. 1981, pp. 53-57.

Peter Hiscocks is an instructor at Ryerson Polytechnical Institute, where he teaches courses in Electrical and Theatre technology. He is a columnist for Audio/Video Canada, builds computer interfaces on a freelance basis, and is currently working on a computer-controlled sound system for the reproduction of insect sounds at the Royal Ontario Museum. He may be contacted at Electrical Dept., Ryerson Polytechnical Institute, 50 Gould St., Toronto, Canada M5B 1E8.

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A/D Conversion Using a 555 Timer IC

by Mike Dougherty

For applications not requiring high-speed analog to digital conversion, this simple 555 Timer circuit yields a high dynamic range for very low cost. Although demonstrated with an Atari 800, this 555 A/D converter may be used by any computer system with one unused bit on an input port.

These programs require:

Atari 800 555 Timer IC and other electronic components

The Atari 800 already contains hardware capable of digitizing a resistive sensor, such as a thermistor or photoresistor, through a game controller jack. During each video scan, the resistance of each paddle is measured by a custom-designed LSI chip called POKEY. At the end of a video scan, the Atari Operating System reads the POKEY registers and places the digitized values in the paddle shadow registers in memory. These values are accessed through the PADDLE() function of BASIC.

Since the POKEY digitization is done by hardware and the shadow registers are maintained by the operating system, this analog to digital conversion method is easy to use. Simply replace the paddle controller with the desired resistive sensor and obtain the digitized result *via* the proper PADDLE() function. The actual A/D conversion will be performed automatically by the hardware and updated by the operating system every 1/60th of a second.

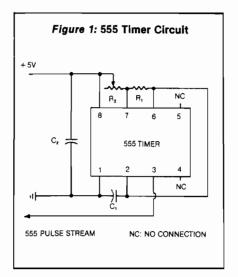
As straightforward as this approach seems, there are two major disadvantages. First, the Atari 800 hardware was designed for the $1M\Omega$ potentiometer used in the paddle controller. To fully utilize the Atari hardware, the variable

resistance to be measured should cover this range. Often it is difficult for the hobbyist to find a resistive sensor with this full range. Secondly, even if such a resistive sensor is found, the Atari hardware can only resolve one part in 228. This yields an effective A/D conversion of $LOG_2[228] = 7.8$ bits. There are many applications which could use finer resolution or greater dynamic range.

This article discusses the use of an inexpensive 555 Timer circuit, the software required to digitize the 555 output, and software designed to display the 555 Timer waveform on the Atari 800. Although the Atari 800 is used to demonstrate the 555 Timer circuit, the general principles may be applied to any 6502-based computer.

Hardware

The 555 Timer is available from Radio Shack in an eight-pin DIP package for approximately \$1 (Radio Shack part number 276-1723). This application uses the 555 as a free-running astable multivibrator, shown in the circuit of figure 1. In this mode of operation, the 555 outputs a TTL-compatible pulse stream on pin 3. The waveform of the pulse stream is determined by the external resistors, R_1 and R_2 , and the



capacitor, C_1 . For each single pulse, pin 3 remains at a low voltage for a time, t_L , proportional to R_1 . Pin 3 remains at a high voltage for a time, t_H , proportional to $R_1 + R_2$. Specifically, these times are defined:

 $t_H = 0.693 (R_1 + R_2) (C_1)$ seconds

 $t_L = 0.693 (R_1) (C_1)$ seconds

Thus, as the resistive sensor R_2 varies, the time that the pulse is high also varies.

By attaching 555 pin 3 to an input pin on a parallel port, the length of time that the pulse is high may be measured by computer clock speed. In the astable mode, only a single input line is required to digitize R_2 .

The circuit of figure 1 was powered through the joystick #4 controller jack. Pin 7 of joystick #4 supplies the +5 volt source, while pin 8 is the Atari ground. With the Atari 800 providing the +5V power source through the controller jack, a random variability up to 10% was observed. This error was eliminated with the addition of a filtering capacitor, C_2 , across the +5V source and ground.

Software

One simple approach to measuring the high portion of the pulse is outlined by the "pseudocode" in listing 1.

The first two DOWHILE loops allow the pseudocode to synchronize with the free-running 555 pulse stream. In the worst case, this synchronization will take $0.693(2R_1+R_2)(C_1)$ seconds. Because of this relationship, R_2 was chosen as the resistive sensor and R_1 was made as small as possible. Thus the time that a pulse is low is kept constant by R_1 and is only long enough for the first and third DOWHILE loops to recognize the zero level of the 555 output.

The time represented by each count is determined by the length of the third

DOWHILE loop and the specific delay utilized. Typically, the exact length of the third DOWHILE loop is not important since most applications will require a series of calibrations to map the digitized counts onto the physical quantity being measured. In practice, the third DOWHILE loop is written to execute as fast as possible. If this is not fast enough, then the value of capacitor C_1 is increased. By controlling both the measuring loop and the capacitor value of C_1 , a wide range of resistive sensors may be utilized.

As an example, the 6502 assembler subroutine in listing 2 demonstrates a 19-cycle timing loop. With a 1 MHz clock, this loop will sample the input line, bit 0 of PORT, every 19 microseconds. Assume that C_1 is $0.1 \mu F$, R_1 is fixed at $2K\Omega$, and the resistive sensor covers the range from $2K\Omega$ to $700K\Omega$ (the practical range of the paddle game controllers). Then tH will vary from 0.2777 ms at 2KΩ to 48.6 ms at 700KΩ with the digitized count varying from COUNT = 15 to COUNT = 2558, a range of 2543 counts. The worst case time to synchronize and take the sample at the maximum resistance is 2t_H + $t_L = 97.5 \text{ ms}$, or approximately 1/10thof a second.

If a longer delay may be tolerated, then C_1 may be increased to $1\mu F$. In this case, t_H will vary from 2.77 ms to 486 ms with the digitized count varying from 146 to 25579, a range of 25433 counts. Thus the $0.1~\mu F$ capacitor gives a digitized dynamic range of $LOG_2[2543] = 11.3$ bits; the $1.0~\mu F$ capacitor gives a dynamic range of $LOG_2[2543] = 14.6$ bits. If time is not a critical factor, the 555 approach allows high dynamic range for very low cost.

Atari 800 Considerations

The approach outlined in the previous sections may be implemented on the Atari after dealing with two operating system problems. Although these problems are specific to the Atari, other computer systems may have similar difficulties.

The Atari computer system drives the video display through direct memory access (DMA). A sophisticated approach utilizing the custom-designed LSI chips, called ANTIC and CTIA, provides the graphics versatility of the Atari. To optimize the video performance, the Atari has a basic clock speed of 1.79 MHz rather than the more common 1 MHz. However, the actual speed of program execution depends

Listing 1: Pseudocode A/D Conversion

```
BEGIN Pulse;
  initialize COUNT to zero;
  DOWHILE line is not equal to zero;
    read I/O port;
    logically "AND" with MASK to isolate 555 line;
  ENDWHILE;
  DOWHILE line is equal to zero;
    read I/O port;
    logically "AND" with MASK to isolate 555 line;
  ENDWHILE;
 DOWHILE line is not equal to zero;
    increment COUNT by one;
    read I/O port;
    logically "AND" with mask to isolate 555 line;
  ENDWHILE;
END Pulse.
```

Listing 2: Sample 6502 A/D Conversion Subroutine

```
0100
                  0110
             9120
                    LISTING # 2
             0130
             9140
                    SAMPLE 6502 SUBROUTINE TO COUNT
                    THE NUMBER OF LOOPS THAT BITO OF I/O PORT IS HIGH, THE RESULT IS
             0150
             0160
             0170
                    LEFT IN ZERO PAGE MEMORY.
                    COUNTING LOOP CONTAINS "DELAY" INSTRUCTIONS TO MAKE THE LOOP
            0180
0120
             0200
                    EXECUTION TIME 19 CYCLES.
             0210
            0220
0230
                  0240
             0250 PORT
0301
                               $D301
                                          PORTE OF ATARI
            0260 COUNT
0270 ;
0004
                         =
                               $D4
                                          ZERO PAGE RESULT BUFFER
0000
             0280
                          ¥=
                               $0400
                                          ATARI FREE MEMORY
             0290
0600 A900
             0300 PULSE
                         LDA
                               *$00
                                          INIT Y, X AS COUNT
0602 AA
             0310
                          TAX
                                          THIS IS FASTER
0603 AB
             0320
                          TAY
                                          THAN MEMORY
             0330
             0340
                    SYNCHRONIZE WITH 555 PULSE STREAM
             0350
0604 AD01D3 0360 SYNCHI LDA
                                          INPUT ALL 8 LINES MASK TO ONLY BITO
                               PORT
0607 2901
             0370
                          AND
                               *$01
0609 DOF9
             0380
                          BNE
                               SYNCHI
                                          STILL HIGH, WAIT TILL LOW
             0390
060B AD01D3
            0400 SYNCLO LDA
                               PORT
                                          INPUT ALL 8 LINES
                                          MASK TO ONLY BITO
060E 2901
             0410
                          AND
                               * * 0 1
0610 F0F9
                          BEQ
                               SYNCLO
             0420
                                          STILL LOW, WAIT TILL HIGH
             0430
             0440
                    COUNT # LOOPS PULSE IS HIGH
             0450
0612 E8
             0460 CNTLP
                         INX
                                          COUNT THIS TIME IN LOOP
0613 0000
             0470
                               NOCARY
                                          NO CARRY TO MSB
                          BNE
                                          ADD CARRY TO MSB
0615 C8
             0480
                          INY
0616 EA
             0490
                          NOP
                                          DELAY FOR 4 CYCLES
0617 EA
             0500
                          NOP
0618 AD01D3
            0510
                               PORT
                          LDA
                                          INPUT I/O PORT
061B 2901
             0520
                          AND
                               #$01
                                          ISOLATE BITO
061D D0F3
             0530
                          BNE
                               CNTLP
                                          STILL HIGH -- COUNT ON
061F F00A
             0540
                          BEQ
                               RETURN
                                          WRAP IT UP
             0550
0621 EA
             0560 NOCARY NOP
                                          DELAY FOR 5 CYCLES
0622 A5FF
             0570
                         LDA
                               $FF
0624 AD01D3
            0580
                          LDA
                               PORT
                                          INPUT I/O PORT
0627 2901
             0590
                          AND
                               #$01
                                          ISOLATE BITO
0629 D0E7
             0600
                          BNE
                               CNTLP
                                          STILL HIGH -- COUNT ON
             0610
                    RESULT IN Y,X REGISTER PAIR
             0620
             0630
0628 8604
             0640
                  RETURN STX
                               COUNT
                                          SAVE IN ZERO PAGE
062D 84D5
             0650
                          STY
                               COUNT+1
062F 60
             0660
                          RTS
8630
                          .END
             0670
```

upon the amount of "cycle stealing" DMA activity required to run the video screen. As the graphics mode uses more memory to define the video screen, more DMA data transfers are required to refresh the screen. Thus a program will execute slower under GRAPHICS 8 mode than under GRAPHICS 0 mode. In addition, for graphics modes combined with text (e.g. GRAPHICS 8), the DMA rate will differ between displaying the graphics portion and the text portion of the screen. This variable rate DMA affects the effective speed of the counting loop used to measure the 555 pulse width.

Fortunately, there is a simple solution to the DMA problem - turn the DMA off. You can disable the DMA by writing a zero in the DMA shadow register, SDMCTL, located at \$022F in memory. This value is transferred to the DMA controller register by the operating system during the VBLANK interrupt routine, every 1/60th of a second. (VBLANK is the interrupt routine executed after every complete video scan. The Atari Operating System uses VBLANK to update the system time, input the game controller reigsters, transfer shadow registers to and from hardware, and other housekeeping tasks.) Thus to turn off the DMA, simply POKE a zero into memory location \$022F and wait for the system clock to change (low order byte at \$0014). Since VBLANK updates the clock, the change in the system clock value indicates that VBLANK has executed and the DMA shadow register has been transferred to the DMA hardware. To restore the DMA, POKE a \$22 (decimal 34) to the DMA shadow register at memory location \$022F.

The second problem concerns the actual time taken to execute VBLANK. A typical 555 digitization time was about a tenth of a second or longer. During this digitization, VBLANK could interrupt several times. Since processor time would be spent executing the VBLANK routine instead of counting, the digitized value would be too low. In addition, the short low period of the pulse could occur during VBLANK execution and be missed by the counting subroutine. The counting subroutine would erroneously continue until a low pulse was recognized.

The Atari designers anticipated this type of problem also. Although the VBLANK interrupt is connected to the 6502 non-maskable interrupt line, NMI, external hardware was provided to allow an Atari program to disable this NMI interrupt before it reaches the

Listing 3: Atari USR Function A/D Conversion

```
0105
                   LISTING # 3
            0110
            0115
            0120
                   ATARI USR FUNCTION TO COUNT THE
            0125
                   LOOPS WHICH A 555 PULSE IS HIGH.
                   THE STEPS PERFORMED ARE:
            0130
            0135
            0140
                   1) TURN OFF DMA SHADOW REGISTER
            0145
                      WAIT FOR VBLANK TO TRANSFER
            0150
                      THE SHADOW REG TO HARDWARE
            0155
                   3) DISABLE THE VBLANK INTERRUPT
            0160
                      SYNCHRONIZE WITH THE 555
            0165
                      PULSE STREAM WITH 2 LOOPS
            0170
                      COUNT THE NUMBER OF LOOPS
            0175
                      THE PULSE IS HIGH
            0180
                      SAVE THE RESULTS IN THE ZERO
            0185
                      PAGE USR RETURN LOCATIONS,
            0190
                      RESET VBLANK AND DMA
            0195
            0200
            0205
                   USAGE: COUNT=USR(1696,LINE)
            0210
            0215
                      WHERE LINE IS THE MASK TO
            0220
                      ISOLATE THE BIT CONNECTED
            0225
                      TO THE 555 PULSE STREAM.
            0230
                      UPON RETURN, COUNT WILL
            0235
                      CONTAIN THE DIGITIZED VALUE.
            0240
            0245
                 0250
            0255
022F
            0260 DMASR
                             $022F
                                       DMA SHADOW REGISTER
D40E
            0265 NMIEN
                        =
                             $D40E
                                        NMI ENABLE REGISTER
                             $D301
D301
            0270 PORT
                                        PORTB -- JOYSTICK #3,#4
                                        LSB OF REAL TIME CLOCK
0014
            0275 RTC
                             $0014
00D4
            0280 USRRET =
                             $00D4
                                        USR RETURN ADDR
00D1
            0285 LINE
                             $00D1
                                        STORAGE FOR LINE MASK
            0290
            0295
0000
            0308
                        *=
                             $06A0
                                       FREE MEMORY IN PAGE 6
            0305
85 0A60
            0310 ATOD
                        PLA
                                        # OF USR ARGUMENTS
06A1 68
            0315
                        PLA
                                       HIGH ORDER OF LINE
0662 68
            0320
                        PLA
                                       LOW ORDER OF LINE
06A3 85D1
            0325
                        STA
                             LINE
                                       SAVE 8 BIT LINE MASK
            0330
                   TURN OFF DMA SHADOW REGISTER
            0335
            0340
06A5 A900
            0345
                        LDA
                             #$00
06A7 BD2F02 0350
                        STA
                             DMASR
                                       CLEAR SHADOW REGISTER
OAAA AA
            0355
                        TAX
                                        INITIALIZE REGS FOR COUNT
06AB AB
            0360
                        TAY
            0365
                   WAIT FOR VBLANK INTERRUPT
            0370
            0375
06AC A514
            0380
                        LDA
                             RTC
                                       CURRENT LISB OF RTC
044F C514
            0385 RTCWAT CMP
                                       SEE IF RTC HAS CHANGED
                             RTC
06B0 FOFC
            0390
                        BEG RTCWAT
                                       NOT YET, WAIT
            0395
                   DISABLE VBLANK INTERRUPT
            0400
            0405
04B2 A920
            8418
                        LDA
                             $$70
0684 8D0ED4 0415
                        STA
                             NMIEN
                                        CLEAR BITS OF NMIEN
            0420
                   SYNCHRONIZE TO 555 PULSE STREAM
            0425
            0430
06B7 AD01D3 0435 SYNCHI LDA
                             PORT
                                        INPUT ALL 8 LINES
06BA 25D1
            0440
                        AND
                             LINE
                                        ISOLATE 555 BIT
06BC D0F9
            0445
                        BNE
                             SYNCHI
                                        STILL HIGH, WAIT
            0450
068E AD01D3
            0455 SYNCLO LDA
                             PORT
                                        INPUT ALL 8 LINES
06C1 25D1
            0460
                        AND
                             LINE
                                        ISOLATE 555 BIT
06C3 F0F9
            0465
                        BEQ
                             SYNCLO
                                        STILL LOW, WAIT
            0470
            0475
                 ; DIGITIZE TIME THAT PULSE IS HIGH
            0480
04C5 E8
            0485 CNTLP
                        INX
                                        COUNT THIS TIME IN LOOP
                                        NO CARRY TO MSB OF COUNT
CARRY TO MSB OF COUNT
09C9 D00C
            0490
                             NOCARY
                        ENE
04C8 C8
            0495
                        INY
06C9 A5FF
            0500
                             $FF
                                        DELAY 3 CYCLES
                        LDA
                                                           (Continued)
```

Listing 3	(Continued)			
0 GCE: AI	D01D3 0505		LDA	PORT	INFUT I/O PORT
06CE 25	5D1 0510		AND	LINE	ISOLATE 555 BIT
06D0 D	0F3 0515		BNE	CNTLP	STILL HIGH COUNT ON
06D2 F	009 0520		BEQ	RETURN	LOW, WRAP IT UP
	0525	;			
06D4 E	A 0530	NOCARY	NOP		DELAY 4 CYCLES
06D5 E	A 0535		NOP		
04D4 AI	D01D3 0540		LDA	PORT	INPUT I/O PORT
06D9 25	5D1 0545	•	ДИA	LINE	ISOLATE 555 BIT
0.9DE: D	0E8 0550		BNE	CNTLP	STILL HIGH COUNT ON
	0555	•			
	0560	; SAVE	RESUL	.TS, RESTOF	RE HARDWARE
	0565	;			
0 Q D D 8		RETURN		USRRET	USR RESULT BUFFER
04DF 84			STY	USRRET+1	
06E1 A			LDA	#\$60	SET BITA OF NMIEN
09E3 81			STA	NMIEN	TO ENABLE VBLANK AGAIN
09E9 V			LDA	# \$22	
09E8 81			STA	DMASR	ENABLE DMA THRU VBLANK
06EB 6			RTS		BACK TO BASIC
04EC	0605		• END		

Listing 4: USR Function to Trace Atari Port B

```
0105
                   LISTING # 4
            0110
            0115
                   ATARI USR FUNCTION TO SAMPLE
            0120
                   AND STORE THE VALUE OF PORTE
            0125
                   JOYSTICK #3 AND #4.
            0130
                                        THE STEPS
                   PERFORMED ARE:
            0135
            0140
            0145
                   1) INITIALIZE
            0150
                   2) DISABLE DMA AND VBLANK
            0155
                   3) IN 256 BYTE BLOCKS, SAMPLE
                      AND STORE PORTB
            0160
                   4) RESTORE VELANK AND DMA
            0165
            0170
            0175
                   USAGE: X=USR(1536,ADR(DATA$))
            0180
            0185
            0190
                   WHERE DATAS IS A STRING VARIABLE
            0195
                      DIMENSIONED TO BE 1024 BYTES
            0200
                      IN SIZE TO HOLD THE ACTUAL
                      PORTE VALUES.
            0205
            0210
            0215
                 0220
            0225
00CB
            0230 ADDR
                             $00CB
                                        STORAGE FOR DATA ADDR
D301
            0235 PORT
                             $D301
                                        ATARI PORTB
0014
            0240 RTC
                             $0014
                                        LSB OF REAL TIME CLOCK
022F
            0245 DMASR
                             $022F
                                        DMA SHADOW REGISTER
D40E
            0250 NMIEN
                             $D40E
                                        NMI ENABLE REGISTER
            0255
            0260
0 \ 0 \ 0 \ 0
            0265
                             $0600
                                        FREE ATARI RAM
                        * =
            0270
            0275
                   INITIALIZE TRACE
            0280
84 0040
            0285
                 TRACE
                        FI A
                                        # OF USR ARGUMENTS
0601 68
            0290
                        PLA
                                        16 BIT DATA BUFFER ADDR
0602 85CC
            0295
                        STA
                             ADDR+1
                                        MSB OF DATA$
0604 68
            0300
                        FLA
0605 85CB
            0305
                        STA
                             ADDR
                                        LSB OF DATAS
0607 A000
            0310
                        LDY
                             #$00
                                        INDIRECT INDEXED POINTER
0609 A204
            0315
                        LDX
                             #$04
                                        FOUR BLOCKS OF 256 SAMPLES
            0320
            0325
                   DISABLE DMA AND VBLANK
            0330
060E A900
            0335
                        LDA
                             #$00
060D 8D2F02 0340
                        STA
                             DMASR
                                       TURN OFF SHADOW REGISTER
0610 A514
            0345
                        LDA
                             RTC
                                        WAIT UNTIL VBLANK INTERRUPT
0612 C514
            0350 RTCWAT
                        CMP
                             RTC
                                        NEW RTC VALUE ?
0614 F0FC
            0355
                        BEQ
                             RTCWAT
                                        NOT YET, WAIT
0616 A920
            0360
                                        YES, DISABLE VBLANK
                        LDA
                             #$20
0618 8D0ED4 0365
                        STA
                             NMIEN
                                        CLEAR BITS OF NMIEN
            0370
            0375
                   TAKE TRACE IN 256 BYTE BLOCKS
            0380
                                                           (Continued)
```

Table 1: Atarl 800 PORTB Pinouts PORTB Joystick Pin# Bit Mask 0 #3 2 2 1 2 #3 3 4 8 3 #3 4 16 5 #4 2 32 6 #4 3 64 #4 128

6502 chip. This interrupt line is controlled by bit 6 of the hardware register NMIEN at location \$D40E. Clearing bit 6 of NMIEN disables the VBLANK interrupt; setting bit 6 of NMIEN enables the VBLANK interrupt. A word of caution: this register also controls other interrupts. To disable the VBLANK interrupt, POKE a \$20 into NMIEN; to enable the VBLANK interrupt, POKE a \$60 into NMIEN.

A BASIC callable USR function to sample an input pin of PORTB, joystick #3 and #4, is given in listing 3. The USR argument is used to mask the 8-bit I/O port to the single I/O line attached to the 555 pulse stream of pin 3. The pinouts for PORTB and associated masks are outlined in table 1.

TRACE

To test the 555 circuit, a simple program was written to trace the PORTB input lines and store the results in memory to be later plotted on the screen. This program, TRACE, takes a trace of 1024 samples and plots the results in GRAPHICS 8 mode *via* the two USR functions in listings 4 and 5. TRACE also uses the USR function of listing 3 to allow the pulse stream to be digitized. The BASIC program is presented in listing 6.

TRACE allows any input pin of PORTB to be sampled and plotted. The specific line is determined by the value of the bit mask variable, LINEMASK. Refer to table 1 for the PORTB pinouts and corresponding mask values.

The loops used to perform the A/D conversion and to trace the waveform take 19 cycles. However, TRACE does contain one timing error. The trace function breaks the 1024 samples into four blocks of 256 single byte samples. This is because the 6502 indirect indexed addressing mode handles 256-byte blocks quickly and conveniently. The extra clock cycles between each 256-byte block were preferred over slowing down the basic loop simply to sample all trace points at equal time intervals.

The plot of the trace is done by a USR function simply to make TRACE more convenient to use. The short marks displayed to the left of each 256-byte trace segment indicate logic 1 and 0.

These techniques form the basis for measuring physical quantities through resistive sensors inexpensively. With very simple circuits much of the world may be sensed by your computer.

References

- Lancaster, Don, "TTL Cookbook," Howard W. Sams & Co., Inc., Indianapolis, Indiana. Copyright 1978.
- Mims III, Forrest M., "Engineer's Notebook, Integrated Circuit Applications," Radio Shack, USA. Copyright 1979.

Mike Dougherty currently works at Martin Marietta Aerospace in Denver, Colorado. His home-based system presently consists of an Atari 800 with 24K bytes of memory, the Atari 410 recorder, and the Atari 850 Interface Module for future communication with single board computers. Address correspondence to 7659 West Fremont Ave., Littleton, CO 80123.



```
Listing 4 (Continued)
 061B EA
             0385 COLLCT NOP
                                          LODP MUST BE 19 CYCLES
 061C EA
                          NOP
                                          TO MATCH A/D CONVERSION
             0390
                               PORT
 061D AD01D3 0395
                          LDA
                                          SAMPLE PORTE
 0620 91CB
                          STA
                               (ADDR),Y
                                          SAVE IN DATAS
             0400
 0622 C8
             0405
                          INY
                                          NEXT MEMORY LOCATION
 0623 D0F6
             0410
                          BNE
                               COLLCT
                                          FOR 256 BYTES
 0625 E6CC
                                          POINT TO NEXT MEM PAGE
                          INC
             0415
                               ADDR+1
 0627 CA
             0420
                          DEX
                                          ALL FOUR BLOCKS ?
 0628 D0F1
                               COLLCT
                                          NO, SAMPLE NEXT BLOCK
             0425
                          BNE
             0430
             0435
                     RESTORE ATARI VELANK AND DMA
             0440
 062A A960
             0445
                          LDA
                                          RESET BITS OF NMIEN
                               #$60
 062C BD0ED4
             0450
                          STA
                               NMIEN
                                          ALLOW VBLANK INTERRUPT
 062F A922
             0455
                          L.DA
                                          SET DMA SHADOW REGISTER
                               #$22
 0631 BD2F02
             0460
                          STA
                                          LEET VBLANK SET HARDWARE
                               DMASR
 0634 60
             0465
                          RTS
                                          BACK TO BASTO
 0635
              0470
                          +END
```

Listing 5: USR Function to Plot Trace Data 0105 LISTING # 5 0110 0115 ATARI USR FUNCTION TO PLOT A 0120 256 BYTE LOGIC GRAPH FROM BIT 0125 NUMBER "LINE" OF THE DATA. 0130 0135 0140 USAGE: 0145 X=USR(1600,data,one,zero,line) 0150 0155 data IS THE ADDRESS OF THE 256 0160 BYTES OF DATA TO BE PLOTTED 01.65 0170 IS THE LEFT HAND ADDRESS OF 0175 THE LOGIC HIGH LINE 0180 zero IS THE LEFT HAND ADDRESS OF 0185 THE LOGIC LOW LINE 0190 line IS THE MASK TO ISOLATE THE 0195 SPECIFIC BIT OF data TO BE 0200 PLOTTED 0205 0210 0215 THE FOLLOWING STEPS ARE PERFORMED: 0220 0225 1) INITIALIZE 0230 2) FOR EACH BYTE OF DATA: ISOLATE BIT "LINE" 0235 0240 IF BIT=0 THEN 0245 SET BIT IN ONE LINE 0250 ELSE 0255 SET BIT IN ZERO LINE 0260 ENDIF 0265 ROTATE THE MASK PATTERN RIGHT 0270 IF ALL 8 BITS THEN 0275 RESET THE MASK 0280 INCREMENT THE GRAPH INDEX 0285 ENDIF 0290 0295 NOTE: DUE TO THE WAY THE GRAPHICS 0300 SCREEN IS ADDRESSED, THE LABLES 0305 ONE AND ZERO SEEM BACKWARD. 0310 0315 0320 0325 0330 OOCB 0335 DATA \$00CB FOR DATA ADDR STORAGE FOR LOGIC HIGH ADDR STORAGE 0.000 0340 ONE \$00CD 00CF 0345 ZERO \$00CF FOR LOGIC LOW ADDR STORAGE 0001 0350 LINE \$00D1 BIT MASK 0355 DINDEX = 06FF \$06FF DATA INDEX 06FE 0360 GINDEX = \$06FE GRAPH INDEX 04FD 0365 MASK \$06FD TO SET BIT IN GRAPH 0370 0375 0000 0380 \$0640 FREE ATARI RAM 0385 0390 INITIALIZE 0395 (Continued)

Listing	5 (Conti	nued)				
0640	68	0400	PLOT	PLA		# OF USR ARGUMENTS
0641	68	0405		PLA		GET DATA ADDRESS
0642	85CC	0410		STA	DATA+1	MSB
0644	68	0415		PLA		
0645	85CB	0420		STA	DATA	LS8
06 4 7	68	0425		PLA		GET ONE LINE ADDRESS
0648	85CE	0430		STA	ONE+1	MSB
06 4 A	88	0435		PLA		
	85CD	0440		STA	ONE	LSB
064D		0445		FI.A		GET ZERO LINE ADDRESS
	85D0	0450		STA	ZERO+1	MSB
0650		0455		P'LA		
	85CF	0460		STA	ZERO	LSB
0.653		0465		PLA		GET PORTE LINE MASK
0654		0470		PLA		ONLY 8 BIT VALUE
	85D1 A900	0475 0480		STA LDA	LINE	LSB
	8DFF06			STA	#\$00 DINDEX	THE DATA THREY
	8DFE06			STA	GINDEX	INIT DATA INDEX INIT GRAPH INDEX
	A901	0495		LDA	#\$01	INII GRAFH INDEX
	8DFD06			STA	MASK	GRAPHICS BIT POSITION
000,2	00. 000	0505	:	O'IH	THOIC	GRAFAICS DIT FUSITION
				EACH	BYTE OF TH	HE 256 BYTE
			; BLOCK			200 2112
		0520				
	ACFF06		PLOTLP	LDY	DINDEX	DATA INDEX
	B1CB	0530		LDA	(DATA),Y	GET BYTE OF DATA
	25D1	0535		AND	LINE	ISOLATE TO SINGLE BIT
0.99E	DOOC	0540		ENE	NE 0	PLOT <> 0 VLAUE
0//0	ACEE 0.	0545	;	. 57	OT VIDEV	
	ACFE06 ADFD06			LDY	GINDEX	GRAPH INDEX
	11CD	0560		LDA ORA	MASK (ONE),Y	CURRENT BIT POSITION SET BIT
	91CD	0565		STA	(ONE),Y	REPLACE IN GRAPH
	DOOA	0570		BNE	NEXT	BRANCH ALWAYS
00,,	50011	0575	:	Little	REXI	EKHKEN HERHIS
0679	ACFE06			LDY	GINDEX	GRAPH INDEX
	ADFD06			LDA	MASK	CURRENT BIT POSITION
067F	11CF	0590		ORA	(ZERO),Y	SET BIT
0681	91CF	0595		STA	(ZERO),Y	REPLACE IN GRAPH
		0600	;			
0683			NEXT	CLC		ROTATE MASK THRU CARRY
	6EFD06			ROR	MASK	NEXT BIT IN GRAPH
	D004	0615		BINE	SKIP	
	6EFD06			ROR	MASK	ROTATE INTO BIT7
0880		0625	CLATE	INY	OTUBEY	PLOT TO NEXT GRAPH BYTE
	BCFE06		2KTP	STY	GINDEX	RESTORE GRAPH INDEX
	DOCF	0640		INC BNE	DINDEX PLOTLP	NEXT BYTE OF DATA
0695		0645		RTS	FLUILF	FOR ALL 256 BYTES RETURN TO BASIC
0696	- 0	0650		• END		WELCKE IN PHOTO
/-		3000				

Listing 6: BASIC Program TRACE

```
1 REM ----- T R A C E -----
2 REM -
3 REM - Low Resolution Logic Analyser
4 REM - and 555 Timer A/D Converter
5 REM -
6 REM -
            by Mike Dougherty
7 REM -
8 REM -----
9 REM
1110 DIM BYTE$(2), DATA$(1024)
1115 TRAP 2000:REM WHEN ALL USR FUNCTIONS HAVE BEEN POKED 1120 READ ADDR:REM STARTING ADDRESS OF CODE
1130 FOR CODE=0 TO 1 STEP 0
1140 READ BYTES: REM BYTE OF CODE
1150 IF BYTE$="*x" THEN SOUND 0,0,0,0;GOTO 1120;REM GET NEXT
     USR FUNCTION
1160 GOSUB 1400:REM CONVERT BYTE$->BYTE
1170 POKE ADDR, BYTE: REM PLACE IN MEMORY
1175 ADDR=ADDR+1
1180 NEXT CODE
1400 REM
1401 REM ---CONVERT BYTE$ TO BYTE
1402 REM
1410 BYTE=0
1420 V=ASC(BYTE$(1)):GOSUB 1500
1430 V=ASC(BYTE$(2)):GOSUB 1500
1440 RETURN
1500 REM
                                                        (Continued)
```

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Listing 6 (Continued) 1510 IF V<58 THEN BYTE=BYTE*16+V-48 1528 IF V>57 THEN BYTE=BYTE*16+V-55 1530 SOUND 0,8YTE,10,8 1540 RETURN 1600 REM 1601 REM ---SAMPLE THE PORT EVERY 19 1602 REM ---CYCLES AND STORE INTO 1603 REM ---THE BYTE ARRAY, DATA\$. 1604 REM 1605 DATA 1536 1610 DATA 68,68,85,CC,68,85,CB 1615 DATA A0,00,A2,04,A9,00,8D,2F,02 1617 DATA A5,14,C5,14,F0,FC 1625 DATA A9,20,80,0E,D4 1630 DATA EA,EA,AD,01,D3,91,CB 1635 DATA C8, D0, F6, E6, CC, CA, D0, F1 1640 DATA A9,60,8D,0E,D4,A9,22 1645 DATA 8D,2F,02,60 1695 DATA ** 1700 REM 1701 REM --- PLOT A ROW OF DATAS ON 1702 REM --- THE SCREEN AS DEFINED 1703 REM --- BY THE ARGUMENTS. 1704 REM 1705 DATA 1600 1710 DATA 68,68,85,CC,68,85,CB 1715 DATA 68,85,CE,68,85,CD 1720 DATA 68,85,00,68,85,CF 1721 DATA 68,68,85,D1 1722 DATA A9,00,8D,FF,06,8D,FE,06 1725 DATA A9,01,8D,FD,06 1730 DATA AC, FF, 06, B1, CB, 25, D1 1735 DATA D0,0C,AC,FE,06,AD,FD,06 1740 DATA 11,CD,91,CD,D0,0A 1745 DATA AC, FE, 06, AD, FD, 06 1747 DATA 11,CF,91,CF 1750 DATA 18,6E,FD,06,D0,04 1752 DATA 6E,FD,06,C8 1755 DATA 8C,FE,06 1757 DATA EE, FF, 06, D0, CF, 60 1795 DATA ** 1800 REM 1801 REM --- SAMPLE A BIT DEFINED BY 1802 REM ---THE ARGUMENT MASK AND 1803 REM --- MEASURE THE TIME THE 1804 REM --- PULSE IS HIGH IN 19 CYCLE 1805 REM ---TIME UNITS. 1806 REM 1810 DATA 1696 1812 DATA 68,68,68,85,D1 1815 DATA A9,00,80,2F,02,AA,A8 1820 DATA A5,14,C5,14,F0,FC 1825 DATA A9,20,8D,0E,D4 1830 DATA AD, 01, D3, 25, D1, D0, F9 1835 DATA AD,01,03,25,01,F0,F9 1840 DATA E8, D0, 0C, C8, A5, FF, AD, 01, D3 1845 DATA 25,D1,D0,F3,F0,09 1850 DATA EA, EA, AD, 01, D3, 25, D1, D0, E8 1855 DATA 86,04,84,05,A9,60 1860 DATA 8D,0E,D4,A9,22,8D,2F,02,60 1895 DATA ** 2000 REM 2001 REM -REPEAT FOREVER: 2002 REM - INPUT THE LINE MASK TO 2003 REM ~ CONTROL THE LINE SAMPLED; 2004 REM -SET UP SCREEN FOR TRACE; DO UNTIL "ESCAPE" KEY: IF KEY = "A": 2005 REM -2006 REM -2007 REM -PERFORM A/D ON LINE 2008 REM -DEFINED BY LINEMASK; TURN OFF KEY; ELSE IF KEY = "C"; 2009 REM -2010 REM -2011 REM -PERFORM A/D ON LINE 2012 REM -DEFINED BY LINEMASK; 2013 REM -ELSE IF KEY = "T": 2014 REM -TURN OFF KEY; 2015 REM -DO A TRACE OF PORT; PLOT BIT DEFINED BY 2016 REM -2017 REM -LINEMASK: ENDIF; 2018 REM -2019 REM - ENDDO; 2020 REM -END REPEAT.

(Continued)

2021 REM 2022 RFM

2110 GRAPHICS 0

```
Listing 6 (Continued)
2120 POSITION 10,10:PRINT "Line mask ";
2130 INFUT LINEMASK
2140 REM
2150 REM
2210 GRAPHICS 8: POKE 752,1
2220 SETCOLOR 1,6,14
2230 SETCOLOR 2,6,4
2240 COLOR 1
2250 REM
2260 REM
2410 FOR LOOP=0 TO 1 STEP 0
2415 FRINT " T - trace
                          A - A/D "
2420 IF FEEK(764)=63 THEN PRINT " T - trace
                                                            ";"A/D: ";
USR(1696,LINE MASK): POKE 764,255
2421 IF PEEK(764)=28 THEN POKE 764,255:GOTO 2000
2422 IF FEEK(764)=18 THEN PRINT " T - trace
                                                A - A/D
                                                            ";"A/D: ";
ÚSR (1696, LINE MASK)
2423 IF PEEK(764)<>45 THEN 2420
2424 POKE 764,255
2430 X=USR(1536,ADR(DATA$)):REM TAKE TRACE
2500 REM
2501 REM ---PLOT TRACE IN 4 SEGMENTS,
2502 REM ---256 VALUES IN EACH LINE
2503 REM --- POKE DIRECTLY INTO MEMORY
2504 REM ---WITH THE USR FUNCTION.
2505 REM ---THE ADDRESSES USED ARE
2506 REM ---COMPUTED IN BASIC
2507 REM ---PASSED AS USR ARGUMENTS.
2508 REM
2510 GRAPHICS 8: POKE 752,1
2520 SETCOLOR 1,6,14
2530 SETCOLOR 2,6,4
2540 COLOR 1
2610 SCREEN=PEEK(106)*256-7373:REM LEFT ADDRESS OF TOP LINE
2620 FOR LINE=0 TO 3:REM EACH 256 BYTE LINE
2630 X=USR(1600,ADR(DATA$)+LINE*256,SCREEN+LINE*1280+640,SCREEN+LINE
*1280, LINEMASK)
2640 NEXT LINE
2710 FOR LINE=0 TO 3:REM ADD LOGIC LEVEL MARKS ON LEFT
2720 FLOT 20,12+LINE*32
2730 DRAWTO 20,12+LINE*32+16
2740 NEXT LINE
                                                                MICRO"
2810 NEXT LOOP
```

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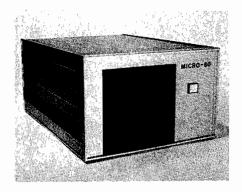
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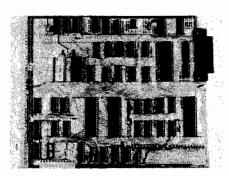
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Delayed Reset and Autoboot for the OSI

by Ugo V. Re

The following two circuit modifications to an OSI computer will enhance the capabilities of your system by adding a time delay to the action of the BREAK key (System RESET) and by providing an automatic boot-up of a disk system on power-up, or after a power failure.

How often, while typing, have you hit the BREAK key when you wanted to hit the RETURN key? I've made this mistake many times, and with a disk system it becomes very annoying to reenter BASIC without booting-up the disk.

One way to correct the problem is to relocate the BREAK key away from the keyboard. Another method is to add a time-delayed action to the BREAK (RESET) key similar to the method used by the C1P series 2 computer. I chose to add a time delay to RESET as this circuit would provide an additional benefit that then can be expanded to provide an automatic boot-up on power-up.

Description of Delayed RESET Circuit

Figure 1a is the Delayed RESET Circuit Diagram. This circuit can be added to any OSI system including the C8P with the RESET key mounted in the computer case.

A resistor placed in series with a capacitor will control the time it takes for a capacitor to fully charge or discharge. Because the capacitor's charge changes slowly it cannot be used to directly control the RESET pin on the 6502. The 6502 requires a rapid change from low to high to RESET the chip. The signal from the capacitor can be used, however, after it has been con-

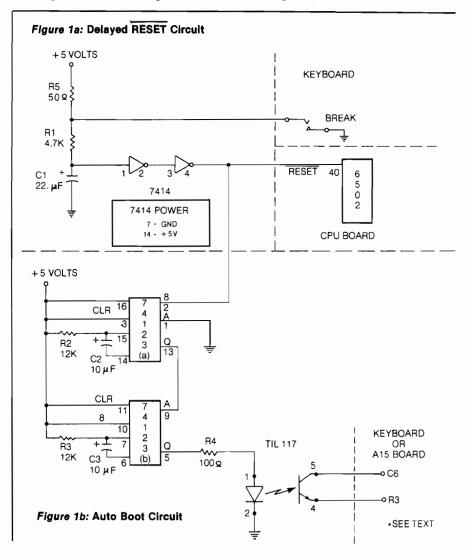
ditioned to change quickly from a low to high state.

The 7414 IC is a Hex Schmitt Trigger that will produce a rapidly changing output signal from a slowly changing (charging capacitor) input signal. The output goes negative when the input exceeds 1.7 volts, and goes positive when the input drops below 0.9 volts. Two of the six gates are wired in series to invert the signal so that a low (BREAK key depressed) is applied to the RESET pin of the 6502 chip.

Construction of Delayed RESET Circuit

The Delayed RESET circuit can be built on a wire-wrap IC socket as follows:

- 1. Wire pin 2 to pin 3.
- 2. Wire pins 5, 9, 11, and 13 to pin 14.
- Solder the 220 µF capacitor between pin 1 and pin 7 (positive side to pin 1).



- 4. Solder a 4.7K resistor to pin 1.
- 5. Solder a 50 Q resistor to pin 14.
- 6. Wire pin 14 to 5V and pin 7 to ground.
- Cut the red lead (trace on the 600 board) that goes from the BREAK key, on the 542 board to pin 40, on the 6502 chip.
- Solder the loose ends of the 50 Q and 4.7K resistors together and wire to the red lead/trace from the BREAK key.
- Wire pin 4, 7414 IC to the red lead/ trace going to pin 40, of the 6502 chip.
- Store the IC socket so that it doesn't short out to any other components.

The combination of a 4.7K resistor and a 220 μ F capacitor will give a time delay of about two to three seconds. Additional delay can be obtained by increasing the size of the capacitor. Do not increase the size of the resistors as the values shown are the maximum that can be used for reliable operation. With the circuit installed, the BREAK key must be depressed for two to three seconds before the screen will clear.

With the Delayed RESET installed, the computer comes up in RESET at power-up. With a few more components we can use the signal from this circuit to provide an automatic boot-up at power-up.

Description of Auto-Boot Circuit

Figure 1b is the Auto-Boot Circuit Diagram. This circuit provides an additional time delay after RESET goes high before shorting the contacts of the "D" key to boot-up the disk system.

In addition to supplying a signal to the 6502 microprocessor chip, the output of the 7414 IC (Schmitt Trigger) is

Figure 2: Printed Circuit Board

74123

R5
1 R1
R1
RESET

R3
C3
1 7414

R3
C6

used to trigger one half of a 74123 IC [Monostable Multivibrator]. Its output is then used to trigger the second half of the 74123 in order to produce still another pulse to turn on the TIL117 (Opto-Isolator).

The 74123 can be triggered in two ways. If input A is held low, bringing input B from a low to high state will trigger the 74123. If input B is held high, bringing input A from a high to low state will trigger the 74123. As a result of a trigger, the Q output goes high and the Q output goes low, staying there for a time determined by the resistor and capacitor combination, before returning to their initial states.

At power-up it takes one to two seconds for the capacitor (C1) to charge to approximately two volts before the output of the 7414 will change from a low to high state. This signal is sent to

input B1 (first half of the 74123, inpu Al grounded), to trigger the 74123. The Q1 output goes high for 100-200 msec before returning to a low state. The Q output is connected to input A2 (sec ond half of the 74123, input B2 is wired to 5 volts), therefore, when it goes low it triggers the 74123. The Q2 output goes high and turns on the light emitting diode (LED) in TIL117. This causes the photo transistor to switch on and short the contacts of the "D" key. After 100-200 msec, Q2 output returns to the low state and the photo transistor returns to an open circuit, with the "D" key no longer shorted. At this time the computer has booted-up BEXEC+.

Construction of Circuit Board

The Delayed RESET and Auto-Boot circuits should be built on a circuit board and mounted at the keyboard. In

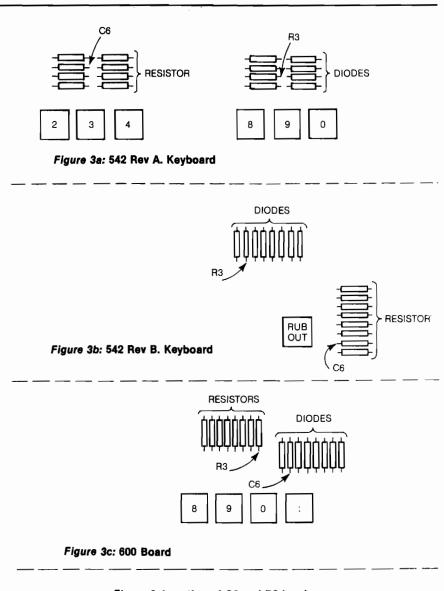
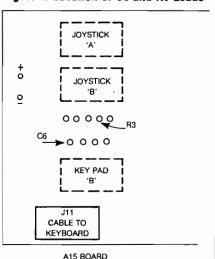


Figure 3: Location of C6 and R3 Leads

a C8P system it could be placed in the CPU case near the A15 circuit board.

You can use point-to-point wiring with IC sockets mounted on a perforated board, or you can make a printed circuit board (see figure 2 for the layout). Check for the correct orientation of the electrolytic capacitors and integrated circuits before soldering any components to the board.

Figure 4: Location of C6 and R3 Leads



Once the circuit board is completed it must be wired to five volts, ground, the BREAK key, and the contacts of the "D" key. This can be accomplished in one of three ways.

The best method is to wire all leads directly to the contacts on the keyboard. This method is the same for all keyboards. With the keyboard face down, the "D" key contact pads are the fourth group from the right-hand edge and the second group from the bottom edge. The topmost pad (the C6 lead) should have a potential of 4-5 volts and is wired to TIL117, pin 5. The bottom pad is wired to TIL117, pin 4. The BREAK key contacts are also on the right-hand edge of the keyboard. A red and black pair of wires leave the keyboard and go to the CPU board. Power can be picked off at any IC — pin 7 or 8 for ground, and pin 14 or 16 for five volts.

Another method is to wire to a resistor, C6 lead, and diode, R3 lead, located on the keyboard. See figure 3 for the location of these components.

This last method can only be used if you have an A15 circuit board installed in your system. The C6 and R3 leads are extended from the keyboard to the A15 board via a 16-conductor cable and appear on pins 2 and 9 of the "A" keypad socket. See figure 4 for the location of socket and wiring points. Power is also available on the A15 board. The BREAK key leads, however, will have to be extended to this board and then routed to the CPU board.

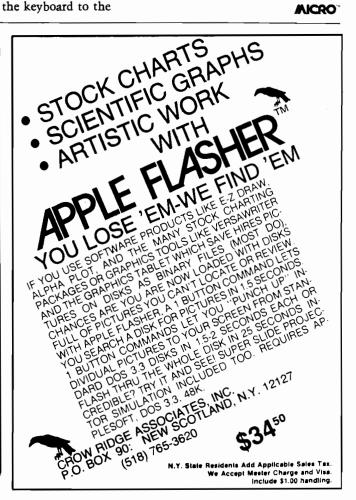
Now whenever you turn on the power, press the BREAK key, or if power returns after a power failure, your computer will boot-up and run BEXEC*. You can modify BEXEC* so that it will run a special program (home control, security, bulletin board, etc.), without any operator intervention. I have my system running continuously and have not yet had a problem.

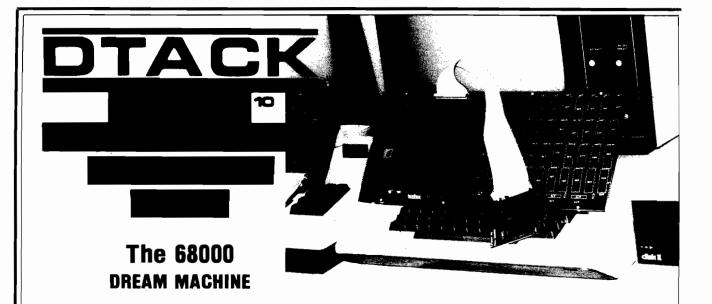
I would recommend that you do not run your system continuously without some form of motor control for the disks. See the article by N.E. Ingersoll in MICRO, September 1981, page 15. I have this circuit installed in my C4P MF system and highly recommend it to reduce power consumption and disk wear.

The author may be contacted at 167 Sprucewood Dr., Levittown, NY 11756.

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The 68000 and the Personal Computer

by Laurence Kepple

Your 6502 system is still a gateway to the future! This article discusses some of the hardware and software aspects of what promises to be an exciting market for the 68000 as an add-on to 6502-based systems.

Very Large Scale Integration (VLSI) technology has made it possible for Motorola to turn the CPU architecture of a minicomputer (Digital Equipment Corporation's PDP-111 into a microprocessor. The 68000 is so compact and so powerful that it threatens to erase the distinction between mini and microcomputers. The question now is: do you hold on to the minicomputer concept in the microprocessor world? Do you run two or more terminals on the 68000 as though it were still a PDP-11? Or do you adopt a "one person, one processor" philosophy? The Fortune 32:16 starts at about \$5000 and may be able to support as many as 16 terminals. The comparably priced Radio Shack Model 16 can support two terminals. The manufacturers of the SAGE II, which costs under \$4000, have deliberately designed their 68000 computer as a single-user system.

Whatever approach is adopted, the rate at which companies are announcing new 68000-based systems makes one thing certain: the 68000 microprocessor has become a chip worth knowing. Fortunately, you do not have to buy an entirely new system to be able to work with the 68000. As is the case with the 6809, add-on boards are becoming available that allow you to use your 6502-based system as a foundation for work with a more advanced processor.

For example, DTACK Grounded [1415 E. McFadden, St. F, Santa Ana, CA 92705] is now shipping peripheral boards for the Apple II and for the PET that allow users to join the 68000 with the 6502 as a co-processor. The cost of these boards ranges from \$595 (for an expansion board with 4K of static

RAM) to \$1123 (for the same board with 92K). (See PEELINGS II, April 1982, for a review of these products.) Company president Hal Hardenbergh, in his newsletter DTACK Grounded, has announced that the company will soon market less expensive boards with dynamic instead of static RAM.

The name "DTACK Grounded" explains the company's hardware approach to getting the 68000 into the personal computer market. Pin 10 of the 68000, "DTACK", stands for DaTa ACKnowledged. The 68000 needs this pin to function with the asynchronous data bus for which it was designed by Motorola. By grounding DTACK, the 68000 is made compatible with the synchronous data busses used in simpler personal computer systems.

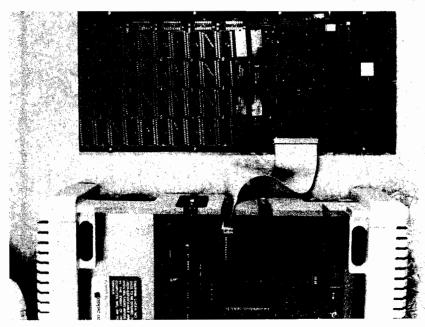
Using the 68000 as a co-processor with a 6502-based system can produce amazing improvements in execution speed. For example, a 3-D graphics demonstration included with the DTACK Grounded board runs in 30 minutes, 5 seconds in Applesoft; 10

minutes and 40 seconds in Applesoft with the 68000 handling floating point operations; and 45 seconds with the 68000 running a machine-code program and the 6502 used exclusively for I/O. A graphics version of the 68000 floating point package that uses a 16-bit mantissa produced the fastest time of all: 18.9 seconds.

There are other 68000 boards available. Motorola itself recently announced an MC68000 Educational Computer Board. As its name suggests, the purpose of this board is to make it economical (cost: \$495.00) for students and designers to get familiar with the 68000. The board includes 32K of RAM and 16K of ROM firmware that provides the user with debug/monitor functions, program entry, assembling, disassembling, and I/O functions. To simplify programming in the educational environment, a primitive assembler/disassembler function allows programs to be entered, displayed, and modified in assembly language. Especially in this latter characteristic. Motorola's board resembles those of

The DTACK Grounded 68000 board (with 12K of static RAM) attached to MICRO's Apple II. The large (!) rectangular shape on the far right of the board is the MC68000.

(Photo by Loren Wright)



DTACK Grounded. Another company, Intellimac (6001 Montrose Rd., Rockville, MD 20852) has announced a 68000 single-board kit, the 68 Magnum, for \$745.00. This board offers 128K of dynamic RAM, 16K bytes of firmware ROM/EPROM, and 16K bytes of user EPROM. The system's firmware offers features similar to those of Motorola's ECB. Also like the ECB, the Magnum requires the user to supply a power source and a CRT terminal.

We assume that other companies are going to follow DTACK Grounded's lead and design 68000 boards especially for specific personal computer systems. Micro Technology Unlimited (2806 Hillsborough St., P.O. Box 12106, Raleigh, NC 27605) has announced a 68000 expansion board for its 6502-based system, the MTU-130 microcomputer. Called the Datamover-256, the board provides a 68000 processor and an additional 256K of RAM (price: \$999.00). It seems that MTU is deliberately pursuing a 6502/ 68000 environment, in which users get the best of both worlds: compatibility with a widely supported 8-bit microprocessor, and upgradability to the power of the 68000. At MICRO, we are interested in 68000 peripheral boards aimed at 6502 machines. The significance of such expansion boards goes beyond simply improving the execution time of existing unprotected software. Bringing the 68000 out of the relatively exclusive minicomputer environment into the personal computing world should greatly stimulate the development of software for the 68000. Given the power of the 68000, this would be a significant event. If the 68000 ever achieves anything like the 6502's level of support, the results could be awesome.

Owners of 6502 systems who are considering entry into the 68000 world should be aware that, for some time, they will be strangers in a strange land. The production of software for the 68000 is only beginning. Phase Zero, Ltd. (2509 N. Campbell Ave., Suite 130, Tucson, Arizona 85719) has just started to market the first development software designed specifically for the 6502/68000 environment. Called "The Apple II to MC68000 Cross-Assembler," the program, written in 6502 machine code, accepts 68000 assembly commands and produces 68000 object code (cost: \$99.00). The program is intended to run on an Apple II equipped with a DTACK Grounded board. Clearly, i you start to develop 68000 software or your Apple any time soon, you will be one of the first. This is not the case fo those 6502 owners who opt for the 6809 on boards such as those offered by Stellation Two and ESD Labs. The 6809 has a substantial base of excellen software, and two powerful operating systems, OS-9 and FLEX.

However, one crucial event has already taken place that will influence the future of a substantial portion of 68000 development work: UNIX has won wide acceptance as the best operating system for the 68000. One important reason for the UNIX-68000 connection is that UNIX was written for the 68000's minicomputer ancestor. the PDP-11. Created at Bell Labs. UNIX has received hundreds of manyears of additional development at some of America's leading universities. If microcomputer users can gain access to UNIX through the 68000, they will have an operating system of tremendous power. UNIX offers a vast number of utilities and a means of linking them into large constructs via "pipes." makes designing large programs sim-

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pler, and encourages the fullest use of the modular approach in solving complex problems. In addition, UNIX is written in, and of course supports, the powerful programming language C.

But the attempt to make UNIX the standard operating system for the 68000 is not without difficulties. In the first place, UNIX itself has yet to be standardized. Because substantial development of UNIX was done at different universities, and because UNIX was designed to be easily modifiable, the operating system assumed a somewhat different form at each site. Variants include Revision 5, Revision 6, Revision 7, and a recently announced System III. Another problem is that, while UNIX provides rich resources in terms of software development tools, it does not offer much application software - again, a situation that reflects the university setting in which UNIX was formed. The absence of an application orientation resulted in some UNIX deficiencies, such as the lack of error recovery for disk I/O. Finally, the language in which UNIX is written, C, while very powerful, is not nearly as portable as COBOL, for instance.

Major efforts are now underway to solve these problems. Microsoft, the company that made BASIC famous, is attempting to achieve the same feat of standardization with UNIX. Microsoft's version of UNIX, called XENIX, supports high-level languages that are well established in the microcomputer market: BASIC, COBOL, FORTRAN, and Pascal. Charles River Data, developer of the \$20,000 Universe 68 (the kind of system that Motorola had in mind when it designed the 68000), has also created UNOS to go along with it. In addition to C, UNOS supports FORTRAN, BASIC, and Pascal.

Some software developers have preferred to create independent operating systems that maintain compatibility with UNIX. For example, the Mark Williams Company (1430 West Wrightwood, Chicago, Illinois 60614) has produced the COHERENT system, which it describes as "a totally independent development" that nonetheless is designed to preserve compatibility with Revision 7 of UNIX. Like UNIX, COHERENT was initially developed on the PDP-11. In addition to C, COHERENT supports a BASIC interpreter-compiler, XYBASIC. Mark Williams has just announced a version of COHERENT for the 8088-based IBM Personal Computer. The firm's president, Robert Swartz, noted that existing PC operating systems such as

MS-DOS and CP/M are "Model Ts" compared to the "Lear Jet" functionality of a UNIX-like system. A 68000 version of COHERENT is on the way.

Not every 68000 operating system will be UNIX-based or UNIX-compatible. Mindful of the relative lack of applications software that runs under UNIX, some manufacturers will try to use large libraries of applications programs to induce buyers to stay with their non-UNIX but processor-independent operating systems. For example, Microcobol Products, Inc. (MPSI), developer of the BOS/5 and MBOS/5 operating systems, plans to release a 68000 version in the fall of 1982. The BOS/5 operating system is already available on a wide range of processors (Z80, PDP-11, 8086) and supports a large base of business applications programs. Once the BOS/5 system is ported to the 68000, all of this applications software will run without any recompilation or relinking. Another business-oriented, non-UNIX operating system is OASIS 16, the 16-bit version of an 8-bit operating system that supports a substantial quantity of business software.

However, UNIX will almost certainly be the primary operating system used in the development of new applications software for 68000 microcomputers. Some powerful microprocessor development tools, in addition to those already part of UNIX, are being made available for the UNIX environment. The Boston Systems Office (469 Moody St., Waltham, MA 02154) recently announced a delivery date in late June for UNIX-compatible versions of the more than 50 high-speed microprocessor assemblers, symbolic debuggers, and linkers that they originally developed for the PDP-11. Given the close relationship between the 68000 and the PDP-11, this minicomputer is an ideal software development system for 68000 systems running UNIX. BSO's strategy is to support such a development effort on the PDP-11.

The low-cost availability of a chip with the power of the 68000 is good news for anyone who believes in the potential of microcomputers. Perhaps the best news of all is that we do not have to spend \$5,000 or more for access to the 68000. Your 6502 system is still a gateway to the future. In time, the development effort at the minicomputer level, in addition to the work done by microcomputer users themselves, will produce microcomputer performance and capacity that we can hardly imagine today.

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An MC68000 Overview

by Tom Whiteside and Joe Jelemensky

This article is the first in a twopart series by the authors describing the Motorola MC68000 microprocessor. This part describes the registers, instruction set and addressing modes.

The second part will give some simple programming examples to illustrate programming techniques and special features of the MC68000.

The Motorola MC68000 is rapidly becoming the most popular new-generation microprocessor used in high performance desktop or personal computers. Several manufacturers have announced systems using the MC68000, with costs ranging from \$5,000 to \$10,000, depending on the amount of memory and size and type of disk storage. These systems pack the power of a minicomputer into a personal computer-size package and price.

The main reason for this favorable performance/price ratio is the advanced micro-programmed architecture of the MC68000. Its 16-bit external data bus and 32-bit internal registers allow high throughput. A 23-bit external address bus, plus upper and lower data strobe, allows direct addressing of 16 megabytes of memory. The powerful instruction set was designed to allow easier implementation of structured high-level languages, such as Pascal and C. Operating systems are also supported by the basic architecture, with features such as:

- · supervisor and user states of operation
- privileged supervisor instructions

- separate supervisor stack pointer
- vectored, multilevel, priority interrupt handling
- TRAP vectors to handle a variety of error conditions and operating system calls

Data types include bit, byte, word, long word, and BCD digits. The machine has 17 32-bit registers. Sixteen of these are accessible in user state (8 address registers and 8 data registers). The remaining register is the supervisor stack pointer and is only accessible when the machine is in supervisor state.

The MC68000 was designed to take advantage of the latest technology in VLSI semiconductor manufacturing. Likewise, the recent advances in computer science have also influenced the design of the CPU and the instruction set of the MC68000. The LINK and UNLK instructions make it easy to allocate and deallocate space dynamically on the system stack. This is especially useful when setting up local variables in a procedure. Another instruction, CHK, aids the implementation of high-level languages. CHK allows the checking of array bounds with a single instruction by comparing the value of a register specified with zero and an upper limit.

The instruction set is quite regular, in that most instructions use the same addressing modes and can operate on most data types. All op codes are 16 bits in length. Additional information necessary for specific instructions and addressing modes may require one to four additional words to contain immediate data, displacement offsets, or absolute addresses.

The basic instruction types are: data movement operations; integer arithmetic; logical operations; shift operations; bit manipulation (bit addressable test, set, clear); BCD arithmetic; and program control and system control operations. The complete instruction set is shown in tables 1 and 2.

The addressing modes of the MC68000 are of six basic types: register direct, register indirect, absolute, immediate, program counter relative, and implied. Add to these basic types the ability to do indexing, post-incrementing, and pre-decrementing, and you have 14 specific addressing modes [as shown in table 3].

Registers

The 17 data and address registers free the programmer from the overhead of a complicated register management scheme in the majority of applications. Eight of the registers (D0 through D7) are data registers and may be used for byte, word, and long word operations. The remaining 8 (A0 through A7) are user-accessible address registers. The address registers and the supervisor system stack pointer may be used for word and long word address operations. All of the 17 registers may be used as index registers. All allow post-incrementing and pre-decrementing, giving the programmer the ability to use any of them as a user stack pointer.

The other registers on the MC68000 are a 16-bit status register and a 32-bit program counter. The registers are summarized in the programming model in table 4.

Data Registers

Each of the eight data registers (D0-D7) is 32 bits wide. Data registers are further subdivided into word (16 bits) and byte (8 bits) lengths for data manipulation. Long word operands, of course, occupy the entire register. Word operands occupy the low order 16

bits, and bytes occupy the low order 8 bits. When data registers are used as either a source or destination operand in an instruction, only the appropriate

Table 1: Instruction SET

Mnemonic	Description
ABCD	Add Decimal with Extend
ADD	Add
AND	Logical And
ASL	Arithmetic Shift Left
ASR	Arithmetic Shift Right
Bcc	Branch Conditional
BCHG	Bit Test and Change
BCLR	Bit Test and Clear
BRA BSET	Branch Always Bit Test and Set
BSR	Branch to Subroutine
BTST	Bit Test
СНК	Check Register Against
CLD	Bounds
CLR	Clear Operand
СМР	Compare
DBcc	Test Condition, Decrement and Branch
DIVS	and Branch Signed Divide
DIVU	Unsigned Divide
EOR	Exclusive OR
EXG	Exchange Registers
EXT	Sign Extend
JMP	Jump
JSR	Jump to Subroutine
LEA	Load Effective Address
LINK	Link Stack
LSL	Logical Shift Left
LSR	Logical Shift Right
MOVE	Move
MOVEM	Move Multiple Registers
MOVEP	Move Peripheral Data
MULS MULU	Signed Multiply Unsigned Multiply
NBCD	Negate Decimal with
	Extend
NEG	Negate
NOP	No Operation
NO	Ones Complement
OR	Logical OR
PEA	Push Effective Address
RESET	Reset External Devices
ROL	Rotate Left without Extend
ROR	Rotate Right without Extend
ROXL	Rotate Left with Extend
ROXR	Rotate Right with Extend
RTE	Return from Exception
RTR	Return and Restore
RTS	Return from Subroutine
SBCD	Subtract Decimal with Extend
Scc	Set Conditional
STOP	Stop
SUB	Subtract
SWAP	Swap Data Register Halves
TAS	Test and Set Operand
	Trap
TRAP	
TRAP TRAPV TST	Trap on Overflow Test

portion of the register is used or changed; the remaining portion is left undisturbed.

Address Registers

Each address register and the stack pointers are also 32 bits wide. The address registers may also be used for word operands, but, unlike the data registers, may not be used for byte operands. Word operands are contained in the low order 16 bits of an address register. When an address register is used as a source operand, either the entire register or the low order 16 bits will be used, depending on whether it is a long word or word operation. If an address register is specified as a destination operand, the entire register is affected. Long word operations will of course write the 32-bit value into the entire 32-bit register. Word operations will cause sign extension on any other operands before the operation takes place.

The system stack pointers (A7 in user and supervisor state) are used by the machine for interrupt, subroutine call, and trap processing. The two stack pointers are both accessed by using A7 in the operand field of an instruction. The supervisor stack will be accessed if the MC68000 is in supervisor state and the user stack pointer will be used if it is not in supervisor state. The user may also use the system stack to store temporary data by pushing and pulling data via the MOVE instruction, or by using the LINK and UNLK instructions described later.

Program Counter

The program counter of the MC68000 is a full 32 bits in length, even though the range of the address space is only 24 bits (23 bits plus upper and lower data strobe) on the current version. Future versions will have 32 bits of address space. The program counter is manipulated by program countrol-type instructions (branches, subroutine calls, etc.). The program counter is also used as the base address in program counter relative address calculations.

Status Register

The status register on the MC68000 is 16 bits long. It is broken up into an 8-bit system status byte and an 8-bit condition code byte. The system status

byte contains an eight-level interrupt mask and additional processor-related status bits. These status bits indicate whether or not the MC68000 is in trace state and/or supervisor state. The condition code byte contains bits for overflow (V), zero result (Z), negative result (N), carry (C), and extend (X). The status word has six unimplemented bits reserved for future extensions to the MC68000 family.

Instruction Set Description

In this section we will discuss the MC68000 instruction set in moderate detail. There are several variations of some instructions, which make them shorter (and/or faster), allow easy manipulation of pointers, and more flexible immediate addressing. The programmer may explicitly choose these variations or allow the Motorola Resident Structured Assembler to select the appropriate form of the instruction. These variations include "quick", "immediate", and "address" instruction types, designated by "Q", "I", and "A" suffixes to the instruction mnemonic.

Data Movement Operations

Unlike the MC6800/6809 architectures, the MC68000 does not have specific load and store register operations containing explicit register reference. Instead, a more powerful MOVE instruction is provided to perform data transfers. The source and destination of data transfer operations are specified independently of the instruction. This allows movement of data between registers, from register to memory, from memory to register, and from memory to memory. Data MOVE instructions allow byte, word, and long word operands. Address MOVE instructions allow only word and long word operands. This insures that only legal address manipulations can be executed.

The MC68000 also includes several special MOVE instructions. The MOVE Multiple register instruction (MOVEM) allows several registers to be moved to or from memory with one instruction. The MOVE Peripheral data instruction (MOVEP) provides a means to transfer word and long word data to or from peripheral data registers, which are located at alternate addresses in memory space. The EXchanGe registers instruction (EXG) allows the swapping of contents between any pair of

the accessible registers. Load Effective Address (LEA) and Push Effective Address (PEA) provide means for manipulating address pointers in position-independent programs by loading the effective address of a variable into an address register or pushing it onto a stack. LINK stack (LINK) and UNLinK stack (UNLK) instructions allow the user to easily reserve and return temporary variable space on a stack. The MOVE Quick instruction (MOVEQ) makes it easy to transfer sign-extended 8-bit immediate data to a register with a very fast one-word instruction.

Integer Arithmetic Operations

The four basic arithmetic functions of add, subtract, integer multiply, and integer divide are supported by the MC68000, along with other useful operations such as compare operand, negate operand, test operand, and clear operand.

The ADD and SUB operations are available for both address and data operations. Data operations allow all sizes of operands. Address operations are restricted to legal address size operands (long word and word). The arithmetic CoMPare instruction (CMP) can be used on data, address, and memory compare operations. The NEGate (NEG) and CLeaR (CLR) instruction can be used on all sizes of data operations (byte, word, and long word). The multiply instruction is used to multiply two word operands to form a long word result. MULU is used for unsigned multiplication and MULS for signed multiplication.

In division, a long word dividend is divided by a word divisor to produce a word quotient with a word remainder. Again, instructions are provided for signed and unsigned operations (DIVS and DIVU). A set of extended arithmetic instructions is also available for programming multiprecision and mixed size arithmetic functions. These include ADDX, SUBX, NEGX, and EXT [sign extend]. The test instruction (TST] is used to set the condition codes as a result comparing the operand to the value zero.

Another powerful instruction in this category is the Test And Set instruction (TAS). TAS sets the N and Z condition codes based on a compare of the byte operand specified in the instruction with zero. The most significant bit

(sign bit) of the byte is then set before the instruction is completed. This is very useful for manipulation of semaphores between processes, since the test and set operation is uninterruptable.

Immediate data of byte, word, and long word length may be added to or subtracted from the data stored at the effective address using the add and subtract immediate instructions (ADDI and SUBI).

A variation of the add and subtract immediate instructions are "quick" add and subtract (ADDQ and SUBQ). These instructions allow the addition or subtraction of immediate data (in the range one to eight) to byte, word, or long word operands. The advantage of these instructions are speed and compactness. They are particularly useful in loops where loop counts must be altered by some small value other than one, but less than nine each time

through the loop.

Binary Coded Decimal (BCD) arithmetic operations are also supported by the MC68000. The following multiprocision BCD arithmetic functions may be performed: add decimal with exten (ABCD), subtract decimal with exten (SBCD), and negate decimal with extend (NBCD). The BCD arithmetic operations are restricted to byte-lengt operands and can be register-to-registed or memory-to-memory operations.

Logical Operations

The MC68000 provides a full set c logical operation instructions. Logica and (AND), inclusive or (OR), exclusive or (EOR), and logical complement (NOT) are available for all sizes c integer data operands. In addition, a se of immediate instructions, ANDI, ORI and EORI provide logical operations of all operand sizes.

Table 2: Instruction Variations

Instruction Type	Variation	Description
ADD	ADD ADDA ADDQ ADDI ADDX	Add Add Address Add Quick Add Immediate Add with Extend
AND	AND ANDI ANDI to CCR ANDI to SR	Logical AND AND Immediate AND Immediate to Condition Code AND Immediate to Status Register
СМР	CMP CMPA CMPM CMPI	Compare Compare Address Compare Memory Compare Immediate
EOR	EOR EORI EORI to CCR EORI to SR	Exclusive OR Exclusive OR Immediate Exclusive Immediate to Condition Codes Exclusive OR Immediate to Status Register
MOVE	MOVE MOVEA MOVEQ MOVE to CCR MOVE to SR MOVE from SR MOVE to USP	Move Move Address Move Quick Move to Condition Codes Move to Status Register Move from Status Register Move to User Stack Pointer
NEG	NEG NEGX	Negate Negate with Extend
OR	OR ORI ORI to CCR ORI to SR	Logical OR OR Immediate OR Immediate to Condition Codes OR Immediate to Status Register
SUB	SUB SUBA SUBI SUBQ SUBX	Subtract Subtract Address Subtract Immediate Subtract Quick Subtract with Extend

Shift and Rotate Operations

Bidirectional arithmetic and logical shift operations are performed in the MC68000 by executing the ASR, ASL, LSR, and LSL instructions. Rotate operations may be performed with the ROR. ROL, ROXR, and ROXL instructions.

Arithmetic shift operations shift the operand in the direction specified. The last bit shifted out of the operand will be contained in the carry (C) bit and the extend (X) bit of the Condition Code Register (CCR). When the operand is shifted right, the value of the sign bit of the operand (Most Significant Bit) will be preserved., During left shift operations, the Least Significant Bit of the operand will be replaced by zero (0). The overflow (V) bit will indicate if the sign of the operand has changed during the left shift operation.

Logical shift operations shift the operand either right or left as specified by the instruction. The last bit shifted out of the operand in either direction will be contained in the C and X condi-

tion code bits. Zeros are shifted into the MSB during right shifts, and into the LSB during left shift operations.

Rotate operations (ROR and ROL) rotate only the bits in the operand and do not include the contents of the extend (X) bit. The operand is rotated in the direction specified by the instruction, and the last bit rotated will also be contained in the carry (C) bit. The contents of the X bit will never be rotated into the operand.

Extended rotate operations are provided by the rotate with extend instructions (ROXR and ROXL), which include the extend (X) bit in the operation. During these operations, the previous contents of the X bit will be shifted into the MSB or LSB, depending on the direction of the rotate operation. Multiple precision shifts and rotates can be performed using a combination of shift and extended rotate operations.

All shift and rotate operations may be performed on memory or registers. When a register is specified as the effective address of the operand, all operand sizes are allowed. A shift count of one to eight bits may be specified in the instruction, or a count of zero to 63 bits may be specified in a data register.

Shifts and rotates on memory operands can only be single bit shifts and are restricted to word length operands.

Bit Manipulation Operations

Several powerful bit manipulation instructions have been implemented in the MC68000. Individual bits may be tested (BTST), tested and set (BSET), tested and cleared (BCLR), or tested and toggled (BCHG). The BTST instruction causes the state of the specified bit in the operand to be reflected by the zero (Z) bit in the condition code register. The BSET and BCLR instructions allow the state of the specified bit to be reflected by the Z bit, and in addition, set or clear the specified bit after the test. The BCHG instruction causes the state of the bit to be reflected in the Z bit, and also changes the state of the specified bit in the operand to the opposite state after the test.

Bit manipulation operations may be performed on register or memory operands. If the effective address is specified as a register, the bit number specification is modulo 32, allowing all bits contained in the register to specified. Memory operands are restricted to byte length and the bit number specification is modulo 8. The bit number is specified as part of the instruction, or in a data register specified in the instruction.

Program Control Operations

Program execution in the MC68000 is controlled by the Program Counter (PC). The PC is updated in the normal processing of an instruction to point to the next instruction to be executed. The PC will either point to the next sequential instruction in memory, or to another instruction sequence elsewhere in memory as a result of a PC modifying instruction.

The program counter is modified by the execution of an instruction in one of two ways. In the first way, the PC is modified by adding a displacement, specified in the instruction, to the current PC to get the address of the next instruction to be executed. The displacement (word or byte) specified in relative branch instructions is a two's complement integer value, which counts the relative distance in bytes.

Table 3: Addressing Modes

Mode	Generation
Register Direct Addressing	
Data Register Direct	EA = Dn
Address Register Direct	EA = An
Absolute Data Addressing	
Absolute Short	EA = (Next Word)
Absolute Long	EA = (Next Two Words)
Program Counter Relative Addressing	
Relative with Offset	$EA = (PC) + d_{16}$
Relative with Index and Offset	$EA = (PC) + (Xn) + d_8$
Register Indirect Addressing	
Register Indirect	EA = (An)
Postincrement Register Indirect	$EA = (An), An \leftarrow An + N$
Predecrement Register Indirect	$An \leftarrow An - N, EA = (An)$
Register Indirect with Offset	$EA = (An) + d_{16}$
Indexed Register Indirect with Offset	$EA = (An) + (Xn) + d_8$
Immediate Data Addressing	
Immediate	DATA = Next Word(s)
Quick Immediate	Inherent Data
Implied Addressing	
Implied Register	EA = SR, USP, SP, PC

Notes:

EA = Effective Address

 Address Register Αn

Dn Data Register

= Address or Data Register used as Index Register Xn

= Status Register

= Program Counter PC

= Contents of

= 8-bit Offset (displacement)

= 16-bit Offset (displacement)

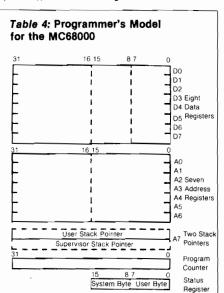
= 1 for Byte, 2 for Words and 4 for Long Words

= Replaces

The value of the PC is the address of the current instruction, plus two. The displacement is calculated by the assembler when the program is assembled, or by the linker when building a load module. By adding displacements to the current PC, the program will be position independent, since all PC changes are relative and control is always resumed at the same location in the program instead of an absolute memory address. This allows programs to execute anywhere in memory instead of from a specific area.

In the second way, the PC may be modified by instructions that load an effective address value into the PC when the instruction is executed. In this case, the value of the PC will be overwritten by the new address specified in the instruction. When these instructions are executed, control will be transferred to the memory address specified regardless of where the program is loaded.

Program control operations are accomplished with a collection of branch instructions, subroutine branches, and return instructions. Branch instructions can be conditional or unconditional. The conditional branches allow testing various condition bits in the condition code register individually or in Boolean combination. If the condition specified in the branch instruction is met, the branch will be taken. If not, the next sequential instruction will be executed. The conditional branch instruction (Bcc) tests the condition specified in the instruction. Then it either branches or falls through to the next instruction, based on whether the condition is met or not. The test condition, decrement and branch instruction (DBcc), tests the specified condition



and falls through if it is true. If the condition is not true, the contents of a specified register is decremented and the result compared to -1. If the value is not -1, the branch will be taken. Otherwise control will resume with the instruction following the DBcc. All conditional branches are relative to the current PC. Table 6 lists the conditions that can be tested by the conditional branch instruction.

Unconditional branch instructions always force the destination address of the branch to be loaded into the program counter. These branches may be relative to the current PC or absolute addresses. The BRAnch unconditional instruction (BRA) will always cause the PC to be replaced by the value of the current PC, plus the relative displacement specified in the instruction. The JuMP instruction (JMP) causes the PC to be loaded with the effective address specified in the JuMP instruction.

There are also two subroutine call instructions. These instructions first save the value of the current PC (address of next sequential instruction to be executed on the system stack (pointed to by address register A7), and then cause the PC to be loaded with the address of the subroutine to be executed. The Branch to SubRoutine (BSR) instruction causes the relative offset specified in the instruction to be added to the PC. The Jump to Sub-Routine instruction (JSR) loads the PC with the effective address of the subroutine specified by the effective address specified by the JSR instruction.

There are two instructions that return program control to the address stored on the system stack. ReTurn from Subroutine (RTS) pulls the new program counter value from the stack and program execution resumes at that address. The ReTurn and Restore instruction (RTR) additionally restores the condition code register from the stack when returning from interrupt processing.

Bcc and BSR can have displacement values of byte and word lengths. DBcc always has a word length displacement value. The effective address used by the JMP and JSR instructions is specified by the addressing mode of the instruction.

Another instruction that uses the condition codes is the Set according to condition (Scc). The Scc instruction tests the specified condition (see table 6) and sets the byte specified by the effective address to all ones if the condition is true. Otherwise the byte is cleared.

System Control Operations

Several instructions offer easy implementation of system control functions. These instructions allow logical operations on the status and condition code bits, trap and exception processing, special range checking, special program flow control operations, and even a special peripheral reset operation.

Many of these can be used only if the MC68000 is in the supervisor mode (S bit in the system byte of the Status Register set) to support the operating system environment. These privileged instructions include those that change the state of bits in the system byte of the status register (ANDI, EORI, ORI, and MOVE EA to SR), change the value of the User Stack Pointer (MOVE USP), ReTurn from Exception trap (RTE), stop program execution (STOP), and cause the RESET pin to become an output and be asserted (RESET).

Instructions that directly change the contents of the Condition Code Register portion of the status register (ANDI, EORI, ORI, and MOVE EA to CCR) and store the status register (MOVE SR to EA) may be executed in user mode also.

The user program may also execute intructions that might generate a trap. These are: the range checking instruction (CHK); the specific trap generation to one of 16 trap vectors (TRAP); and causing a trap on overflow bit set in the condition code register [TRAPV].

Addressing Modes

The MC68000's 14 addressing modes can be grouped into three broad classes. These are: 1. Register District, 2. Memory, and 3. Inherent addressing.

Register Direct Addressing Modes

In Register Direct addressing, the effective address is one of the 16 registers. Register addressing can be divided into these two categories: 1. Data Register Direct — Dn; 2. Address Register Direct — An. The notation for Data Register Direct addressing is "Dn" [e.g., D0 refers to data register 0]. Likewise, Address Register Direct addressing notation is "An".

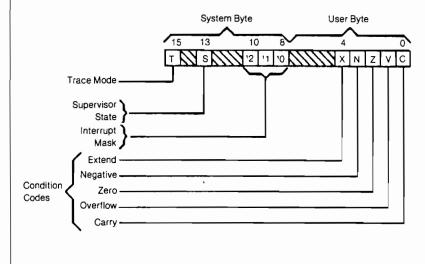
Memory Addressing Modes

Unlike Register Direct addressing, Memory addressing modes provide the

address of an operand that is in memory. A list of Memory addressing modes for the MC68000 follows. Beneath each of the Memory addressing modes is an example of its assembler syntax. All indirect references are placed in parentheses.

- 1. Indirect Addressing
 - a. Address Register Indirect (Anl
 - b. Address Register Indirect with Postincrement (An) +
 - c. Address Register Indirect with Predecrement -(An)
 - d. Address Register Indirect with Displacement disp[An]
 - e. Address Register Indirect with Index disp(An + Im. W) where Im is Am or Dm disp(An + Im. L) where Im is Am or Dm
- 2. Absolute Addressing
 - a. Absolute Short inst.W ADDRESS

Table 5: MC68000 Status Register



- b. Absolute Long inst.L ADDRESS
 - 4. Immediate Addressing
- a. Program Counter with Displacement disp(PC)

Program Counter Relative Addressing

- b. Program Counter With Index disp(PC + In) where In is An or Dn
- - a. Immediate #xxx
 - b. Quick Immediate #xxx

(continued)



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(Continued on page 98)

Table 6: Condition Codes Allowed for the DBcc, Scc, and Bcc Instructions.

CC	Carry Clear	LS	Lower or Same
CS	Carry Set	LT	Less Than
EQ	Equal	MI	Minus
F ·	False *	NE	Not Equal
GE	Greater or Equal	PL	Plus
GT	Greater Than	T	True
HI	Higher	VC	Overflow Clear
LE	Less or Equal	VS	Overflow Set

^{*} The false condition does not apply to Bcc.

The various forms of Address Register Indirect addressing provide great flexibility. In the simplest of these, the effective address is the contents of the specified address register. In the Postincrement mode, the effective address is still the specified address register but the address register will be incremented after the operation. The Predecrement mode is the same except the address register is decremented before the operation. The Pre-decrement and Post-increment modes automatically adjust the number of words to decrement or increment based on the size of the operation. For the Address Register Indirect with Displacement mode, the effective address is formed by summing the contents of the specified address register with a sign-extended 16-bit offset. The Address Register Indirect with Index effective address is the contents of the specified address register, summed with the sign extended 8-bit displacement and the specified index register. The index register can be any of the address or data registers and can be specified as either a long word or a word (which will be sign extended). The execution speed of the Address Register with Index is not affected by the size of the index.

In the Absolute Addressing modes, the effective address is specified in the word or words following the opcode. In the case of Absolute Short addressing, the word is sign-extended to form the effective address.

The Program Counter addressing modes use the contents of the program counter to form an effective address. These modes are important for writing position-independent code. The Program Counter with Displacement mode sums the contents of the program counter with a 16-bit sign-extended displacement just like Address Register Indirect with Displacement. The Program Counter with Index mode is identical to Address Register Indirect with

Index, except that the program counter is used instead of an address register.

For normal immediate addressing, the effective address is the word or long word immediately following the opcode, depending on the size of the operation. Single bytes are stored as a word (the upper byte is zeros). In Quick Immediate addressing, a special case of immediate addressing, the immediate data (a number from 1 to 8) is contained in a 3-bit field in the opcode. Quick Immediate addressing is available for ADD and SUB instructions where constants are used frequently. Immediate addressing limits the destination addressing mode to register direct. However, many special immediate instruction variants exist, such as ADDI, for using memory addressing for the destination address.

Implicit Addressing

Many instructions make implicit references to the program counter, the supervisor stack pointer, the user stack pointer, or the status register. For example, the RTS (return from subroutine) implies use of the program counter and the stack pointer.

Using the MC68000

In the second part, we hope to provide a better feel for using the MC68000 through a series of simple examples, In addition to showing assembler syntax, these examples will illustrate instructions such as LINK, UNLK, and CHK which may be unfamiliar.

Joe Jelemensky and Tom Whiteside are engineers in the Motorola MOS Microprocessor Design Group. They can be contacted at Motorola, Inc., Mail Drop M2880, 3501 Ed Bluestein Blvd., Austin, Texas 78721.



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68000 Instruction Set

Data Size/CCR Name

by Joe Hootman

Comments/Address modes and oncode

Table 1: Data Movement

Mnemonic

Editor's note: The author has compiled several detailed tables of 68000 instructions. This month's article offers a brief introduction to the 68000, along with table 1. We will publish more instruction tables in future issues, including those on addressing modes.

Since its introduction, the Motorola MC68000 has set a trend for 16-bit microprocessors that may become an industry standard. One reason for the 68000's quick success is the design philosophy Motorola used when the 68000 was conceived. Motorola wanted to give the 68000 a simple instruction set, and a flexible set of addressing modes. They also wanted it to be compatible with 6800 peripheral hardware and to contain many programming features. The implementation of this philosophy allows 6800/6809 microprocessor users to adapt easily to using both 68000 hardware and software. The instruction set, while compact, contains many special instructions that are unique to the 68000. These instructions allow two different processing states to exist in the 68000, as well as movement of data from 6800 peripherals. Special arithmetic instructions are also provided.

68000 users can turn to several references for help. The Motorola Users Manual¹ is essential for every 68000 user. An excellent book by L. J. Scanlon² explains the 68000 in detail, and provides excellent programming examples. The 68000 Microprocessor Handbook by G. Kane³ discusses characteristics and features of the 68000.

Special features of the 68000, which differentiate it from the 6800 series of microprocessors, are:

- 1. The data bus of the 68000 is 16 bits wide.
- 2. The 68000 is microprogrammed.
- The 68000 is expandable. These expandable features will be defined by Motorola in the future.

_	Mnemonic	Data Size/CCR	Name	Comments/Address modes and opcode
	MOVEA	16, 32 CCR XNZVC	Move Address	Moves the contents of the source to the destination address register. Sign of word data is extended to 32 bits. Opcode Format
				-
				15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0 Destination Source Mode Reg O 0 1 Mode Reg
				All addressing modes can be used for the source field. *
	MOVEM	16, 32 CCR XNZVC	Move Multiple Registers	Designated registers may be moved to an effective address.
				Opcode Format
i				15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0 0 1 direction
				Register List Mask
				Direction: 0-register to memory, 1-memory to register Size: 0-word field, 1-long word
				Post Increment Address Mode
				15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
				A7 A6 A5 A4 A3 A2 A1 A0 D7 D6 D5 D4 D3 D2 D1 D0
				Predecrement Addressing Mode
				15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
				D0 D1 D2 D3 D4 D5 D6 D7 A0 A1 A2 A3 A4 A5 A6 A7
				Predecrement Address modes cannot use Addressing modes: 1, 2, 4, 11, 12, 13, 14 *
				Post increment Address modes cannot use Address modes: 1, 2, 5, 13, 14 *
	MOVEP	16, 32 CCR XNZVC	Move Peripheral Data	Data is moved between data registers and memory. The high order byte of data bus is used when address is even; if address is odd the transfers are made to the low half of data bus. This instruction can be used to transfer data from 6800 devices. 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
	,			0 0 0 Data Op-Mode 0 0 1 Address Register
				Displacement
				Opmode: 100-Word from memory to register 101-Long word from memory to register 110-Word from register to memory 111-Long word from register to memory
	MOVEQ	32 CCR	Move Quick	Move 8-bit immediate data to a data register, with sign extension
		XNZVC	a. 177	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
		1100		0 1 1 1 Register 0 Data
				(Continued)

Mnemonic	Data Size/CCR	Name	Comments/Address modes and opcode
MOVE from SR	16 CCR	Move from status	The contents of the status register is moved to the destination location.
	X N Z V C	register	15 14 13 12 11 10 9 8 7 6 5 4 3 2 Î 0
			0 1 0 0 0 0 0 0 1 1 Effective Address Mode Register
			This instruction cannot use the following Address modes: 2, 10, 11, 12, 13, 14.*
MOVE	8, 16, 32 CCR	Move data from	This instruction moves the contents of the source to the destination.
	XNZVC - • • 00	source to destination	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
			0 0 Size Destination (EA) Source (EA) Register Mode Mode Register
			The size specifies the size of the data to be moved.
			01-Byte 10-Long word 11-Word
			The effective address of the destination cannot use: 10, 11, 12, 13, 14 *
			The effective address of the source can use all the addressing modes except 13, 14.
MOVE to	16 CCR	Move condition	The content of the source is moved to the condition code register.
	XNZVC	code	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
			0 1 0 0 0 1 0 0 1 Effective Address Mode Register
			All addressing modes can be used except 13, 14*
SWAP	16 CCR	Swap register	This instruction exchanges the upper half of a data register with the lower half of the data
	X N Z V C	halves	register. 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
			0 1 0 0 1 0 0 0 0 0 0 1 0 0 Register
			The addressing mode of this instruction is inherent.
EXG	32 CCR	Exchange registers	This instruction exchanges the contents of two registers. The exchange can take place between
	X N Z V C		any of the registers. 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
			1 1 0 Register 1 Op-mode Register Number
			Opmode: 01000-data registers 01001-address registers
			10001-data and address registers (bits 0-2 must be address register number)
LEA	32 CCR	Load effective	The effective address is loaded into the specified address register.
	X N Z V C	address	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
			0 1 0 0 Register 1 1 Effective Address Mode Register
			This instruction cannot be used with the following addressing modes: 1, 2, 4, 5, 13, 14*
PEA	36 CCR	Push effective	The effective address is computed and the long word is pushed onto the stack.
	XNZVC	address	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
			0 1 0 0 1 0 0 0 1 Effective Address Mode Register
			This instruction cannot use the following addressing modes: 1, 2, 4, 5, 13, 14 *

^{*}The Addressing Modes will be presented in future issues.

- 4. The 68000 will work with 6800 parts, with a special software statement, and hardware for completing the handshaking requirements.
- 5. The 68000 is capable of addressing a large amount of memory.
- The 68000 has a minimum number of instructions with a large number of addressing modes.
- 7. The 68000 is capable of supporting multiple users.
- 8. A supervisory state exists that is protected from access by the user. This state is used to support the overhead for multiple-user hardware interrupts, and certain classes of usergenerated errors and traps.
- The hardware architecture and software of the 68000 will easily support higher-level languages such as ADA, Pascal, Fortran, etc.

These features indicate that the 68000 will exist in its present form, or in a mildly modified form, for a significant length of time. The expandable features of the 68000 will allow users to learn a machine, and develop software and hardware, without fear of their work becoming quickly obsolete.

MOVE

Table 1 displays the MOVE instructions implemented in the 68000. These instructions move data both inside the processor from register to register, and from external RAM and external devices. The table indicates which bits are set in the USR with the completion of the instruction. The user part of the status register is the lower eight bits of the register; the system part is the upper eight bits. Motorola, in their instruction set, has chosen to refer to the user portion of the status register as the Condition Code Register [CCR].

A dash (—) in a bit location indicates that the operation has no effect on the bit. A * indicates that the operation may alter the bit, depending on the data and operation. Note that all the move operations except one do not set the V or C bits; these bits can only be set or cleared by an operation on data, or specific operations on the CCR.

The MOVE to CCR (move to condition code) can set or clear every bit in the CCR. This instruction could be used to check on the state of the first four

bits of a word, or to set or clear bits in the CCR. (The instruction could also be used to unstack the CCR from memory.)

MOVE (MOVE data from source to destination) is a popular instruction because it allows movement of data between data registers and address registers inside the 68000. It also allows the movement of data between the 68000 and RAM.

MOVE and MOVE A (Move Address) are similar, except that MOVE A is the instruction reserved for moving data to the address registers. That is, the destination register in the MOVE A instructions are the address registers. The MOVE instruction is generally used to move data to and from the data registers. Proper coding results in the MOVE A instruction.

If you want to examine the status register to find out the state of the X, N, Z, V, and C bits, as well as allowing the checking of the interrupt mask, the trace (T), and supervisor status, use the MOVE from SR (Move from Status Register) instruction.

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structions is MOVE M (Move Multiple Registers). This instruction allows the movement of the selected data registers to consecutive memory locations. It could be used to store the results of multiple word arithmetic operations. For example, if the IEEE floating point arithmetic standard is implemented, the registers would contain the various components of the floating point number. Note that because both data registers and address registers can be moved, this instruction can be used to stack the registers.

MOVE P (Move Peripheral Data) is used to move 8-bit data to the data registers from memory, or transfer 8-bit data from the data registers to memory. This instruction, in conjunction with the proper hardware interface, allows the transfer of data between the 68000 and 6800 peripheral parts.

MOVE Q [Move Quick] allows the immediate movement of 8-bit data to any designated data register. The sign of the data is extended to fill the 32 bits of the data register, thus the operation is always a long word operation. Some assemblers will use a MOVE Q instruction every time 8-bit data are to be moved.

The SWAP (Swap Register Halves) is not the same as the previously discussed MOVE instructions, but it is a data movement instruction. This instruction will place the upper 16 bits of the designated data register into the lower 16 bits of the designated data register. The lower 16 bits will be moved to the upper 16 bits of the register. This instruction could be used to move data in a 32-bit register into a position to be tested by the CCR.

EXG (Exchange Registers) exchanges the contents of two registers using the full 32 bits of the two registers. EXG will exchange data registers with data registers, address registers with address registers, and address registers with data registers. This type of instruction could move an address register to a data register to allow operations and testing of the state of the address, while not destroying the contents of the data register.

LEA [Load Effective Address] loads a specified address register with an effective address. LEA loads all 32 bits of the address register from memory. For example, LEA cannot be used to load the address register from a data register. MOVE A can be used for this. LEA effectively increments or decrements the contents of a register. To implement

the incrementing of a register, LEA is used with auto-incrementing or auto-decrementing addressing.

PEA (Push Effective Address) pushes the specified address onto the stack. This instruction is used as the traditional push instruction.

References

- 1. 16-Bit Microprocessor User's Manual, Third Edition. Prentice-Hall, Inc.; Englewood Cliffs, New Jersey 07632.
- 2. The 68000: Principles and Programming, L.J. Scanlon, Howard W. Sams and Co. Inc.; 4300 West 62nd St., Indianapolis, Indiana 46268.
- 68000 Microprocessor Handbook, G. Kane, OSBORNE/McGraw-Hill; 630 Bancroft Way, Berkeley, California 94710.

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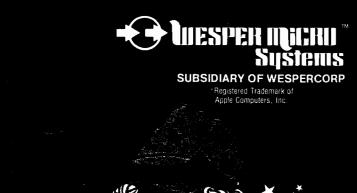
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Apple Slices

By Tim Osborn

This month's Apple Slices presents a technique to quickly access records on a random access file. This technique, called hashing, key-to-address transformation, or randomizing, converts a user key into an address (or record number) where the record associated with the key is stored. This key may be a customer number, part number, invoice number, or anything that identifies the target record to the user.

Hashing should be used where quick random access is desirable. If the application calls for a lot of sequential processing, hashing will not be a good design choice for data organization. In situations where data is accessed by key in some undetermined order, hashing is at an advantage, because most records can be found within a few reads. Listing 2 is an example in which name and address records are hashed on the name field. I will refer to the listing to help clarify some points and to present a practical example.

An important consideration in hashing is dealing with collisions or overflows. An overflow occurs when two or more keys in the key range generate the same address (home record) within the address range. This can happen if there are as many variations in the key set as there are addresses to store the data. For example, in my name and address retrieval system (listing 2), the file can hold up to a maximum of 500 records (a system design constraint) and there are an infinite number of names. Therefore, many names may be transformed into the same address, creating overflow situations.

Some overflow handling techniques include:

 Overflow linked lists: A separate area of the file is set aside for overflows. If an overflow occurs, it is stored in the first available overflow space and chained off from the home record. On a recall it is necessary to

Listing 1

0 A\$ = "" 10 CD = CHR\$ (4) PRINT COS"OPEN TEST-FILE, L100" 30 FOR J = 0 TO 500 PRINT COS"WRITE TEST-FILE, R"; J 40 50 FOR I = 1 TO 6 60 PRINT A\$ 70 NEXT 80 NECT PRINT CDS"CLOSE TEST-FILE"

search the chain until the key of the stored record matches the search key. The disadvantage of this technique is that the chain must be maintained and the overflow space must be managed, which adds complexity. The advantage is that any number of overflows can be supported as long as there is spare room in the overflow area.

- 2. Distributed overflow space: Instead of using a separate overflow area. overflows are interleaved with normal records by setting aside some records and designating them as overflow. The advantage here is that the proximity of an overflow may allow it to be accessed without requiring additional head movement of the random access device. For Apple owners this means that we would attempt to store overflows on the same track as the home record. The disadvantage is that chain maintenance adds complexity to the system.
- 3. Open addressing: This technique tries to store the record in the home space first; if it is full, it stores the record in the next available space. This method is easy to implement. As long as a sufficient amount of free space exists in the file, the record can be stored close to the home record. The disadvantage is that, when the file begins to fill up, the search time increases, as it takes longer to find an unused record.

After considering the various techniques available, I chose the open addressing technique because of its design

simplicity. I implemented this in my FIND routine (lines 9000-9060) by incrementing the record number (KEY) until a free record was found or until I searched 20 records. I recognize a free record by NM\$ being equal to NAM\$ (NAM\$ is set to null by the ADD routine before calling FIND), or by NM\$ being equal to "0". NM\$ is null if the record has never been used (this is the way the file is initialized when listing 1 creates the file and NM\$ is equal to "0" if it was used at one time but is now available. The reason for a different code for a once-used record is that the program must continue to search up to 20 records, or until NM\$ is equal to null (" "), to make sure the new record is not a duplicate. If NM\$ = "", then I know a record with this same key could not have been stored further into the file. It would have been stored here if it had searched this far before. If NM\$ = "0", it is possible that this record was used when a record with the new record's key was added and the system had to look further into the file at that time to find a free space.

If it finds a duplicate, the RC (return code) is set to 3 and returns to the ADD subroutine. The ADD subroutine will display a message and return to the main menu (line 7034). If NM\$ = ''0'', FIND saves the available record number in KY and sets RC = 2. This tells the ADD subroutine to use KY instead of KEY as the available space. This is necessary because the FIND subroutine continues to increment KEY to search for a record with a null value in NM\$. This process can be described as follows (upon return to ADD):

- If RC = 0, then KEY = the available space where we can store the new record.
- If RC=1, then 20 records were searched and all were used. (ADD will display a "FILE TOO FULL" message.)
- If RC=2, then a previously used record was available. The value in KY will be used as the available space.

 If RC=3, then the new record is a duplicate. (ADD will display a "DUPLICATE RECORD" error message.

FIND can be forced to search more records by raising the value of BIG (which is currently set at 20). If BIG is decremented, it is possible that previously stored records will not be found. I did some extensive testing with random key values and found that the "FILE TOO FULL" message was produced when the file contained approximately 300 records with BIG set at 20. The trade-off is between efficiency and space; the further you allow records to be stored from the home record, the lower the efficiency. The more you limit record storage to the home record, the larger the amount of wasted space.

The other major consideration when designing a hashing system is the key-to-address transformation technique. This technique converts the user key into a physical location for storage. All techniques transform the key into a number within an order of magnitude that is the same as the number of addresses (records). Then they multiply the intermediate result by a factor that limits the final result to the range of addresses available on the storage device.

In my system I chose a method called center-squaring. This method involves taking the key value, converting it into a number which can be squared, squaring the number, stripping off as many outer digits as necessary, and multiplying by a factor to limit the address to the valid range. My COMPUTE ADDRESS subroutine [lines 6000-6448] follows this sequence of operations.

- Line 6000 sets KEY to 0. KEY will become the address of the home record — the first record where we will attempt to store a new name and address record.
- Line 6002 finds the length of KEY\$, which holds the name associated with the new name and address record.
- 3. Lines 6005-6030 convert KEY\$ into a number through table lookups and store the accumulation in KEY. The [J * J / 3] expression in line 6020 helps insure that a number of the proper magnitude is produced.

Listing 2

```
1 GOSUB 10000: REM INITIALIZE TABLE
10 BIG = 15:FL = 500:CD$ = CHR$ (4): DIM SV$(6)
14 PRINT CD$"OPEN TEST-FILE, L100"
15
    PRINT CD$
    IF RC = 0 THEN GOTO 18
    FOR J = 1 TO 3000: NEXT J:RC = 0
HOME: PRINT "(Q)UIT (A)DD"
17
18
    INPUT "(C)HANGE (D) ELETE (I) NQUIRE "; AS
   IF A$ = "A" THEN GOSUB 7000: GOTO 16
IF A$ = "C" THEN GOSUB 7200: GOTO 16
IF A$ = "D" THEN GOSUB 7500: GOTO 16
IF A$ = "I" THEN GOSUB 8500: GOTO 16
70
    IF A$ = "Q" THEN PRINT CD$"CLOSE TEST-FILE": END
   6010 18
5998 REM
5999 REM
           ****COMPUTE ADDRESS****
6000 KEY = 0
6002 LN = LEN (KEY$)
6005 FOR I = 1 TO LN
6010 MD$ = MTD$ (KEY$, I, 1)
6015 FOR J = 0 TO 35
6020 IF MD$ = TBL$(J) THEN KEY = KEY + (J * J / 3):J = 35
6030 NEXT : NEXT
6050 KEY = KEY * KEY
6060 KEY$ = STR$ (KEY)
6070 KEY$ = HID$ (KEY$,2,3)
6080 KEY = VAL (KEY$)
6090 KEY = INT (KEY * .5)
6100 RETURN
6448
      REM
6449
      REM ****WRITE RECORD****
6500
       PRINT CD$; "WRITE TEST-FILE , R"; KEY
6510
      PRINT NMS
6520
       PRINT ROS
6525
       PRINT CTYS
6527
       PRINT ST$
6530
       PRINT ZP$
6535
       PRINT TELS
6540
      PRINT CD$
6550
       RETURN
6998
      REM
      REM ****ADD RECORDS****
6999
7000 FUNC$ = "ADD":NAM$ = ""
      GOSUB 8000: REM GET INPUTS
7005
7006 SV$(1) = NM$:SV$(2) = RD$:SV$(3) = CTY$:SV$(4) = ST$:SV$(5) = ZP$:SV
      $(6) = TELS
7007 KEY$ = NM$
7010 GOSUB 6000: REM COMPUTE ADDRESS
7020 GOSUB 9000: REM FIND AVAIL. REC
7025 FUNC$ = ""
      IF RC > 0 THEN PRINT CD$
7027
7030
      IF RC = 1 THEN
                        INVERSE : PRINT "FILE TOO FULL": NORMAL : RETURN
7032 IF RC = 2 THEN KEY = KY
7034 IF RC = 3 THEN INVERSE : PRINT "DUPLICATE RECORD": NORMAL : RETURN
7035 NMS = SV$(1):RO$ = <math>SV$(2):CTY$ = <math>SV$(3):ST$ = <math>SV$(4):ZP$ = <math>SV$(5):TE
      L$ = SV$(6)
7040 GOSUB 6500: REM WRITE RECORD
7050 RETURN
7198
       REM
      REM ****CHANGE****
7199
7200 HOME : PRINT "DO YOU WISH TO CHANGE THE NAME?"
      INPUT "REPLY (Y)ES OR (N)O ";A$
7210
7220
       IF A$ = "N" GOTO 7290
      IF A$ < > "Y" GOTO 7210
INPUT "OLD NAME = ";NM$; IF NM$ = "" GOTO 7235
7230
7235
7240 KEY$ = NMS: GOSUB 6000: REM COMPUTE ADDRESS
7250 NAMS = NMS: GOSUB 9000: REM FIND RECORD
7260 IF RC = 1 THEN : INVERSE : PRINT "RECORD NOT FOUND": NORMAL : PRINT
      CDS: RETURN
7270 NM$ = "0": GOSUB 6500: REM DELETE OLD RECORD
7280 INPUT "NEW NAME = "; NM$: IF NM$ = "" GOTO 7280
7282 \text{ KEY$} = \text{NMS}
7284 GOSUB 6000: REM COMPUTE NEW KEY
7286
       GOTO 7330
      INPUT "NAME
7290
                        = "; NM$: KEY$ = NM$: IF NM$ = "" GOTO 7290
7300 GOSUB 6000
7310 NAMS = NMS: GOSUB 9000: REM FIND RECORD
7320 IF RC = 1 THEN INVERSE : PRINT "RECORD NOT FOUND": NORMAL : PRINT
     CDS: RETURN
7330 IMPUT "CHANGE STREET? ,(Y)ES OR (N)O ";A$
7340 IF A$ = "Y" THEN INPUT "STREET = ";RD$: GOTO 7360
7350 IF A$ < > "N" GOTO 7330
```

(Continued)

Listing 2 (Continued)

```
7340 IF A$ = "Y" THEN INPUT "STREET = ";RD$: GOTO 7360
7350 IF A$ < > "N" GOTO 7330
      INPUT "CHANGE CITY?, (Y)ES OR (N)O ";A$

IF A$ = "Y" THEN INPUT "CITY = ";CTY$: GOTO 7382
      IF AS = "Y" THEN INPUT "CITY
IF AS < > "N" GOTO 7360
7370
7380
      INPUT "CHANGE STATE?, (Y)ES OR (N)O ";A$
7382
      IP A$ = "Y" THEN INPUT "STATE
IF A$ < > "N" GOTO 7382
                                              = ";ST$: GOTO 7390
7384
7386
      INPUT "CHANGE ZIP?, (Y)ES OR (N)O ";A$
7400 IF A$ = "Y" THEN INPUT "ZIP
7410 IF A$ < > "N" GOTO 7390
                                               = "; A$: GOTO 7420
      INPUT "CHANGE TEL. NO?, (Y)ES OR (N)O ";AS
IF AS = "Y" THEN INPUT "TEL. NO. = ";TELS: GOTO 7450
7420
7430
      IF A$ < > "N" GOTO 7420
7440
      GOSUB 6500
7450
7470
      RETURN
7498
      REM
      REM ****DELETE****
7499
7500
      HOME
      INPUT "NAME
7510
                        = ":KEYS
7515 NAMS = KEYS
7520 GOSUB 6000: REM COMPUTE ADDRESS
7530 GOSUB 9000: REM FIND RECORD
7540 IF RC = 1 THEN INVERSE : PRINT "RECORD NOT FOUND": NORMAL : PRINT
     CDS: RETURN
7550 NM$ = "0"
7560 GOSUB 6500: REM WRITE RECORD
7570
      RETURN
7998
      REM
      REM ****GET INPUTS****
7999
8000
      HOME
      INPUT "NAME
                        = ":NMS: IF NMS = "" OR NMS = "0" GOTO 8000: REM R
8010
      ESTERVED NAMES
8020
       INPUT "STREET = ":RD$
       INPUT "CITY
                        = ";CTY$
8030
8035
       INPUT "STATE
                       = ":ST$
       INPUT "ZIP
                        = ";ZP$
8040
8050
       INPUT "TEL. NO.= "; TELS
8060
       RETURN
8498
       REM
      REM ****INQUIRE****
8499
      HOME : INPUT "NAME
8500
                                = ":NAMS
8505 KEYS = NAMS
      GOSUB 6000: REM COMPUTE ADDRESS
8510
      GOSUB 9000: REM FIND RECORD
8520
      IF RC = 1 THEN INVERSE : PRINT "RECORD NOT FOUND": NORMAL : PRINT
8530
      CD$: RETURN
8550
      PRINT "STREET = "; RD$
      PRINT "CITY
                        = ";CTY$
8560
      PRINT "STATE = ";ST$
8565
      PRINT "ZIP
                        = "; ZP$
8570
      PRINT "TEL. NO. = ": TELS
8580
8590
      PRINT
      PRINT "PRESS RETURN TO CONTINUE"
8600
8610
      GET AS
8620
       RETURN
8998
      REM
              ****FIND RECORD****
8999
      REM
9000 J = 0:RC = 0
      IF J < > BIG GOTO 9020
9010
      IF RC = 0 THEN RC = 1: RETURN
9012
      RETURN: REM RC=2

IF KEY > PL THEN KEY = 0
9014
9020
9030
      PRINT CDS "READ TEST-FILE.R": KEY
      INPUT NMS, RDS, CTYS, STS, ZPS, TELS
IF FUNCS < > "ADD" THEN GOTO 9050
IF NMS = "0" AND RC = 0 THEN KY = KEY:RC = 2: GOTO 9060
9040
9042
9044
      IF NM$ = SV$(1) THEN RC = 3: RETURN
9045
9050 IF NM$ = NAM$ THEN PRINT CD$: RETURN
9060 KEY = KEY + 1:J = J + 1: GOTO 9010
9998 REM
9999 REM ****BUILD TABLE****
10000 DIM TBL$(35)
10010 FOR J = 0 TO 35: READ TBL$(J): NEXT J
10015 RETURN
10100 DATA "1","2","3","4","5","6","7","8","9","0","0","%","E","R","T",
"Y","U","I","0","P","A","S","D","F","G","H","J","K","L","Z","X","C",
"V","B","N","M"
```

- 4. Line 6050 squares KEY.
- 5. Line 6060 converts KEY back to a string (KEY\$).
- Line 6070 takes out the second, third, and fourth digits, and stores them back in KEY\$.
- 7. Line 6080 converts KEY\$ back to a numerical value (KEY).
- 8. Line 6090 brings KEY within the range of records on my file. When listing 1 creates the file, it creates 500 records, thus the factor of .5.

It is important that a key-to-address transformation method spread the records throughout the file as evenly as possible to minimize overflows. The address generated should bear no relation to the key, so that a key with a higher value may be stored below a key with a lower value. This is often necessary because applications have many records with similar keys. If there is a relationship between the keys and the addresses generated, it would cause one part of the file to become full; overflows will occur in great numbers.

I hope I have provided enough information to allow you to develop your own hashing applications. If you don't feel up to programming your own system, listing 1 and listing 2 can be used as a quick-access address book. Remember to run listing 1 first to intitialize the file. If you get the "FILE TOO FULL" error message, then you may wish to bump up BIG, which is initialized on line 10. This will slow down access but will allow you to store more records.

Otherwise, you can change listing 1 to load more than 500 records. If you do, remember to change FL [file length—line 10] from 500 to the number of records you wish to load. Also change the range multiplication factor from .5 to .xxx, where xxx is equal to the number of records you wish to load (line 6090). If you plan to load more than a thousand records, you will need to change the center-extraction from MID\$ [KEY\$,2,3] to MID\$ [KEY\$,2,4] or MID\$ [KEY\$,3,4].

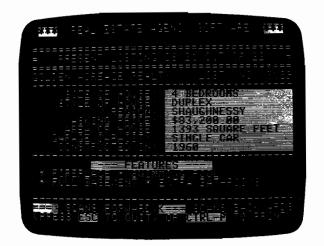
Reference

 Computer Data-Base Organization, by James Martin, Prentice-Hall, Englewood Cliffs, New Jersey.

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AMPER-POS for the Apple

by Philippe Francois

This machine-language utility extends Applesoft to include a POSition function similar to Pascal's.

AMPER-POS

requires:

Apple II or Apple II Plus with Applesoft in ROM or language card

To Pascal, there is a POS function that searches within the string variable TEXT for an occurrence of a character string matching the context of the "pattern" variable PATTERN. The value returned is an integer representing the first character position where the matching string is found. If no matching string is found, the POS returns a value of zero that does not correspond to one of the allowable character positions in a STRING variable [see reference 1].

PROGRAM POSITION; VAR PATTERN, TEXT: string; BEGIN PATTERN: = 'pal'; TEXT: = 'So long,pal..'' WRITELN('Position of ',PATTERN,' in ',TEXT,' = ',POS(PATTERN, TEXT)); END.

This function fails in Applesoft, so you need to write a short machine routine, AMPER-POS, which gives the first occurrence of PATTERN in TEXT, but also gives the other occurrences (if any).

The Routine

AMPER-POS was designed to find the occurrences of a sub-string A\$ in a string B\$. There are two entry points:

 The first entry point finds the first occurrence of A\$ in B\$ and has the format:

& P (A\$, B\$, C%)

where:

A\$ is the sub-string variable B\$ is the string variable C% is the output integer variable

The second entry point finds the other strings' occurrences and has the simple format:

CALL 880

System Requirement

AMPER-POS is written in assembly language and can be run on a 16K, 32K, or 48K system with either disk or cassettes. The \$300 (768 decimal) address has been chosen for ORG because the \$300,\$3FF memory range is available for short machine-language programs (see reference 2).

```
Listing 1
0800
                                        PAG
0800
                                                                  POSITION FUNCTION
0800
0800
                                                                        (AMPER-POS)
0800
                                                                                                             ***
0800
0800
                                                                  PHILIPPE FRANCOIS
0800
0800
                      11
12
13
14
15
0800
                               AMPERPOS IS AN APPLESOFT ROM ROUTINE FOR FINDING OCCURENCES OF A SUB-STRING "PATTERN" IN A STRING "TEXT"
0800
0800
0800
0800
                      16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
                                ENTRY POINTS:
റമററ
                                                    1) FOR THE FIRST OCCURENCE: & P (A$,B$,C%)
                               WHERE:
0800
                                                   A$=SUB STRING (PATTERN)
B$=STRING (TEXT)
C%=POSITION OF A$ IN B$
0800
0800
0800
0800
0800
                                IT RETURNS:
                               A) ZERO IN C% IF AS IS NOT IN BS
B) THE FIRST CHARACTER POSITION WHERE
THE MATCHING STRING AS IS FOUND IN BS
0800
0800
0800
0800
                                                    2) CALL 880 FOR THE NEXT OCCURENCES OF A$ IN B$
0800
                      31
32
33
34
35
36
37
                             ; AMPERPOS USEAGE
0800
0800
                             BEGINL EPZ SOO
                                                                         : EFFECTIVE STRING BEGINNING
0800
                             BEGINH EPZ BEGINL+$01
                                                                            "PATTERN" LENGTH
വളവ
                                         EPZ BEGINH+$01
                                                                           "TEXT" LENGTH
PTRL,H IS A POINTER
TO THE STRING ADDRESS
0800
                                        EPZ M+$01
                      38
39
40
0800
0080
                                        EPZ N+$01
EPZ PTRL+$01
                             PTRH
                                                                           STARTING POSITION IN TEXT
IF PATTERN IS FOUND IN TEXT
0800
                             INDICJ EPZ PTRH+$01
0800
                      41
42
43
44
45
46
47
48
49
                             PLAG
                                        EPZ INDICJ+$01
0800
                                                                            THEN FLAG=1
                                                                         ; ELSE FLAG=0
0800
0800
                             ; APPLESOFT USEAGE
0800
                             XSAV
                                                                        ; O=NUMBER FF=STRING
; CHECK THE "("
; CHECK THE ")"
; CHECK THE ","
; "MISMATCH" ERROR
                            VALTYP EPZ $11
CHKOPN EQU $DEBB
CHKCLS EQU $DEBB
0800
0800
                      50
51
52
53
54
0800
                            CHRCOM EQU SDEBE
                             ERROR
0800
                                        EQU $DD76
                            CHRGET EQU $00B1
0800
                                                                         ; READ A VARIABLE AND FIND IT IN ; MEMORY(ON EXIT THE ADDRESS TO ; THE VALUE OF THE VARIABLE IN A,Y)
                      55
0800
                      56
57
0800
                                        ORG $300
                      58
                                                                                                               (Continued)
0300
```

Entering AMPER-POS

To enter the program, type in the hex codes or instructions from the source listing with an assembler. To save the program, type:

BSAVE AMPER-POS OBJ,A\$300, L\$BC (for disk)

300-3BCW (for cassettes)

Theory

Suppose we want to find the first instance of the pattern string PATTERN(1)...PATTERN(M) in the target string TEXT(1)...TEXT(N), M = N. [PATTERN(I] denotes the Ith character of PATTERN.] An obvious approach is to align PATTERN with the leftmost M characters at TEXT and start matching. When a mismatch occurs, shift PATTERN right one character and restart matching from its first position. Continue until either PATTERN matches or there are no more characters to match in TEXT.

The following algorithm that we use in the AMPER-POS program is the formal version of what has just been described (see reference 3 for more details):

```
INTEGER i,j,m,n;
STRING pattern(1:m),text(1:n);
i: = j: = 1;
DO WHILE i <= m AND j <= n;
IF pattern(i) = text(j) THEN
DO/*keep matching*/
i: = i + 1;/*bump pattern position*/
j: = j + 1;/*and text position*/
END
ELSE
DO/*backtrack*/
j: = j + 2 - i;
i: = 1;
END
END</pre>
```

Program Logic

Here is a synopsis of the program logic:

Lines 64-143 retrieve the parameters from the "&" statement. Applesoft ROM routines are used to reduce the length of the program.

Lines 152-194 translate the precedent algorithm. Take the X and Y registers for the I and J indexes, but because AB-SOLUTE,X and INDIRECT,Y addressing begin with the zero value for X and Y, the algorithm has been slightly modified.

```
Listing 1 (Continued)
0300
                  60
0300
0300
                          * PARSING
0300
0300
                           VERIFY " P ( "
0300
                  65
0300
                  66
                  67
68
0300
0300 C9D0
                                CMP #"P"
                                                           ; CHECK THE "P"
0302 F003
                                BEQ *+5
                  70
71
0304
                                PAG
0304 4C76DD
                                 IMP ERROR
0307 208100
                  72
                                JSR CHRGET
                  73
74
030A 20BBDE
                                                           ; CHECK THE "("
030D
030D
030D
030D
                  76
77
                           SEARCH "PATTERN"
030D
                  78
                                 JSR PTRGET
030D 20E3DF
                  79
0310 E611
                  80
                                INC VALTYP
0312 F003
0314 4C76DD
                                BEQ STRG1
                                                          : VALTYP EQUAL ZERO. IT IS A STRING
                  81
                                JMP ERROR
                       STRG1:
0317
                  83
                                STA PTRL
STY PTRH
LDY #$00
0317 8504
0319 8405
031B A000
                  85
031D B104
031F 8502
0321 C8
0322 B104
                  87
                                LDA (PTRL), Y
                                 STA M
                  88
                                 INY
                  99
                  90
                                 LDA (PTRL),Y
                  91
92
                                STA BEGINL
INY
0324 8500
0326 CB
                                T.DA (PTRL),Y
0327 B104
                  93
0329 8501
                  94
95
                                STA BEGINH
032B
032B
                  96
                  97
98
                           REWRITE THE SUBSTRING "PATTERN" AT PATTRN ADDRESS
032B
032B
032B
                  99
032B A0FF
032D C8
                 100
                                LDY #$FF
                                INY
                       LABEL1
032E B100
0330 99BA03
0333 C402
                                LDA (BEGINL),Y
                 102
                 103
104
                                STA PATTRN,Y
0335 DOF6
                 105
                                 BNE LABELI
0337
0337 20BEDE
                 106
107
                                 JSR CHKCOM
033A
                 108
                 109
033A
                           SEARCH "TEXT"
033A
                 110
                 111
033A
                 112
033A 20E3DF
033D E611
                                 JSR PTRGET
                                 INC VALTYP
033F F003
0341 4C76DD
                                 BEO STRG2
                                 JMP ERROR
0344
0344 8504
0346 8405
0346 A000
                       STRG2:
                 119
                                 STY PTRH
                                 LDY $500
                 120
034A B104
034C 8503
                                 LDA (PTRL),Y
STA N
                 121
034E C8
                                 LDA (PTRL),Y
034F B104
0351 B500
                 124
125
                                 STA BEGINL
0353 C8
0354 B104
0356 8501
                 126
127
                                 INY
                                 LDA (PTRL), Y
STA BEGINH
                 128
0358
0358 20BEDE
035B
                 130
131
                                 JSR CHKCOM
035B
                  132
035B
                           SEARCH OUTPUT VARIABLE
035B
                 134
035B
035B 20E3DF
035B A611
0360 F003
0362 2076DD
0365
0365
                                 JSR PTRGET
LDX VALTYP
                                 BEQ INT
JSR ERROR
                 138
                 140
                        INT
0365 B504
0367 8405
                                 STA PTRL
STY PTRH
0369
0369 2088DE
                                 JSR CHKCLS
036C
036C
036C
036C
                          * RESOLVING
036C
036C
                  150
                  151
152
 036C
 036C
036C A900
036E 8506
                 154
155
                                 LDA #$00
STA INDICJ
                                                           , INDICJ:=0
0370
                 156
157
                            SECOND ENTRY POINT: CALL (ORG ADDRESS+$63)
 0370
                  158
                                                                                            (Continued
 0370
```

```
Listing 1 (Continued)
0370 A200
0372 8607
0374 A406
0376
                                   LDX #$00
                                                               ; I:=0 ; J:=INDICJ
                  162
163
                                   STX FLAG
                                   LDY INDICJ
                  164
165
0376
0376
0376 E402
                         BEGIN:
                  166
167
                                   CPX M
                                                               ; DO WHILE I <= M AND J <= N
0378 F027
037A C403
037C F023
037E B100
                                   BEQ END
CPY N
                  168
                  169
170
                                   BEO END
                                   LDA (BEGINL),Y
                  171
0380 DDBA03
0383 D00E
                  172
173
                                   CMP PATTRN,X
BNE LABEL2
                                                                     IF PATTERN(I)=TEXT(J) THEN
0385 A507
0387 D006
0389 A901
0388 8507
                  174
175
176
177
                                   LDA FLAG
                                                                          IF PLAG=0 THEN
                                   ENE LABELS
                                                                          DO
                                   STA PLAG
                                                                              FLAG:=1
038D 8406
                  178
                                   STY INDICJ
                                                                              INDICJ:=J
038F
                                                                          RND
0387
                  180
                                                                          ELSE
038F
                                                                          DO
038F E8
                  182
                         LABELS INX
                                                                             1:=1+1
0390 CB
                  183
                                   INY
                                                                             J_1=J+1
0391
0391 DOE3
                  184
185
                                                                          END
                         1
                                   BNE BEGIN
0393 C8
0394 8646
0396 38
0397 98
                  186
187
188
189
                         LABEL2 INY
STX XSAV
                                                                      00
                                   SEC
                                                                         J:=J+2-I
                                   TYA
0398 E546
039A A8
                  190
                                   SBC XSAV
0398 A200
039D 8607
                   192
                                   LDX #$00
                  193
                                   STX FLAG
                                                                          FLAG:=0
0395
039F FOD5
                  195
196
197
                                   BEO BEGIN
03A1
                                                              : END
03A1
                  198
03A1
03A1
                  199
                             END OF ALGORITM
                  200
03A1
03A1
                  202
                         END:
03A1 A507
                                   LDA FLAG
03A3 D004
03A5 A9FF
03A7 8506
                                                              ; IF FLAG=0 THEN
                  204
                                   BNE LABEL4
                                   LDA #SFF
STA INDICJ
                                                                 INDICJ:=0
                  206
03A9
                  207
03A9
03A9 A506
                  208
                         LABEL4:
                                   LDA INDICJ
                                                              : ELSE
03AB
03AB 18
                  210
                                   PAG
                                                              ; INDICJ:=INDICJ+1
03AC 6901
                  212
213
                                   ADC #$01
O3AE
                  214
215
                             PUT INDICJ IN OUTPUT VARIABLE
OBAE
                  216
03AE
                  217
03AE A001
03B0 9104
                  218
                                   LDY #$01
                  219
                                   STA (PTRL).Y
03 B2
                  221
03B2
                             FOR NEXT OCCURENCE : INDICJ:=INDICJ+M
03B2
03B2 18
03B3 A506
03B5 6502
03B7 8506
03B9 60
                                   LDA INDICJ
ADC M
                  225
                  226
227
228
                                   STA INDICJ
                                   RTS
03BA
03BA
                  229
230
                         PATTRN:
03BA
03BA
                  231
                  232
233
                             ROOM FOR A "PATTERN" STRING UP TO 15 CHARACTERS
03 BA
                  234
03BA
03C9
                  235
                                   DFS 15
                  236
03C9
                         STOP:
```

```
Figure 1
            TAB( 14); "*********
    PRINT
            TAB( 14); "* EXAMPLE 1 *"
TAB( 14); "***********
    PRINT
     PRINT CHR$ (4)"BLOAD AMPER-POS.OBJ"
POKE 1014,0: POKE 1015,3: REM FETCH THE "&" AMPERSAND
A$ = "!":B$ = "THIS!IS!A!TEST"
30 A$ =
     & P(A$,B$,PO%)
     GOSUB 100
CALL 880: GOSUB 100
50
60
70
     IF PO% < > 0 THEN 60
90
     REM PRINTING SUBROUTINE
      IF POS = 0 THEN PRINT : PRINT "NO (OR NO MORE) OCCURENCE OF <"; AS; "
100
        ": RETURN
      PRINT : PRINT "THERE IS AN OCCURENCE OF <"; A$; "> IN <"; B$; "> AT THE
       "; PO%; "TH POSITION": RETURN
                                                                   (Continued on next page)
```

Lines 200-216 put the result of the search in the output variable (zero if the search is unsuccessful).

Lines 221-225 prepare the INDICJ variable for the next searching.

See figure 1 for an example of how to use AMPER-POS.

If you save a name "SMITH", an address "31ST AV NY", and a phone number "333 444 555", on a mailing sequential file, you can use the sequence shown in figure 2.

```
Figure 2

10 PRINT "*************
20 PRINT "* EXAMPLE 2 *"
30 PRINT "**************
40 PRINT
50 D$ = CHR$ (4)
60 PRINT D$"OPEN MAIL"
70 PRINT D$"WRITE MAIL"
80 PRINT "SMITH":
PRINT "31ST AV N.Y."
PRINT "333 444 5555"
90 PRINT D$"CLOSE MAIL"
```

But because it is better to have only one PRINT statement, we concatenate the three strings, separated with a "!". The statement 40 becomes:

```
40 A$ = "SMITH!31st AV NY!333 444 555!"
```

and statement 80 becomes:

80 PRINT A\$

so that figure 3 will look like:

```
Figure 3

10 PRINT "***********
20 PRINT "* EXAMPLE 3 *"
30 PRINT "*************
40 A$ = "$MITH!31ST AV

N.Y.!333 444 5555!"

50 D$ = CHR$ (4)
60 PRINT D$"00PEN MAIL"
70 PRINT D$ "WRITE MAIL"
80 PRINT A$
90 PRINT D$"CLOSE MAIL"
```

And to re-read the three values we use the program in figure 4.

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- "A Comparison of Three String-Matching Algorithms," G. de V. Smit, Software Practice and Experience, Vol. 12, No. 1.

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Figure 1 (Continued)

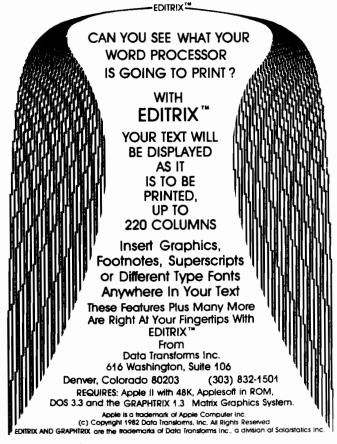
THERE IS AN OCCURENCE OF <1> IN THISIISIAITEST AT THE 5TH POSITION
THERE IS AN OCCURENCE OF <1> IN THISIISIAITEST AT THE 8TH POSITION
THERE IS AN OCCURENCE OF <1> IN THISIISIAITEST AT THE 10TH POSITION
NO (OR NO MORE) OCCURENCE OF <1>

Figure 4

```
10 D$ = CHR$ (4)
15 PRINT D$"BLOAD AMPER-POS.OBJ"
16 POKE 1014,0: POKE 1015,3: REM LOAD & VECTOR
20 PRINT D$"OPEN MAIL"
30 PRINT D$"READ MAIL"
40 INPUT B$
50 PRINT D$"CLOSE MAIL"
60 A$ = "1"
70 & P(A$, B$, PO$)
80 NA$ = MID$ (B$,1,PO$ - 1):J = PO$ + 1
90 CALL 880:AD$ = MID$ (B$,J,PO$ - J):J = PO$ + 1
100 CALL 880:PN$ = MID$ (B$,J,PO$ - J)
```

MICRO"



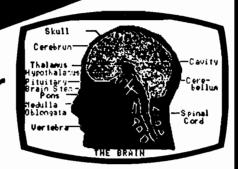


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6809 Macros for Structured Programming

by Hal Clark

This article presents a technique for using assembler macros to allow structured assembly-language programming. The examples presented are for the 6809, but they can be easily modified for other processors.

Structured Macros

requires:

6809-based machine with macro assembler May be adapted to other processors

Structured program development is defined as top down programming. When using this approach, the main body of the program is developed first, then each succeeding level is developed until the total program is finished. When the program is entered into the computer, it is usually entered in the same, top down, fashion.

The newer structured languages, such as ADA, Pascal, and Modula, require that constants, variables, and procedures/functions (subroutines) be defined before use, thus giving the following program structure:

PROGRAM name CONSTant definitions VARiable definitions FUNCTION definitions PROCEDURE definitions main program

BASIC and machine language, however, do not require that constants, variables, or subroutines be defined before use. Neither BASIC nor machine language requires program structuring. A lack of program structuring often results in an excessive use of memory and an unreasonable amount of time devoted to debugging, and customer support.

Pascal is a highly strucutred language that allows the systematic

```
Listing 1
*********************
       TITLE - MACRO, X09
       AUTHOR: HAL CLARK
             ON-GOING IDEAS
             RD#1 BOX 810
             STARKSBORG, UT 05487
**********************
************
            A MACRO TO DEFINE THE BEGINNING OF A PROGRAM
             IT NAMES THE PROGRAM FOR THE LINKER AND
             JUMPS TO THE LABEL <BEGIN>.
             EX.
             ******************
PROG
       MACR
             PNAME
       NAME
             PNAME
       JMP
             BEGIN
       FNIM
       PAGE
      *****
            A MACRO TO PUSH PASSED PARAMETERS ONTO THE SYSTEM
                    THE TYPE IS EXPECTED TO BE BYTE, WORD, REF,
             STACK.
             OR REG.
      ********************
*****
      EQU
WORD
       EQU
REGA
       EQU
       EQU
REGB
REGI
REGX
       EQU
REGY
       EQU
REGU
       EQU
FARM
       MACRO
             VALUE, TYPE
             NARG.EQ.1
       TF
             VALUE.EQ.REGA
ZZZPOS
       SET
             ZZZPOS+1
       F'SHS
       ELSE
             VALUE.EG.REG®
ZZZPOS
       SET
             ZZZPOS+1
       PSHS
       ELSE
             VALUE, EQ.REGD
ZZZPOS
       SET
             ZZZPOS+2
       PSHS
       ELSE
             VALUE FORREGY
ZZZPOS
      SET
             ZZZFOS+2
       PSHS
       ELSE
             VALUE.EQ.REGY
ZZZPOS
      SET
             ZZZPOS+2
       PSHS
      ELSE
             VALUE, EQ. REGU
777P0S
      SET
             ZZZFOS+2
      PSHS
       ELSE
      ERROR
             INVALID ARGUMENT
      ENDIF
       ENDIF
      ENDIF
      FNDTF
      ENDIF
```

(Continued on next page)

development of computer programs. These programs are easy to debug and modify due to their structure. Unfortunately, not all applications can afford the overhead in memory usage and execution speed that many versions of Pascal require. The most transportable versions of Pascal compile the source code into some form of intermediate code, usually called P-code, which is then interpreted each time the program is run

The macros presented in this article allow structured development of machine-language programs. Programs developed using these macros appear to be a combination of Pascal and in-line assembly language. By using macros, the structure of Pascal may be combined with the execution speed and memory efficiency of assembly language.

These macros generate machine code for the Motorola 6809 CPU. They are designed to be used with the Microtek 6809 cross-assembler, which is written in FORTRAN and available for most computers. The version used in the examples is running on a Digital Equipment Corp. PDP 11/34.

Although these macros generate instructions for a specific microprocessor, the ideas behind their design may be used to develop similar macros for other processors. Even if a macro assembler is not available, the ideas presented here can be used in machinelanguage programs to increase reliability and decrease development time.

Implementation

In this section we shall discuss the actual implementation of the macros, as well as some other aspects of structured programming not required by the use of the macros.

Macros, while allowing the program to have a structure similar to Pascal, do not actually require the same format (PROGRAM, CONSTANT, VARIABLE, FUNCTION, PROCEDURE, main program). However, most programs are easier to understand with that structure, so all the examples will use it.

The Stack Machine

These macros and programs written using the macros define the 6809-based computer as a stack-oriented machine. The 6809 makes this implementation easy because of its two stack registers and many addressing modes that allow stack access.

```
Listing 1 (continued)
        ELSE
                TYPE.EQ.BYTE
ZZZPOS
        SET
                ZZZPOS+1
        LDB
                VALUE
        PSHS
        ELSE
                TYPE.EQ.1
ZZZPOS+1
ZZZPOS
        SET
        LDB
                VALUE
        PSHS
        ELSE
        ΤF
                TYPE.EQ.WORD
ZZZPOS
        SET
                ZZZPOS+2
        LDD
                VALUE
        PSHS
        ELSE
                TYPE.EQ.2
        ΙF
        SET
ZZZPOS
                ZZZPOS+2
        LDD
                VALUE
        PSHS
        ELSE
                TYPE.EQ.REF
        ΙF
ZZZPOS
        SET
                ZZZPOS+2
        TFR
                II.n
        ADDD
                #VALUE
        PSHS
        ELSE
        ERROR
                INVALID ARGUMENT
        ENDIF
        ENDIF
        ENDIF
        ENDIF
        ENDIF
        ENDIF
        ENDM
        PAGE
CALL - A MACRO TO CALL A SUBROUTINE WHILE PASSING UP TO
                SIX PARAMETERS. EACH PARAMETER MUST BE ENCLOSED WITH < > AND CONTAIN THE PROPER TYPE.
                       CALL TEST, <RAM, WORD>, <ROM, BYTE>
*********
                ********************
CALL
        MACRO
                DEST, P1, P2, P3, P4, P5, P6
ZZZPOS
        SET
        ΙF
                NARG.GT.1
        PARM
        ΙF
                NARG.GT.2
        PARM
                NARG - GT - 3
        PARM
        IF
                NARG.GT.4
        PARM
                F 4
                NARG.GT.5
        IF
        PARM
         TE
                MARG.GT.6
        PARM
                23
        ENDIF
        ENDIF
        ENDIF
        ENDIF
        ENDIF
        ENDIF
         JSR
                 DEST
                 777F0S
         TF
         LEAS
                ZZZPOS,S
         ENDIF
        ENDM
        PAGE
               ***********************
               A MACRO TO DEFINE VARIABLES USED THROUGHOUT A PROGRAM.
                UF TO SIX VARIABLES MAY BE DEFINED AT ONCE. EACH
                VARIABLE MUST BE DEFINED AS A <NAME, LENGTH> PAIR.
                                <FIRST, BYTE>, <FLAG1, WORD>
        *****
                          ******************
 プリムR
         MACRO
                LBL,SIZE
 LBL
         RES
                SIZE
        ENTIM
 VAR
         MACRO
                 T1, T2, T3, T4, T5, T6
                 NARG.GE.1
        7UAR
                 T1
        ENDIF
                 NARG.GE.2
         ΊF
         ZVAR
         ENDIF
                 NARG.GE.3
         ENDIF
```

```
Listing 1 (continued)
                NARG.GE.4
        ZVAR
        ENDIF
                 NARG.GE.5
        ZVAR
        ENDIF
        ΙF
                NARG.EQ.6
        ZVAR
        ENDIF
        ENDM
        PAGE
        A MACRO TO DEFINE PROGRAM CONSTANTS. UP TO SIX
                CONSTANTS MAY BE DEFINED AT ONCE USING THE STANDARD
                                                                         *
                <NAME, VAL> PAIR DEFINITIONS.
                                 <TRUE, #1>, <FALSE, #0>, <MAYBE, #3>
                **********************************
ZCONS
        MACRO
                LBL, VAL
        EQU
                VAL
LBL
        ENDM
CONS
        MACRO
                T1,T2,T3,T4,T5,T6
                NARG.GE.1
        ZCONS
        ENDIF
        ΙF
                NARG.GE.2
        ZCONS
        ENDIF
        IF
ZCONS
                NARG.GE.3
        ENDIF
                NARG.GE.4
        ZCONS
        ENDIF
                NARG.GE.5
        ZCONS
        ENDIF
                NARG.EQ.6
        1F
        ZCONS
        ENDIF
        ENDM
        PAGE
       **********************
*
                A MACRO TO DEFINE PROCEDURE CONSTANTS.
                CONSTANTS MAY BE DEFINED AT ONCE USING THE STANDARD <NAME, VAL> PAIR DEFINITIONS. THESE CONSTANTS ARE
×
                VALID ONLY FOR THE LIFE OF THE PROCEDURE.
                                <TRUE, #1>, <FALSE, #0>, <MAYBE, #3>
******
               **********************************
ZLCONS
       MACRO
                LBL, VAL
LBL
        SET
                VAL
        ENDM
LCONS
        MACRO
                T1,T2,T3,T4,T5,T6
        ΙF
                NARG.GE.1
        ZLCONS
        ENDIF
                NARG.GE.2
        ZLCONS
        ENDIF
        IF
                NARG.GE.3
        ZLCONS
        ENDIF
                NARG.GE.4
        ΙF
        ZLCONS
        ENDIF
                NARG.GE.5
        IF
        ZLCONS
        ENDIF
        IF
ZLCONS
                NARG.EQ.6
                T6
        ENDIF
        ENDM
        FAGE
        PROC - A MACRO TO DEFINE PROCEDURE (SUBROUTINE) ENTRY POINTS
COMPLETE WITH UP TO SIX PARAMETERS. NOTE IF DEFINED
WITH X PARAMETERS EACH CALL MUST USE X ALSO.
                                                                         *
                                                                         ×
           N O T E - 1. THESE PARAMETERS ARE REFERENCED BY <PARM>+U
                                                                         *
                        IF THE PROCEDURE NEEDS TEMPS THEY SHOULD BE ALLOCATED USING THE LVAR MACRO.
                                                                         *
                       2. REGS X & Y ARE AUTOMATICALLY SAVED
```

All data in the system, except for constants and variables defined in the main body of a program, are stored on the stack. Each procedure or function has the capability of receiving up to six passed parameters each time it is used. Procedures and functions also have a reserved area on the stack for their own use while active. When a procedure or function is finished, the area on the stack is given up so that the next subroutine may use it. This keeps the amount of ''dedicated-all-the-time, used-some-of-the-time" memory to a minimum. Use of the stack also avoids the problems caused by using a common variable at the wrong time.

Register Usage

The Motorola 6809 has five 16-bit registers, two 8-bit registers, and one 16-bit accumulator that may be used as two 8-bit accumulators. The registers and their function within the defined stack machine follow:

16-bit

PC — program counter. Points to the next instruction to execute, and is only modified by the jump to subroutine, return from subroutine, and the various branch instructions.

S — system stack pointer. Must be initialized at the start of a program. This register should not be changed by the user program as it contains the necessary information to switch between subroutines.

U — user stack pointer. Data that is passed to a called subroutine may be found in this stack, and is referenced by an offset from this register. The value stored in U should never be changed by the user; the macros CALL and RETURN will alter U as necessary.

X — a general purpose register available to the user program. This register is saved and restored by the macros.

Y — a general purpose register available to the user program. This register is saved and restored by the macros.

8-bit

DP — direct page register. Unused by the stack machine.

CC — condition code register. This contains flags pertaining to the results of the last instruction. This register is not saved by the macros.

(continued)

Accumulator D (A and B)

Accumulators A and B are each 8 bits. Accumulator D is 16 bits, consisting of the A and B accumulators. These registers are considered temporary and will be modified by the stack machine whenever a subroutine is called.

Reserved Symbols

The following symbols are restricted to reference only, and must not be redefined by the user's program:

BYTE used to represent a length of 8 bits.

WORD used to represent a length of 16 bits (2 bytes).

REF used when passing parameters between subroutines.

REGA used to pass the machine register A.

REGB used to pass the machine register B.

REGD used to pass the machine registers A and B.

REGX used to pass the machine register X.

REGY used to pass the machine register Y.

The Macros

The following paragraphs describe the available macros, their uses, calling parameters, and an example.

PROG xxxx

PROG: This macro expects one parameter that will name the program. The macro causes a jump to the symbol BE-GIN, which should be placed on the first line of the main body of the program. ex. PROG TEST

CALL xxx, <P1, TP>,..., <P6, TP>

CALL: Performs the jump to subroutine xxx after all the optional passed parameters are placed on the stack. ex. CALL COPY, <FROM, WORD>, <TO, WORD>, <LEN, BYTE>

VAR <N1,TP>,..., <N6,TP>

VAR: Defines variables that will be used throughout the program, not just within subroutines.

ex. VAR <FLAG,BYTE>,<TEMP1, WORD>

CONS <N1,V1>,..., <N6,V6>

```
Listing 1 (continued)
                   ZZZPOS
i Bi
         SET
ZZZPOS
                   ZZZPOS+SIZE
         SET
         ENDM
SAVE
         EQU
PROC
                   NA,F1,F2,F3,F4,F5,F6
         MACRO
NA
                                               PROCEDURE NAME
         EQU
ZZZFNC
         SET
                                               TELL RETURN NO PARMS TO PASS
777ENV
ZZZSAV
         SET
         T F
                   NARG.GT.1
                                               PASSING PARAMETERS ?
         P'SHS
                   U,X,Y
                                                         YES - SAVE X,Y,U RESET S
         LEAU
ZZZENU
         SET
                                                    SHOWTHAT THEY MUST BE CLEANED FROM THE STACK.
777P0S
         SET
         1F
                   NARG.GT.2
                   NARG.GT.3
         IF
         15
                   NARG.GT.4
         ΙF
                   NARG.GT.5
                   NARG.GT.6
         FPAR
         ENDIF
         FPAR
                   F5
         ENDIF
         FPAR
         ENDIF
         FFAR
                   F3
         ENDIF
         FPAR
                   F2
         ENDIF
         FPAR
                   F1
         ELSE
                                             NO PARAMETERS PASSED - SAVE X,Y
         PSHS
                   X . Y
         ENDIF
ZZZPOS
         SET
         ENDM
         PAGE
      ************************************

    A MACRO TO DEFINE PROCEDURE (SUBROUTINE) ENTRY POINTS
COMPLETE WITH UP TO SIX PARAMETERS. NOTE IF DEFINED

       FPROC
                   COMPLETE WITH UP TO SIX PARAMETERS. NOTE IF DEFINED WITH X PARAMETERS EACH CALL MUST USE X ALSO.
             N O T E - 1. THESE PARAMETERS ARE REFERENCED BY <PARM>,U

IF THE PROCEDURE NEEDS TEMPS THEY SHOULD BE
                            ALLOCATED USING THE LVAR MACRO.
                           2. REGS X & Y ARE NOT SAVED
 ***********
                                                           *******
FPROC
         MACRO
                   NA,F1,F2,F3,F4,F5,F6
NA
         EQU
                                                PROCEDURE NAME
ZZZFNC
                                                TELL RETURN NO PARMS TO PASS
         SET
ZZZENV
          SET
ZZZSAV
         SET
                                                NOT SAVING REGS X,Y
                   NARG.GT.1
                                               PASSING PARMS?
YES - SAVE U
NO - DO NOTHING
          PSHS
          LEAU
                   0,5
ZZZENV
                                                         THAT THEY MUST BE CLEANED FROM THE STACK.
         SET
ZZZPOS
          SET
          ΙF
                   NARG.GT.2
          IF
                   NARG.GT.3
          ΙF
                   NARG.GT.4
          ΙF
                   NARG.GT.5
          ΙF
                   NARG.GT.6
         FPAR
         ENDIF
          FPAR
                   F5
          ENDIF
          FPAR
                   F4
          ENDIF
                   F3
          ENDIF
                   F2
          FPAR
          ENDIF
          FPAR
                   F1
          FNDIF
ZZZF'OS
          SET
          PAGE
        ************************
        FUNC - A MACRO TO DEFINE FUNCTION (SUBROUTINE) ENTRY POINTS COMPLETE WITH UP TO SIX PARAMETERS. NOTE IF DEFINED WITH X PARAMETERS EACH CALL MUST USE X ALSO.
                  BY DEFINITION A FUNCTION RETURNS A SINGLE VALUE.
             N O T E - 1. THESE PARAMETERS ARE REFERENCED BY <PARM>,U

IF THE FUNCTION NEEDS TEMPS THEY SHOULD BE
ALLOCATED USING THE LVAR MACRO.
                           2. REGS X & Y ARE SAVED.
****
        FUNC
         MACRO
                  NA, F1, F2, F3, F4, F5, F6
         EQU
                                               FUNCTION NAME
```

```
Listing 1 (continued)
ZZZFNC
                                              TELL RETURN NO PARMS TO PASS
         SET
ZZZSAV
                  0
         SET
         İF
                  NARG.GT.1
                                              PASSING PARAMETERS ?
         PSHS
                                                        YES - SAVE X,Y,U RESET S
         LEAU
                  4.5
ZZZENV
         SET
                                                        THAT THEY MUST BE CLEANED
ZZZPOS
         SET
                                                        FROM THE STACK.
                  NARG.GT.2
         ΙF
                  NARG.GT.3
         IF
                  NARG. GT. 4
         ΙF
                  NARG.GT.5
         ĮF
                  NARG.GT.6
         FPAR
                  F6
         ENDIF
         FFAR
                  F5
         ENDIF
                  F4
         ENDIF
         FPAR
                  F3
         ENDIF
         FPAR
                  F2
         ENDIF
         FPAR
                  F1
         ELSE
                                                        NO - SAVE X.Y
         PSHS
         ENDIF
ZZZPOS
         SET
                  -4
         ENDM
************************************
              - A MACRO TO DEFINE FUNCTION (SUBROUTINE) ENTRY POINTS
                  COMPLETE WITH UP TO SIX PARAMETERS. NOTE IF DEFINED WITH X PARAMETERS EACH CALL MUST USE X ALSO.
                  BY DEFINITION A FUNCTION RETURNS A VALUE.
                            THESE PARAMETERS ARE REFERENCED BY <PARM>,U
IF THE FUNCTION NEEDS TEMPS THEY SHOULD BE
ALLOCATED USING THE LVAR MACRO.
                             REGS X & Y ARE NOT SAVED
*****
                   **********************
FFUNC
         MACRO
                  NA,F1,F2,F3,F4,F5,F6
         EQU
                                              FUNCTION NAME
ZZZFNC
                                               TELL RETURN NO PARMS TO PASS
ZZZENU
         SET
ZZZSAV
         SET
                                              NOT SAVING REGS X.Y
                   NARG.GT.1
                                              PASSING PARMS?
         ΙF
                                                       YES - SAVE U
NO - DO NOTHING
THAT THEY MUST BE CLEANED
         PSHS
         LEAU
                   0.5
ZZZENU
         SÉT
                                                        FROM THE STACK.
         SET
                   NARG.GT.2
         TF
                   NARG.GT.3
                   NARG.GT.4
         IF
         ΙF
                   NARG.GT.5
         ΙF
                   NARG.GT.6
         FPAR
                   F'6
         ENDIF
                   F5
         FPAR
         ENDIF
                   F4
         FPAR
         ENDIF
         FPAR
                   F3
         ENDIF
         FPAR
                   F2
         ENDIF
                   F1
         FPAR
         ENDIF
ZZZPOS
                   C
         ENDM
         PAGE
**************************
                - A MACRO TO DEFINE TEMPORARY VARIABLES FOR A PARTICULAR PROCEDURE OR FUNCTION. THE TEMPS ARE ALLOCATED ON THE SYSTEM STACK AND ARE REFERENCED BY <TEMP>.U. THESE
                   VARIABLES ARE DISTROYED UPON EXIT OF THE PROCEDURE.
                  FX.
                                       <T1,BYTE>,<T2,WORD>
                            LVAR
                            LDD
****
                  *************
         *****
LIPAR
         MACRO
                  LBL,SIZE
                   ZZZPOS-SIZE
ZZZPOS
         SET
LBL
         SET
                  ZZZPOS
                                                                           (continued)
```

CONS: Defines constants that will be used throughout the program.

ex. CONS <TRUE,1>, <FALSE,0>, <MAYBE,3>

LCONS <N1,V1>,...,<N6,V6>

LCONS: Defines local constants that are used within the current subroutine definition only.

ex. LCONS <TAB,9>, <CRLF,13>

PROC xxx, $\langle P1, TP \rangle$,..., $\langle P6, TP \rangle$

PROC: Defines a procedure (subroutine). PROC must be followed by a RETURN before the next PROC or FUNC macro usage. xxx gets assigned to the entry point of the procedure. The parameters P1-P6 get assigned as offsets on the stack to allow referencing of the passed parameters at execution.

ex. PROC COPY, <IN, WORD>, <OUT, WORD>, <LEN, BYTE>

FUNC xxx, <P1, TP>,..., <P6, TP>

FUNC: Defines a subroutine that returns a value (function). This macro, like PROC, must be followed by the RETURN macro before the next FUNC or PROC macro is used. The name xxx is assigned to the entry point of this subroutine and the labels P1-P6 are assigned as offsets to the stack for access to the passed data during execution. The results of the function are returned in machine register D.

ex. FUNC SQR, < VAL, WORD>

LVAR <N1,TP>,..., <N6,TP>

LVAR: Defines up to six local variables at one time. These variables can only be referenced within the definition of this defining subroutine. Subroutines called from within defining subroutines may reference these variables only if passed to the subroutine as a call parameter. LVAR, at this time, requires parameters to be passed to the defining subroutine to function properly.

LVAR <TEMP1,BYTE>, <TEMP2, WORD>

RETURN data

RETURN: Ends the previous FUNCtion or PROCedure definition. It removes temporary variables as well as passed parameters from the stack, restores the saved registers, and returns to the calling program. If the RETURN is from a function, it also places the returned value in the machine register D. ex. RETURN RESLT (from a function) ex. RETURN (from a procedure)

The author may be contacted at On-Going Ideas, RD #1, Box 810, Starksboro, Vermont 05487.

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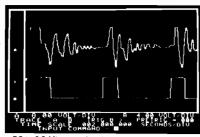
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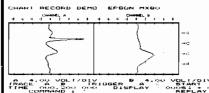


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Listing 1 (continued)

ENDIF **ENDIF** ENDIF

TF

FNITE

*		
LVAR	MACRO	V1,V2,V3,V4,V5,V6
	IF	ZZZENV.EG.O
ZZZENV	SET	-1
	PSHS	U
	LEAU	0,S
ZZZPOS	SET	0
	ENDIF	
	IF	NARG.GE.1
	DPAR	V1
	IF	NARG.GE.2
	DPAR	V2
	IF	NARG.GE.3
	DPAR	V3
	IF	NARG.GE.4
	DPAR	V4
	IF	NARG.GE.5
	DPAR	V5
	IF	NARG.GE.6
	DPAR	V6.
	ENDIF	
	ENDIF	777000 U
	LEAS	ZZZPOS,U
	ENDM	
	PAGE	

RETURN - A MACRO TO DEALLOCATE PASSED PARAMETER STACK SPACE UPON THE COMPLETION OF A FUNCTION OR PROCEDURE.

N O T E - FUNCTION WILL RETURN A VALUE IN REGA OR REGD THIS VALUE IS SPECIFIED IN THE RETURN CALL.

REGA ?

RETURN <T1,U>,WORD

*****	*****	*********	************************
RETURN	MACRO	VAL, TYPE	**************************************
	IF	ZZZFNC.EQ.1	FUNCTION RETURN?
	IF	NARG.EQ.0	YES - BUT NO PARMS(ERROR)
	ERROR	FUNCTION MUST	RETURN A VALUE!!!!!
	ELSE		
	IF	NARG.EQ.1	YES - MAYBE A REG IS RETURNED
	IF	VAL.EQ.REGA	REGA ?

4=4 ELSE IF VAL.EQ.REGD REGD ? D=DELSE

VAL.EG.REGB REGB ? TFR R.A A=B ELSE ΙF VAL.EQ.REGX REGX ? TFR D=X

ELSE VAL.EQ.REGY ΙF REGY ? TFR Y,D B=Y ELSE

FRROR INVALID RETURN ARGUMENT ENDIF ENDIF

ELSE NARG.NE.2 MORE THAN 1 RETURN ARG? ERROR INVALID RETURN ARGUMENT

ELSE RETURNING A BYTE? IF TYPE.EQ.BYTE LDA VAL A=VAL

ELSE IF TYPE.EQ.WORD RETURNING A WORD?

LDD D=VAL ELSE FRROR INVALID RETURN ARGUMENT

ENDIF ENDIF ENDIF

ENDIF ELSE TF NARG.NE.O ERROR PROCEDURES DO NOT RETURN VALUES!!! ENDIF

END OF SPECIAL FUNCTION RETURN LOGIC

Listing 1 (continued)

```
ZZZFNC SET
        IF
LEAS
               ZZZENV.LT.0
               0,U
        PULS
               U
        IF
               ZZZSAV
        PULS
        ENDIF
        ELSE
               ZZZENV.GT.O
        IF
               ZZZSAV
        LEAS
               -4,U
        PULS
        ELSE
        LEAS
               0.0
        PULS
        ENDIF
        ELSE
               ZZZSAV
        PULS
               X.Y
        ENDIF
        ENDIF
        ENDIF
        RTS
        ENDM
        PAGE
****************************
       RECORD DEFINITION MACROS
**********
             *************************
RECORD
       MACRO
ZZZPOS
       SET
       ENDM
FIELD
       MACRO
              NAME, SIZE
NAME
       EQU
              ZZZPOS
ZZZPOS
              ZZZPOS+SIZE
       SET
       ENDM
RECSIZ
       MACRO
SIZE
       EQU
              ZZZPOS
       ENDM
       PAGE
TABLE GENERATION MACROS
                                                                 *
              EACH TABLE MUST CONSIST OF:
                     TABLE - TABLE HEADER (1)
TABDTA - TABLE DATA ENTRIES (N)
TABSIZ - TABLE LENGTH AND END MARKER (1)
**********************
       MACR
TABLE
       SET
ZZZTBL
              0
       ENDM
TARRTA
       MACR
              UAL LIF . TYPE
              TYPE.EQ.BYTE
       DATA
              VALUE
ZZZTBL
       SET
              ZZZTBL+1
       ELSE
              TYPE.EQ.WORD
       IF
       ACON
              VALUE
ZZZTBL
       SET
              ZZZTBL+2
       ELSE
ERROR
              UNSPECIFIED DATA TYPE
       ENDIF
       ENDIF
       ENDM
TABSIZ
       MACR
              SIZE
SIZE
       EQU
              ZZZTBL
       DATA
       ENDM
       PAGE
STORE A STRING (MUST BE USED WITHIN A PSCT, OUTSIDE EXECUTABLE CODE)
       THE PARAMETER IS THE STRING ENCLOSED IN SINGLE QUOTES, AS WELL AS ANGLE BRACKETS, IF NECESSARY.
********
MAKSTR
      MACRO
              XS
ZZZSVS
       SET
              *+1
       ORG
              XS
       FCC
ZZZSTR
       SET
       DRG
              ZZZSVS
              ZZZSTR-ZZZSVS-1
       FCB
       ORG
       ENDM
       PAGE
                                                              MICRO
```

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CX853 16K RA	٩N	١.	•	٠.	•	•	• •	•	• •	•	٠	٠	•	•	٠	٠	•	•		•	•	•	٠	•	٠	٠	٠	•	2	7	۲.	95

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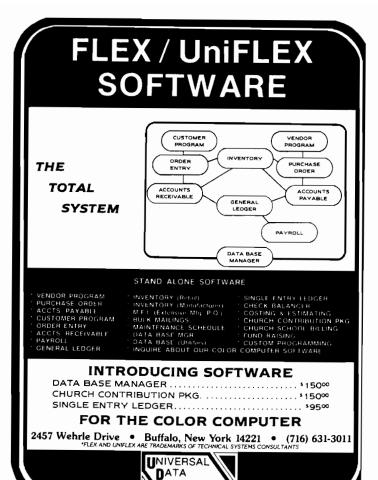
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Market Projection Program for the Color Computer

by Leonard I. Suckle

This sophisticated business program is implemented on an inexpensive Color Computer system. It may be modified for other computers.

Market Projection

requires:

TRS-80 Color Computer with 16K It does *not* require Extended Color BASIC

The introduction of low-cost personal computers has placed the capability of huge computational powers within the economic reach of nearly everyone. Unfortunately, many of the lower-priced systems are being promoted or received as either toys or games.

The program described in this article is an example of how the Radio Shack Color Computer may be used in an actual business application. The computer used here is the 16K version without the Extended BASIC ROM. It may be purchased for under \$500. The addition of a line printer and a cassette tape recorder add approximately \$300 to provide for a complete, versatile system.

The program is written in BASIC and may be used on any compatible system. Variations could easily be made to customize it to the users' specific needs. These applications should substantiate that the use of low-cost personal computers in business should not be overlooked.

Program Description

Making projections on industry and company sales, when a multitude of product lines are involved, can be a long and tedious task. A single change in value usually results in complete recalculation of the other parameters for each year of the projection. Knowledge of the required changes causes

either a conscious or subconcious hesitation in the modification of data to provide more realistic interpretation of the market conditions of the future.

The intent of this computer program is to alleviate the tedious calculation and recalculation associated with revisions. With the capability of making changes and seeing their immediate effect on the rest of the data, more accurate decisions may be made in industry planning functions.

The market projection computer program evaluates a market and share of the market by year and by each of many product categories. The market parameters evaluated for each of the product categories are shown in table 1. The computer refers to each of these parameters by their item # (1 through 9) shown in the table and in figure 1 in brackets

The total of all columns of data will be calculated upon request. Furthermore, a top-down summation value may be entered for TAM and NSB, and the difference between the actual and top-down summations will be displayed. Upon request, the computer will adjust the TAMs and NSBs, based on product mix, so they equal the top-down value.

Entering and working with two years of data at a time allows you to make projections and/or calculate compound growth rates. Data from one year may be projected by annual compound growth rate (%) to the other year in memory. Select any of the nine parameters for projection. Calculations will be done either on a product category basis or for all product categories. If data is entered for both years, the computer can calculate the growth rate of any data on an annual compound percentage.

Table 1: Market Parameters

- 1. Company Market Share (NSB/TAM) in %
- 2. Total Available Market (TAM) in millions of \$
- 3. Industry Product Mix [TAM of specific product/Total TAM] in %
- 4. Industry Quantity in millions of units
- 5. Industry Average Selling Price (ASP) in \$
- 6. Company Sales (NSB) in millions of \$
- 7. Company Product Mix (NSB of specific product/Total NSB) in %
- 8. Company Quantity in millions of units
- 9. Company Average Selling Price (ASP) in \$

Data may be saved and loaded by year from cassette. Two years of data may be stored in memory for entry and calculation. Within any one of the two years, data may be calculated based on other data entered. This may be done on a product category basis, or for all product categories. For example, if the units and ASP for industry have been entered, the program will calculate the TAM. For expediency of operation, calculations are made only when requested, rather than whenever new data is entered.

Video Display Characteristics

Data for each of the two years stored in memory is presented in an array of pages for each year on the TV display of the Color Computer. These pages are arranged in a $3 \times N$ matrix, where N is dependent upon the number of product categories.

Three vertical pages can display a maximum of 29 product categories. This example is shown in figure 1. The product categories are numbered and



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listed along the left side of the display, and the market parameters and their item numbers are displayed across the top.

The two years stored in memory are defined as the primary and secondary years, and are located on pages 1 through 3, and 4 through 6, respectively. When in the display mode of the program, the page being displayed may be changed by direct numerical entry of the desired page, or by indexing left, right, up, and down (see DISPLAY).

Printer Output

Data from either the primary or secondary year may be output to a printer connected to the Color Computer. The Quick Printer II interfaces directly, and provides a 32-character line width. The program prints the data within the year last displayed on the video. All pages of the displayed year will be printed independent of the page shown on the video display.

Operation

The program is loaded from cassette tape into memory using the CLOAD command. Sufficient memory must be allocated to the program (13,700 bytes]. Although the program was written with standard ROM, it will also operate with an extended ROM computer, but a comparable amount of memory must be freed to permit operation. Extended BASIC will not free more memory than that amount created by the PCLEAR 1 command. This is not enough memory for this program, so an additional page of memory must be freed prior to loading. This may be done by entering the following command:

POKE 25,6 (enter)

After the program is loaded, entering RUN initiates operation. The screen will clear and the program enters MAIN CONTROL. The program is initiated with all data cleared, primary year = 1981, secondary year = 1986, and the display page pointer set at 1,(1).

MAIN CONTROL

MAIN CONTROL is the principal operating routine from which all other routines may be activated. Operation is indicated by an inverted-screen (dark background) "M" in the lower left of the display, preceding the cursor.

Commands available from MAIN CONTROL are shown in table 2. The

Figure 1: Video Display Pages 1 through 3 for Primary Year (Secondary Year Pages are 4 through 6). Vertical Page is specified by "shift (n)", where n=1 to 3. Item #'s are shown in brackets and are described in table 1.

Year PRODUCT LINE 1 2 3	NSB/TAM (1)	TAM (2) 1 .				MIX-NUNITS (7) (8)		(1)
it	PG1	. 11		PG2	n		PG3	
Year PRODUCT LINE 12	NSB/TAM (1)	TAM (2) 12	MIX-T UNITS (3) (4)			MIX-N UNITS (7) (8)		(2)
22	PG1	22		PG2	·		PG3	
Year PRODUCT LINE 23			MIX-T UNITS (3) (4)		NSB	MIX-NUNITS (7) (8)	(9)	(3)
29 SUMMATION 30 TOP DOWN DELTA	TOTAL	29 SUM 30 DELTA	.	PG2	29 SUM 30 DELTA		PG3	

"enter" key must be used when operating in this routine.

DISPLAY

The display routine outputs the data for the primary or secondary years stored in memory to the video display. Operation in this routine is indicated by an inverted-screen "D" in the lower left of the display.

As previously described under Video Display Characteristics, each year is displayed in a 3 × N matrix of pages. Any page may be immediately displayed by entering the horizontal page number directly, and the vertical page number using the shift key. The display may also be changed by using the arrow of the appropriate direction (up, down, left, right). The "enter" key is not needed with entries from DIS-PLAY, and all actions will occur im-

mediately upon depression of the appropriate key.

Commands available from DIS-PLAY are shown in table 3.

ENTRY

The entry routine permits entry of data into the designated column of the year last displayed on the video. An inverted-screen "E" is displayed in the lower left of the video screen to indicate operation in this routine. The "enter" key is required for entries in this routine.

Upon entering this routine, the display will prompt the user with the request:

ROW(* OR YEAR), ITEM #(*), VALUE [OR 'YR',,YEAR]

(Continued on page 71)

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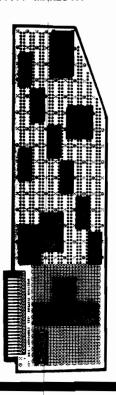
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Data Entry Options

a. Entry or change of specific data within the displayed year: Enter the location of the new data (row # shown at left of screen, item # shown at top of screen), followed by the new value of data. For example, to enter a value of 10 for TAM (item #2) of product category #3, enter:

3,2,10 (enter)

b. Entry or change of all values of a specific item # within the displayed year: Enter an "*" for the row, followed by the item # and a comma. The program will then prompt the user for the desired data value for each row (product category) by displaying the name of the product category, and the present value (PV). Three possible entries of data may be made: 1. the new value, 2. enter, which is accepted as a value of zero, and 3. "/", which retains the present value.

Example of entering data for TAM for all product categories:

*,2, (enter)

c. Entry or change of all values of data for a specific product category (row): Enter the row #, followed by an "*" for the item #, followed by a comma. The program will then prompt the user to enter data for each of the nine items for the specific product category designated. A value must be entered for each item request. The "enter" key with no preceding value is interpreted as a value of zero.

Example of requesting entry for all items for product category #14:

14,*, (enter)

d. Change of primary or secondary year: The value of the years for primary and secondary display pages is initialized at 1981 and 1986, respectively. Either of these years may be changed to any other year value. The display should be set to the year to be changed (pages 1 through 3 for primary year, pages 4 through 6 for secondary year). The enter command may then be used, as shown in the following example, which changes the year to 1989:

YR,,1989 (enter)

When any of the entry requests are completed, the program will always return to DISPLAY.

CALCULATE

The calculate routine which generates data within the displayed year, projects data from the primary year to the secondary year using an entered annual compound growth rate, or redistributes either the TAM or NSB of the displayed year to equal an entered top-down summation. The annual compound growth rate between items in the primary and secondary years may also be calculated.

Operation in this routine is indicated by an inverted-screen "C" in the lower left of the display. Entry into CALCULATE may be performed from either MAIN CONTROL or DISPLAY, but exit is always to DISPLAY.

Upon entry to CALCULATE, the display will prompt the user with the following request:

DATA,RATE,PROJCTN,SPREAD? (continued)

Table 2: Commands Available fro	m MAIN CONTROL
---------------------------------	----------------

Command	Routine	Action
D	DISPLAY	Program enters video display routine.
E	ENTER	Program enters data entry routine.
С	CALCULATE	Program enters calculate routine, where new data is determined based on previously entered data.
S		Calculates sums of all columns within the year that is presently displayed on video.
CLR	CLEAR	Permits clearing data in either primary or secondary year, as specified.
R		Displays previously calculated compound growth rates on video screen (see CALCULATE).
P		Data within vertical page last displayed is output to the printer, as described in PRINTER OUTPUT.
RP		Outputs previously calculated compound growth rates to the printer (see CALCULATE).
LD	LOAD	Loads yearly data previously stored on cassette tape.
SAVE	SAVE	Saves primary or secondary year data on cassette tape.
HEADER		Cassette tape is prepared to receive data for a year not previously stored. See "Tape Initialization" under SAVE.

Table 3: Commands Available from DISPLAY

Command	Routine	Action
R		Same as command described under MAIN CONTROL
E	ENTER	Same as above
С	CALCULATE	Same as above
P		Same as above
S		Same as above
+		Special Sum — calculates sums of columns between specific rows (product lines) designated by user.
Q		Return to MAIN CONTROL.

The user response must be followed by an "enter" key.

Calculate Options

D Data: Calculates data within the displayed year based on existing data. Entry into this subroutine results in the following request on the display:

ROW(*), ITEM #?

The location of the desired parameter is entered using the row # shown on the left of the screen and the item # shown at the top of the screen. If an entire column is to be calculated, an * may be used in place of the row #. For example, to calculate the value of industry units (item 4) based on TAM and ASP, enter the following:

*,4 (enter)

The calculations for TAM and NSB may be based on a variety of constants. Whenever a request for these calculations is made, the computer requests more information:

CONSTANT (3), (4)&(5), (1)?

The item # to be held constant is entered using the "enter" key, and the calculation is performed.

D134 Data: Automatically performs a series of calculations comparable to "D" for items 1, 3, and 4 for all product categories (rows).

D178 Data: Automatically performs a series of calculations comparable to "D" for items 1, 7, and 8 for all product categories (rows).

R Rate: Calculates the annual compound growth rate between primary and secondary year data. Entry into this subroutine results in the display of the prompt:

ROW(OR "*"),ITEM?

The growth rate for a particular product category (row) and item may be entered, or all product categories for the specific item may be calculated by using "*" for the row #. The algorithm for calculating the compound growth is relatively slow, so an indication of which row is being calculated is shown to indicate that the computer is operating. At the completion of the calculation, the value(s) are shown on the display as:

YEAR(1) PRODUCT LINE RATE(%) YEAR(2)

Appendix A

```
1 CLEAR
     MPP
10 '
      L.I.SUCKLE
12 ' 12/80
15 '
      REV: 10/19/81
   ' INITIALIZATION
25 *****ENTER NUMBER OF PRODUCT CATEGORIES IN LINE 30
30 PC=6
35 PT=PC+1:PM=INT((PC+3)/11)+1
40 DIM T(PT,18),H16(9),H26(9)
45 DIM PL6(PT),SM(18),RT(PT)
50 DIM TM(4)
60 H14="PRODUCT CAT."
90 DATA NSB/TAM, TAM, MIX-T, UNITS, ASP
100 DATA NEB, MIX-N, UNITS, ASF
120 DATA % (1),(2),(3),(4),(5)
130 DATA (6),(7),(8),(9)
160 FOR N=1 TO 9
180 READ H1$(N)
200 NEXT N
220 FOR N=1 TO 9
240 READ H2$(N)
260 NEXT N
275 PG=1:PP=1
280 Y1=1981
290 Y2=1986
300 YR=Y1
320 SL=32*13
330 *****ENTER THE NAMES OF PRODUCT CATEGORIES IN LINES 340-400
340 DATA WIDGETS, THINGOTS, DIPSIES, DOODLES, WHATCHA, MACALLETS
410 FOR N=1 TO PC
420 READ PL$(N)
430 NEXT N
440 FX=10
450 BOSUB 9000
470 PL# (PT) = "TOP-DOWN TOTALS"
480 JF=0
1000 REM MAIN CONTROL
1010 PRINTOSL
1020 PRINT98L, "m";
1040 INPUT 20$
1055 @#=LEFT# (@0#, 1)
1060 IF Q8="D" THEN 2000
1070 IF Q8="E" THEN 7000
1080 IF Q0$="8AVE" THEN 4000
1090 IF Q08="LD" THEN 5000
1095 IF Q08="CLR" THEN 11000
1100 IF Q#="C" THEN 6000
1110 IF Q#="P" THEN 8000
1120 IF QOS="HEADER" THEN 10000
1125 IF QO$="RP" THEN 2700
1130 IF Q0="R" THEN L1=1;L2=29;GOSUB 6340
1140 IF Q0="8" THEN GOSUB 2500
1999 GOTO 1020
2000 REM LIST ROUTINE
2020 CLS
2025 IF PG>3 THEN YR=Y2 ELSE YR=Y1
2030 DN PS BOTO 2038, 2040, 2050, 2052, 2054, 2056
2038 ST=1:60T0 2060
2040 ST=2: GOTO 2060
2050 ST=6:80T0 2060
2052 ST=10:GOTO 2060
2054 ST=11:GOTO 2060
2056 ST=15
2060 LB=1+11*(PP-1)
2070 IF PC<LS+10 THEN LE-PC ELSE LE-LS+10
2090 PRINTOO.RIGHT# (STR# (YR) . 4)
2100 PRINT 932, "(YR)"
2110 IF PG=1 QR PG=4 THEN 2300
2120 FOR L=4 TO 25 STEP 7
2125 IF PG>3 THEN 81=8-9 ELSE S1=8
2130 PRINTOL," "+H1+(S1)
2140 PRINTOL+32," "+H2$ (S1)
2150 K=64
2155 IF L8>PC THEN 2220
2160 FOR P=L8 TO LE
2170 IF L=4 THEN PRINTOK,P
2175 T=INT(T(P,8) $100+.5)/100
2180 PRINTOL+K.TI
2190 K=K+32
2200 NEXT P
2210 IF PP<>PM THEN 2270
2220 IF L<>4 THEN 2260
2230 PRINTØK+64,PT:PRINTØK+68,T(PT,S)
2240 PRINTØK+96,"DLTA":IF FS=1 THEN PRINTØK+100,
INT(100*(T(PT,8)-8M(8))+.5)/100
2250 PRINT#K+32, "SUM"
2260 IF F8=1 THEN PRINTPL+K+32, INT(SM(S) #100+.5)/100
2270 S=8+1
2290 BOTB 2900
```

Appendix A (continued)

```
2300 PRINT95, H1$
2310 PRINT925, H1$(1)
2330 PRINT957, H2$(1)
2340 K=64
2345 IF L8>PC THEN 2420
2350 FOR P=LS TO LE
2360 PRINTOK,P
2370 PRINT9K+4, PL$(P)
2380 PRINT9K+25, INT(T(P,ST) $100+.5) /100
2390 K=K+32
2400 NEXT P
2410 IF PP<>PM THEN 2900
2420 PRINT@K+36,"SUMMATION"
2430 IF FS=1 THEN PRINT@K+57,INT(SM(8T)*100+.5)/100
2440 PRINTOK+64,PT; " TOP-DOWN TOTAL" 2450 PRINT 0K+100, "DELTA"
2460 BOTO 2900
2470 PRINTOSL, "sum FIRST ROW, LAST ROW";
2480 INPUT JS, JE
2485 GUSUB 2505
2488 PP=3
2490 JF=1
2495 GOTO 2000
2500 PRINTOSL, "CALCULATING SUMS"
2501 JS=1:JE=PC
2502 JF=0
2505 FOR N=1 TO 18 *
2510 SM(N)=0
2520 NEXT N
2530 FOR N=JS TO JE
2532 SM(3)=SM(3)+T(N,3)
2535 SM(7)=SM(7)+T(N,7)
2536 SM(12)=SM(12)+T(N,12)
2537 SM(16)=SM(16)+T(N,16)
2540 FOR J=2 TO 17 STEP 2
2550 SM(J) = SM(J) + T(N, J)
2560 IF J-8 THEN J-9
2570 NEXT J
 2580 NEXT N
2590 FOR J=0 TO 9 STEP 9
2600 IF SM(2+J)<>0 THEN SM(1+J)=100*SM(6+J)/SM(2+J)
2610 IF SM(4+J)<>0 THEN SM(5+J)=SM(2+J)/SM(4+J)
2620 IF SM(8+J)<>0 THEN SM(9+J)=SM(6+J)/SM(8+J)
2635 FS=1
2640 RETURN
2700 REM RATE PRINT
2710 PRINT#-2, TAB(4)H1*(IR);
2710 PRINT#-2, TAB(1)Y1;
2730 PRINT#-2, TAB(18)Y2;
2740 PRINT#-2, TAB(25) "RATE"
2750 FOR N=1 TO PC
2760 PRINT#-2,CHR$(0);
2770 PRINT#-2,N;LEFT$(PL$(N),8);
2773 DR=N:DI=IR:GQ8UB9500
2774 PRINT#-2, TAB(11)D;
2777 DI=IR+9:GOSUB9500
2780 PRINT#-2, TAB(18)D;
2790 PRINT#-2, TAB(25)INT(RT(N)*10+.5)/10
2800 NEXT N
2810 GOTO 1000
2900 PRINT9503, "PG"PG;
2901 PRINTOSL, "d";
2902 IF JF=1 AND PP=3 THEN PRINTOSL, "special sum ROWS"JS; " TO"; JE
2903 PRINTOSL, "d";
2903 PRINTSSL, "d";
2904 X$=INKEY$
2905 IF X$="" THEN 2902
2906 IF X$="" THEN L1=1:L2=PC:GOTO6340
2909 IF X$="" THEN 1000
2910 IF X$="Q" THEN 1000
2911 IF X$="C" THEN 6000
2912 IF X$="C" THEN 6000
2913 IF X$="S" THEN GOSUB 2500
2914 IF X$="P" THEN 8000
2915 X=VAL(X$)
2920 IF X$O AND X<7 THEN PREX
2920 IF X>O AND X<7 THEN PG=X
2925 Y$=CHR$(ABC(X$)+16)
2930 Y=VAL(Y$)
2930 Y=VAL(Y9)
2935 IF Y>O AND Y<=PM THEN PP=Y
2940 IF X=CHR$(9) THEN IF PG<6 THEN PG=PG+1
2945 IF X=CHR$(8) THEN IF PG>1 THEN PG=PG-1
2950 IF X=CHR$(94) THEN IF PP>1THEN PP=PP-1
2955 IF X=CHR$(10) THEN IF PP<PM THEN PP=PP+1
2965 GOTO 2020
3000 REM MULTIPLE CALC.
3010 R#="#"
3020 I=1:GOSUB 6530
3030 I=3:608UB 6530
3040 I=4:G0SUB 6530
3050 GQTQ 2000
3060 R$="#"
3070 I=1:00SUB 6530
3080 I=7:608UB 6530
                                                                                                                (Continued)
```

When the entire column has been calculated, a display page is filled and waits for any key input before displaying the next page. After the last data is displayed, any key entry returns the operation to DISPLAY. The latest rate calculation may be reviewed from either DISPLAY or MAIN DISPLAY by entering "R". The rate data may be output to the printer by entering "RP" from MAIN CONTROL.

P Projection: Calculates a new value of data in the secondary year based on existing data in the primary year and an entered annual compound growth rate in %. Entry into this subroutine results in a displayed prompt:

ROW(* OR S), ITEM, RATE?

The specific row #, and annual rate are entered, followed by the "enter" key. The calculated data is stored in the appropriate location in the secondary year data table. Completion of the calculation returns the program to DISPLAY.

Sample ENTRY:

3,2,10 (enter)

Several options for "row #" are

* Data for all product categories under the specified item # will be calculated. Using an entry, as shown by example:

*,2, (enter)

will result in a display prompt for annual rate inputs in percent for each product category.

S Data for all product categories under the specified item # will be calculated with identical growth rates equal to that value entered. An example for items 2 and 10% is:

S,2,10 (enter)

The growth rate previously calculated with the "R" command under CALCU-LATE may be used in place of an entered value(s) for rate. This is performed by using "R" for the value of the rate. An example of projecting an entire column of data is:

*,2,R (enter)

S Spread: Redistributes the values of either TAM or NSB within the year last displayed so the total is equal to an entered top-down summation. Redistribution is based on product mix (item 3

or 7). Entry into this subroutine yields the prompt:

ITEM # (2 OR 6)?

Either 2 or 6 will be accepted, using the "enter" key, for spreading TAM or NSB, respectively. The program checks for a valid entry of top-down summation, and if one exists, the calculation is performed and the program returns to DISPLAY. If an invalid summation is found (zero), the program requests that a top-down entry be made and returns to MAIN CONTROL (indicated by inverted-screen "M").

CLEAR

Data for either the primary or secondary year may be reinitialized to zero by entering CLR from MAIN CONTROL. The program will prompt with:

CLEAR WHICH YEAR?

The year is entered [using "enter" key] and if it corresponds with either the primary or secondary year, all data in memory for that year will be cleared. If an invalid year is entered, the display will indicate:

YEAR NOT IN DISPLAY

Exit from the routine is to DISPLAY.

LOAD

The load routine retrieves data previously stored on cassette tape and loads it into memory. Access to this routine may only be performed from MAIN CONTROL. Entering LD results in the response:

LOAD WHICH YEAR INTO PAGES 1-3(1) OR 4-6(2)?

Pages 1-3 represent the primary year, and 4-6 the secondary year. For example, if data for 1984 is to be placed into the secondary year memory, the following entry would be made:

1984,2 (enter)

The user is then prompted to "RE-WIND TAPE & PLAY". After the data is loaded, the prompt, "TURN RECORDER OFF", is issued and the program returns to MAIN CONTROL.

Appendix A (continued)

```
3090 I=8:609UB 6530
 3100 GOT02000
 3200 IF VAL(R#)<>0 THEN 3240
 3205 R#="#"
 3210 IF I=IR THEN 6911
 3220 PRINT"CALCULATE RATES FOR ITEM": IR
 3230 BOTO 1000
 3240 Y3=VAL(Rs)
 3250 R$="#"
 3270 GOSUB 6225
 3280 Y2=Y3
 3290 GOT06911
 4000 REM SAVE
4005 DL=200
 4010 PRINTOSL, "BAVE WHICH YEAR",
 4020 INPUT YES
 4022 YS=VAL(YS$)
 4030 IF YSC>Y1 AND YSC>Y2 THEN PRINT "DATA NOT AVAILABLE" | GOTO1000
 4031 GOTO 4070
 4035 IF RIGHT# (Y8#, 1) = "X" THEN 4070
4040 PRINT 98L, "REWIND TAPE & 'PLAY'"
4050 FN#="H"+STR#(YS)
 4060 SKIPF FNS
 4070 PRINTOSL, "SET TO 'RECORD' & ENTER 'READY'"
 4080 INPUT @$
4090 IF Ge<>"READY" THEN 1000
4100 IF YS=Y1 THEN 8F=1:EF=9
4110 IF YS=Y2 THEN 8F=10:EF=18
 4120 OPEN "O",~1, "F"+STR$(Y8)
 4125 RP=1
 4160 FOR N=SF TO EF
 4170 FOR K=1 TO PT
4175 ON RP 60T0 4180,4185
4180 PRINT#-1,T(K,N):G0T04190
 4185 INPUT#-1, T(K, N)
4190 NEXT K
4200 NEXT N
4210 CL08E-1
 4212 ON RP GOTO 4214,4220
4214 MOTORON
4216 FOR T=1 TO DL
4217 NEXT T
 4218 MOTOROFF
4220 CL$
4230 PRINT@96,"TURN RECORDER OFF"
4240 GOTO 1000
5000 REM LOAD
5005 CLS
5010 PRINTOSL, "LOAD WHICH YEAR."
5015 PRINT "INTO PAGES 1-3(1) OR 4-6(2)
5020 INPUT Y8, YP
5030 IF YP<>1 AND YP<>2 THEN 1000
5035 IF YP=1 THEN SF=1:EF=9:Y1=YB
5037 IF YP=2 THEN SF=10:EF=18:Y2=Y8
5040 CLB:PRINTƏSL, "REWIND TAPE & 'PLAY'"
5050 OPEN"I",-1, "F"+STR*(YS)
5060 RP=2
5070 GOTO 4160
6000 REM CALCULATE
6010 PRINTOSL, "c DATA, RATE, PROJETN, SPREAD";
6020 INPUT @8
6022 F8=0
6022 F8=0
6025 IF G8="D134" THEN 3010
6026 IF G8="D178" THEN 3060
6030 G9=LEFT**(G$,1)
6040 IF G8="D" THEN GD8UB 6500
6050 IF G8="R" THEN GD8UB 6200180T0 1000
6060 IF G8="P" THEN 6900
6070 IF G8<"B" THEN 2000
6100 REM SPREAD
6105 PRINTOSL
6110 PRINTOSL, "c ITEM # (2 DR 6)";
6120 INPUT I
6130 IF I<>2 AND I<>6 THEN 1000
6135 11=1
6140 IF PB>3 THEN I=I+9
6142 IF T(PT,I)=0 THEN CLS:PRINT "ENTER TOP-DOWN ";H1*(I1):GOTO 1000
6145 GOBUB 2500
6150 D=T(PT, I)-BM(I)
6160 FOR N=1 TO PC
6165 IF SM(I)=0 THEN 6180
 6170 T(N, I)=T(N, I)+D*T(N, I)/SM(I)
6180 NEXT N
6185 BOSUB 2500
6190 BOTO 2000
6200 REM RATE
6205 PRINTOSL
6210 PRINTOSL, "ROW(OR '#'), ITEMA";
6220 INPUT R$, I
6225 IR=I
6230 DY=Y2-Y1
6240 IF R$="#" THEN L1=1:L2=PC:GOTO 6270
                                                                                         (Continue
6260 L1=R: L2=R
```

```
Appendix A (continued)
6270 FOR N=L1 TO L2
6275 IF T(N,I)=0 THEN 6330
6280 FX=T(N,I+9)/T(N,I)
6295 PRINTOSL, "CALCULATING FOR ROW "; N
6290 GOSUB 9000 'NAT.LN
6300 X=FN/DY
6310 BOSUB 9200'EXP
6320 RT(N)=(EX-1) *100
6330 NEXT N
6340 BOSUB 6360
6345 GOSUB 6420
6350 RETURN
6360 CLS
6370 PRINT@4, H1$ (IR)
6380 PRINT911, Y1
6390 PRINT@18, Y2
6400 PRINT@25, "RATE"
6410 L=32
6415 RETURN
6420 FOR R=L1 TO L2
6430 PRINTOL, R; PL$(R)
6440 PRINTQL+11, T(R, IR)
6450 PRINTQL+18, T(R, IR+9)
6460 PRINTOL+25, INT (RT (R) $10+.5) /10
6470 L=L+32
6472 IF L<480 THEN 6480
6474 Q$=INKEY$
6476 IF Q$="" THEN 6474
6478 GOSUB 6360
6480 NEXT R
6490 RETURN
6500 REM DATA
6505 PRINTOSL, "c-data ROW(*), ITEM #";
6520 INPUT R$, I
6530 IF R$="#" THEN L1=1:L2=PC: GOTD6560
6540 R=VAL(R$)
6550 L1=R:L2=R
6560 I1=I
6570 ON I GOTO 6580,6590,6600,6610,6620,6630,6640,6650,6660
6580 V1=2;V2=6;EQ=4;GGTO 6670
6590 V1*="(3),";V2*="(4)&(5),";V3*="(1)";GGTO6750
6600 V1=2:EQ=3:GOSUB 2500:GOTO 6670
6610 V1=2: V2=5: EQ=1: GOT06670
6620 V1=2:V2=4:EQ=1:GOT06670
6630 V1=4"(1);"!V25="(8)&(9),"! V3$="(7)"!GDTD 6750
6650 V1=6: V2=9: EQ=1: GOTG6670
6660 V1=6:V2=8:EQ=1:GDT06670
6670 IF PG>3 THEN V1=V1+9:V2=V2+9:I1=I1+9
6680 FOR R=L1 TO L2
6680 FOR R=L1 TO L2
6690 ON EQ BOTO 6695,6710,6715,6725,6728,6705
6695 IF T(R,V2)=0 THEN 6730
6700 T(R,II)=T(R,V1)/T(R,V2):BOTO 6730
6705 T(R,II)=T(R,V1)*T(R,V2)
6706 BOTO 6730
6710 T(R,I1)=T(R,V1)*T(30,I1)/100:G0T06730
6715 IF $M(V1)=0 THEN 6730
6720 T(R,I1)=100*T(R,V1)$M(V1):FS=0
6721 G0T0 6730
6725 IF T(R,V1)=0 THEN 6730
6726 T(R,II)=100*T(R,V2)/T(R,V1):00T06730
6728 T(R,II)=T(R,V1)*T(R,V2)/100:00T0 6730
6730 NEXT R
6740 RETURN
6750 PRINTOSL, "CONSTANT "; V1$; V2$; V3$
6760 INPUT V1
6760 INPU: VI
6770 IF V1=3 OR V1=7 THEN EQ=2: GOTO 6670
6773 IF V1=1 AND I1=6 THEN V2=2:EQ=5:GOTO 6670
6775 IF V1=1 THEN V2=6:EQ=4:GOTO 6670
6780 IF V1<>4 AND V1<>8 THEN 6750
6790 V2=V1+1
6800 EQ=6
6820 GOTO 6670
6900 REM PROJECTION
6902 Y4=Y2
6905 PRINTOSL,"c projection ROW(* OR S), ITEM, RATE"; 6908 INPUT R$,I,RT$ 6909 IF RT$="R" THEN 3200
6910 RT=VAL(RT$)
6911 IF R$<>"#" THEN 6917
6912 CLS:PRINT"ENTER RATE FOR ";H1$(I)
6913 FOR W=1 TO PC
6914 IF RT="R" THEN RT=RT(W):GOTO6916
6915 R$=STR$(W):PRINT W;PL$(W):INPUT RT
6916 R$=STR$(W)
6917 R1=1+RT/100
6920 DY=ABS(Y2-Y1)
6925 S=1/R1
6930 FOR N=0 TO DY
6935 S=S*R1
6940 NEXT N
```

SAVE

The save routine stores data for a complete year on cassette tape. The routine must be entered from MAIN CONTROL. Entering SAVE will result in the response:

SAVE WHICH YEAR?

The year to be stored is entered (followed by the enter key). If the entered year does not correspond to either the primary or secondary years, the display will print "DATA NOT AVAILABLE" and return to MAIN CONTROL.

If a valid year is entered, the instruction "REWIND TAPE & PLAY" will be issued. The computer will search for the appropriate space on the tape assigned to that year (see Tape Initialization) and then prompt the user with "SET TO RECORD & ENTER READY". Following this instruction, the data is stored on the tape. The user is then instructed to "TURN RECORDER OFF" and control is returned to MAIN CONTROL.

Tape Initialization

Data is stored on a year-by-year basis, in a file named "F", followed by the year (e.g., "F1983"). The data for each year is spaced on the tape to allow sufficient room for updating by using a "header" file (named "H" followed by the year) preceding each data file. If a new data tape is being used, or if a different year is being stored than has been previously stored, the tape must be prepared to accept the data. This may be done with the following procedure:

- 1. Advance the data tape to that location where the new yearly data is to be stored.
- 2. Set the tape recorder to the record mode.
- 3. Return the program to the MAIN CONTROL routine.
- 4. Enter "HEADER".
- 5. Enter the year when requested.

The program will write the Header file and return to MAIN CONTROL. Continue with:

6. Enter "SAVE".

(Continued) | 0. Eliter

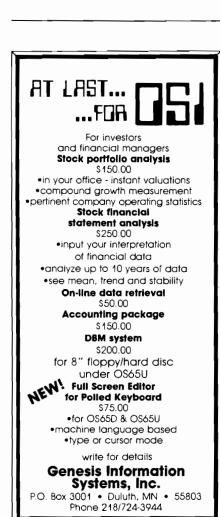
6945 IF Y2-Y1<0 THEN S=1/S

- 7. When requested with "SAVE WHICH YEAR", enter the year, followed immediately with an "X" (e.g., "1983X").
- 8. Enter "READY" when requested to do so.
- 9. Turn off the recorder when instructed.

The data has now been stored on tape and the program will return to MAIN CONTROL.

Summary

The Market Projection Program operates on a 16K Radio Shack Color Computer with Standard BASIC ROM, rather than the Extended BASIC ROM. Several subroutines were required to perform the mathematical operations of natural log and exponentiation. The program could easily be modified to operate with Extended BASIC by replacing the rather lengthy subroutines with their simple instruction



Appendix A (continued)

```
6950 IF R8="8" THEN L1=1:L2=PC: GOTO 6965
6955 R=VAL(R8)
 6960 L1=R:L2=R
6965 FOR N= L1 TO L2
6970 T(N,I+9)=8#T(N,I)
 6975 NEXT N
6980 IF W<>0 THEN NEXT W
6982 IF Y2<>Y4 THEN CLB:PRINT9495,Y4:GDT02025
6985 GDT0 2000
 7000 REM ENTER
 7005 FB=0
7010 PRINTDBL, "e ROW(* OR YR), ITEM *(*), VALUE"
7015 PRINT" (OR 'YR', YEAR)";
7020 INPUT Re, IS, V
7021 IF Re="Q" THEN 1000
7023 IF IS="#" THEN 7300 ELBE I=VAL(IS)
 7025 I1=I
7026 IF PG>3 THEN I=I+9
7030 IF R9="#" THEN 7090
7040 IF R8</">
7050 IF PB<4 THEN 71=V ELBE Y2=V
7060 GDTD 7200
 7070 R=VAL(R$)
7080 T(R,I)=V
7085 8010 7200
 70<del>9</del>0 CLB
7100 PRINT"ENTER VALUES FOR ";H1$(I1)
7100 FRINI "ENTER VALUES FOR ";H18()
7110 FOR L=1 TO PC
7120 PRINT L;PL8(L);
7125 PRINT " PV="T(L,I);:INPUT T$
7130 IF T8="/" THEN 7140
7135 T(L;I)=VAL(T$)
7140 NEXT L
7200 GDTD 2000
7300 R=VAL(Re)
7310 IF PB>3 THEN I1=10:I2=18 ELSE I1=1:I2=9
 7320 CLB
7330 PRINT "E- ";PL$(R);YR
7340 FOR I=I1 TO I2
7350 PRINT H16(I);
7360 INPUT T(R,I)
7370 NEXT I
 7380 GOTO 2000
8000 REM PRINT
8010 IF YR=Y1 THEN F=0 ELSE F=9
8020 PRINT#-2, YR; H1$;
8030 PRINT#-2, TAB(22) H1*(1)
8050 FOR N-1 TO PC
8060 D1=INT(T(N,1+F)*100+.5)/100
8065 PRINT#-2, CHR$ (0)
8070 PRINT#-2, N; PL#(N); 8080 PRINT#-2, TAB(24) D1
8090 NEXT N
8091 PRINT#-2
8092 PRINT#-2, "SUMMATION";
8093 PRINT#-2, TAB(24) INT(SM(1+F)*100+.5)/100
8094 PRINT#-2, PT; "TOP-DOWN TOTAL"
8095 PRINT#-2, " DELTA"
8100 FOR D2=1 TO 5 STEP 4
8110 PRINT#-2
 8120 PRINT#-2
8130 PRINT#-2, YR; H16(D2+1);
8140 PRINT#-2, TAB(11) H16(D2+2);
8150 PRINT#-2, TAB(18) H16(D2+3);
8160 PRINT#-2, TAB(25)H16(D2+4)
8170 FOR N=1 TO PC
8180 FOR K=1 TO 4
8190 I=K+F+D2
8200 TM(K)=INT(T(N, I) #100+.5)/100
 8210 NEXT K
8215 PRINT#-2, CHR# (0);
8220 PRINT#-2, N; TM (1);
8230 PRINT#-2, TAB (11) TM (2);
8240 PRINT#-2, TAB (18) TM (3);
8250 PRINT#-2, TAB(24) TM(4)
8260 NEXT N
8270 PRINT#-2
8275 PRINT#-2,"SUM";
8280 FOR K=1 TO 4
8290 PRINT#-2,TAB((K-1)*7+3) INT(SM(K+F+D2)*100+.5)/100;
8300 NEXT K
8310 PRINT#-2
8320 PRINT#-2,PT;T(PT,F+D2+1)
8325 D3=INT((T(PT,F+D2+1)-8M(F+D2+1))*100+.5)/100
8330 PRINT#-2,"DLT";D3
 8500 NEXT D2
8600 GOTO 2000
9000 REM LOG
9010 FM=1
 9020 IF FX=<10 THEN 9060
 9030 FX=FX/10
 9040 FM=FM#10
 9050 BOTD 9020
```

(Continu

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Appendix A (continued) 9060 GOBUB 9110 9070 IF FM<10 THEN RETURN 9080 FN=FN+FD 9090 FM=FM/10 9100 BOTO 9070 9110 FK=(FX-1)/(FX+1) 9120 F2=FK*FK 9130 FZ=1/51 9140 FOR F-49 TO 1 STEP-2 9150 FZ=1/F+FZ*F2 9160 NEXT F 9170 FN=FZ*FK*2 9180 RETURN 9200 REM EXP 9210 Z=1 9220 EX=0 9230 FOR XE=1 TO 50 9240 EX=EX+Z*X/XE 9245 Z=Z#X/XE 9250 NEXT XE 9255 EX=EX+1 9240 RETURN 9500 REM ROUND-OFF CALCULATE 9510 D=INT(T(DR,DI) \$100+.5)/100 9520 RETURN 10000 REM HEADER PREPARE 10010 CLB 10020 PRINTOSL, "YEAR"; 10030 INPUT YS 10040 OPEN "O",~1,"H"+STR#(YS) 10050 PRINT#-1,"HEADER",YS 10060 CLOSE-1 10080 DL=500 10090 Y1=Y8 10100 BOTO 1000 11000 PRINTOSL, "CLEAR WHICH YEAR" 11010 INPUT Y 11020 IF Y=Y1 THEN K1=1:K2=9:BOTG 11100 11030 IF Y=Y2 THEN K1=10:K2=18:BOTG 11100 11040 PRINT "YEAR NOT IN DISPLAY" | GOTO 2000 11100 FOR N=1 TO PT 11110 FOR I=K1 TO K2

counterpart. There are also other command substitutions, such as providing formatted output, which would shorten the program. The difficulty with performing these changes, of course, is that the program will no longer run with a computer which only has the Standard BASIC ROM.

The listing for the program is given as Appendix A. To customize the program to specific market areas, the product categories of interest should be entered on lines 340 through 400. The number of categories should be entered on line 30 as the value of PC (product categories). The program will accept any number of categories; however, the amount of memory required for operation will increase or decrease according to the category quantity.

The author may be contacted at Motorola Inc., Semi Conductor Products Sector, 5005 East McDowell Rd., Phoenix, Arizona 85008.

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11120 T(N, I)=0 11130 NEXT I

11140 NEXT 11150 GOTO 2000

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PET Vet

By Loren Wright

Carl Moser's MAE and ASM-TED

MAE and ASM-TED have been around since the early days of the PET. Other assemblers have come and gone or faded into obscurity, but few others have been able to match the prices, features, and support of these assemblers.

Both are written in machine language. ASM-TED is primarily intended to operate in a cassette environment with 16K or more. MAE requires a disk and 32K. The syntax and operation are nearly identical. Both are available for a number of other 6502 machines, which comes in handy when transferring programs from one machine to another.

MAE is a macro assembler. Macros allow you to assign a name to a frequently used sequence of instructions. Each time you want to include this sequence later in your program, you instruct the assembler to expand the macro you name. This can save a lot of time on large programming projects.

Another powerful feature of MAE is conditional assembly. One popular application of this capability is to allow one source listing to generate different object files, depending on which ROM set you have.

Another feature is interactive assembly, where the assembler pauses to get some input from the user before it continues assembling. MAE supports labels

up to ten characters long, and the editor allows extensive, convenient file editing.

MAE coexists with BASIC, an extended monitor, and your assembly program, if you haven't decided to assemble it on top of the assembler! To handle such problems and to allow greater programming flexibility, a relocating loader program is included. The loader acts upon a relocatable object file, which is easily created by the assembler. An enhanced DOS wedge program is included, which allows you to enter the assembler, loader, or extended monitor (also included on the disk), with a two-letter abbreviation, such as 'AW' for 'assembler warm-start.'

The assembler offers a wide variety of pseudo-ops, including codes to handle data, address, and word storage, listing generation; assembly from a sequence of modules with a control file (not implemented in ASM/TED); as well as the macro, conditional assembly, and interactive assembly commands, and the more usual pseudo-ops.

Unlike the standard three-letter syntax, MAE uses two-letter codes, preceded with a period. For instance, instead of ORG, MAE uses .BA (begin assembly.)

There are other deviations from normal 6502 assembler syntax. Page zero labels must be preceded with an asterisk. To specify the high byte of a label, the syntax is: #H, Label. It is also important to distinguish in the equate portion of the file between label references internal and external to the pro-

gam, particularly if the relocating loader is to be used. These variations, learned quickly, were included to increase the speed and flexibility of MAE and the relocating loader.

Several utility and sample programs are included on the disk. The manual is thorough, including examples of macro, conditional, and interactive assembly. EHS publishes a newsletter on an irregular basis to keep users up on improvements, applications, and other information of interest to MAE owners. Finally, there is a large and very active users' group dedicated to Carl Moser's assemblers and assembly-language programming on the PET. They maintain a large disk library of assembly and other PET programs.

ATUG Disk Exchange c/o Brent Anderson 200 S. Century Rantoul, IL 61866 [217] 893-4577

MAE and ASM-TED are available from Eastern House Software (3239 Linda Drive, Winston-Salem, NC 27106). MAE comes on disk for \$169.95, and ASM-TED on cassette for \$49.95. Specify BASIC ROM and disk format.

The Transactor is Back!

After a brief absence, The Transactor is back. Commodore decided to discontinue publishing it after Volume 3. Karl Hildon has left Commodore to continue as editor of the new Transactor, now under the auspices of Canadian Micro Distributors. The new publication will undergo some changes, including a glossier presentation, advertising, and a more reference-oriented format. Most of the same authors will continue to contribute. A subscription to Volume 4 is \$15 US or Canadian.

The Transactor Canadian Micro Distributors 500 Steeles Avenue Milton, Ontario L9T 3P7 Canada

Midnight Software Gazette and The Paper Merge

The Midnight Software Gazette, now published by the Central Illinois

Sample MAE Listing

end of Meeters.

	0010 USRVEC .DE ≸01
	0020 SCREEN .DE \$8000
	0030 :
	0040 .BA \$360 ;2ND CASSETTE BUFFER
	0050 .08
	0060 ;
0360- A9 00	0070 INIT LDA #L,SCREEN
0362-85 01	0080 STA *USRVEC
0364- A9 80	0090 LDA #H.SCREEN
0366-85 02	0100 STA *USRVEC+1
0368- A0 00	0110 LDY #0
036A- 98	0120 LOOP TYA
0368 91 01	0130 STA (USRVEC),Y
036D~ 88	0140 DEY
036E- D0 FA	0150 BNE LOOP
0370-00	0160 BRK
00,0 00	0170 .EN

PET Users' Group on a SASE basis, and The Paper, now published by Ralph Bressler, have merged. The name of the new publication will be determined by the results of a reader contest, in which the grand prize will be a VIC. Current Paper subscriptions will be fulfilled with issues of the new publication. A six-issue subscription is \$20 — US, Canada, and Mexico; \$30 — foreign surface; and \$40 — foreign air mail. As with the Midnight Software Gazette, the new publication will be mailed First Class. Advertising is \$25 per quarter page.

Midnight Software Gazette (for the moment) c/o Jim Oldfield 635 Maple Street Mt. Zion, IL 62549

Other Commodore-oriented Publications

The Code Works has decided to discontinue publication of its famous *Cursor* cassette magazine until the new Commodore-64 is released. In the meantime, they will publish a newsletter.

Another new Commodore-oriented newsletter is being published by Roger Olanson. Further information was not available at press time.

Strictly Commodore 47 Coachwood Place, N.W. Calgary, Alberta T3H 1E1 Canada

The Whole PET Catalog Now Available

The Midnight Software Gazette has published a book called The Whole PET Catalog, which will include the contents of all of the first seven issues of Midnight, plus what would have been issue number 8. The material has been updated, edited, and reorganized. In addition there will be full listings of the contents of both the ATUG and TPUG libraries. The price for The Whole PET Catalog is \$10 from the above address or it may be obtained from AB Computers (252 Bethlehem Pike, Colinar, PA 18915, (215) 822-7727), Professional Computer Sales (117 Skyway Avenue, Toronto, Ontario, Canada, (416) 675-7818), and

other dealers. *Midnight* has concentrated on reviews and news, so it will be useful to see it all in one place.

Patch for 2031 with Upgrade BASIC

Willi Kusche (creator of KMMM Pascal, reviewed here in the January issue) of Bellwamr, NJ reports a problem in the operation of the CBM 2031 single disk drive with Upgrade ROM (3.0) PETs. He has provided the following patches to the \$F000 ROM:

F17F 4C ED FF F182 EA FFED AD 40 E8 FFF0 29 FB FFF2 8d 40 E8 FFF5 A9 5F FFF8 4C 87 F1

You can program a substitute EPROM yourself, or he will provide one for \$15:

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Double-Entry Formatting for Your Checkbook on the CIP

by Leo Jankowski

A simple checkbook balancing program for the C1P is presented. The program is easily modified for other applications and computer environments.

Double Entry requires:	
OSI C1P	
Cassette unit (optional)	
Epson MX-80 (optional)	

A drawback to many checkbook balancing programs is that they are more difficult to use than the pencil and paper approach! After all, you just need a credits column, a debits column, their totals, and difference.

This program, designed primarily for use with a printer, provides this information. As is, the program is not concerned with check numbers, but if needed, they could be input as another column of (unformatted!) figures. The program could be easily adapted for other uses that require simple double-entry bookkeeping. By changing the headings to, say, GROSS PAY and TAX, a record could be made of paychecks and tax-bites due.

The user is given a choice of one entry per line or two. Every line used is numbered. Entries can be positive or negative, and in any format. Leading zeros can be omitted. The decimal point is not mandatory for dollar-only amounts. The only drawback is that the largest amount or total must be less than or equal to 9999.99. The C1P rounds off to seven significant figures or more, which introduces small arithmetical errors.

No entry-error checking routine is provided. Peculiar printouts will follow entries like 45.006! An error routine would have made the program unnecessarily complicated. The character codes in line 3080 are for the Epson MX-80. All lines containing REMs can be

Sample Runs				
		DOLLAR AMOUNT		TOTAL
LINE	CREDIT		DEBIT	NET
1 2 3	34.56		34.56	
2	569.20		45.60 56.70	
4	234.56 56.70		3.45	
5	456.12		56.78	
TOTALS	\$ 1351.14	- \$	197.09	= \$ 1154.05
	***	DOLLAR AMOUNT	***	TOTAL
			2001-	
LINE	CREDIT		DEBIT	NET
1	2300.34			
	450.10			
2 3 4	34.40			
	00.01		74 50	
5 6			34.50 23.10	
7			12.00	
8	6.40		12.00	
TOTALS	\$ 2791.25	- \$	69.60	= \$ 2721.65

removed without affecting the program. Also, lines 3070 to 9998, and line 140, could be omitted without affecting the guts of the program.

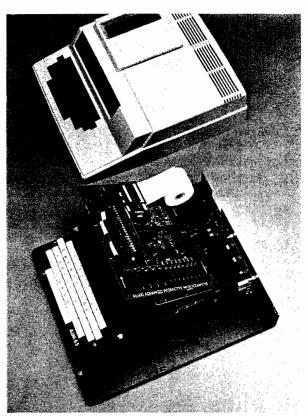
Changing line 170 to 170 READ CA, allows you to use the program with DATA statements that have been added to the listing or that are to be read in from tape.

Mr. Jankowski has been involved with computers for 12 years. He now owns a Superboard and has several projects underway, including writing programs to teach Statistics and to keep stock records in farming. He may be contacted at Otaio RD1, Timaru, New Zealand.

Important Variables

C\$	clear screen
CO	column
EN\$	one entry per line or
	two
FF	counter for Form Feed
FL	form length
MU\$	money string to be formatted
N	line number
NSUM	negative, 2nd column
	sum
PSUM	positive, first column
	sum
POKE 517,255	printer ON .
POKE 517.0	printer OFF

```
Listing 1
10 PDKE515,0:REM turn off LOAD.
90 ENS="Y"
100 FF=9:C0=22:FL=61:BL$=CHR$(32)
110 C$=CHR$(127):L$=CHR$(10):CR$=CHR$(13)
120 FH$="CREDIT": SH$="DEBIT": TH$="TOTAL": XH$="NET"
125 :
130 :
135 REM Go for Instructions & print headings.
140 GOSUB4000: GOSUB3070
141 :
145 :
146 REM Turn Printer OFF & ask for Input.
150 CC=22:POKE517,0
170 INPUT" Cash amount":CA:PRINT
180 CENTS=CASH#100
190 IFCA>OTHENPSUM=PSUM+CENTS: GOSUB700: GOTO150
200 IFCA<0THENNSUM=NSUM+CENTS: C0=45: G0SUB700: G0T0150
210 :
220 REM CA must be 0
240 REM Turn Printer ON.
250 POKE517,255: PRINTL$
300 GDSUB2000: GOTD900
400 :
500 :
600 REM Decide where to print next amount.
700 PDKE517,255
710 IF EN$="N"THENPRINTCR$:60T0760
     IFCA>OTHENPRINTCR$: GOTO760
750 IFPE>OANDCA<OTHENCO=22:GOTO805
755 CO=22:IF PE<OANDCA<OTHENPRINTCR$:CO=45:GOTO760
758 CO#45
759
     REM Line number.
760 N=N+1:PRINTN;
800 :
801 REM Format the cash amount.
805 POKE517,0
808 MUNNY$=STR$ (CE) : L=LEN (MUNNY$)
810 IFL=3THENL=5:MU$="00"+RIGHT$(MU$,2):G0T0855
820 IFL=2THENL=4: MU$="000"+RIGHT$ (MU$, 1):GOTO855
830 IFCE (OANDL=3THENMU$="00"+RIGHT$ (MU$, 2):GOTO855
840 IFCE<OANDL=2THENMU$~"000"+RIGHT$(MU$,1):GOTD855
845 MU$=RIGHT$ (MU$, L~1)
855 L=LEN (MU$)
870 POKE517,255
872 FF=FF+1: IF FF=FL THEN PRINTCHR$(12)
874 REM Form-feed ?
875 FF=FF+1: IF FF=FL THEN PRINTCHR$ (12)
876 REM Print the formatted amount
880 PRINTTAB(CO-L)LEFT$(MU$,L-2)"."RIGHT$(MU$,2);
890 PE=CA: RETURN
895 :
896 :
897 REM Last amount was 0, so now print totals.
900 PRINTBL$:PRINT"TOTALS";
905 CENTS=PSUM:PRINTTAB(14)"$";:GOSUBB05
703 CENTS-PSUM:rRINTIAB(14) $ ;: GOSUBBOS
910 CO=45: CENTS-NSUM: PRINTTAB(34) "- $";: GOSUBBOS
920 CO=67: CENTS-PSUM+NSUM: PRINTTAB(56) "- $";
     GOSUBBO5: PRINTBL$
927
928 GOSUB2000
935 PRINTCHR$(7):PRINTCHR$(12)
940 POKE517,0:GOT09999
1000 :
1500 :
1600 REM Print a line.
2000 FORX=1T071:PRINT"-";:NEXT:PRINTBL$:RETURN
2010 :
2020 :
3050 REM Print headings.
3070 POKE517,255
3075 REM Commands to EPSON 80 Printer.
3089 PRINTCR$:PRINTCHR$(27);:PRINTCHR$(69)
3090 GDSUB2000
3100 PRINTTAB(17) "$$$
                               DOLLAR AMOUNT
                                                     $$$":
3110 PRINTTAB(65)TH$
3120 GOSUB2000: PRINTLS
3130 PRINT" LINE"TAB(17)FH$TAB(41)SH$TAB(67)XH$
3140 GOSUB2000: PRINTL$
3150 POKE517,0:RETURN
3500 :
3600 :
3610 REM Instructions.
4010 PRINT: When prompted enter":PRINT
4010 PRINT" When prompted enter":PRINT
4020 PRINT" your money amounts.":PRINT
4030 PRINT" They can be negative"
4035 PRINT:PRINT" or positive."
4040 PRINT:PRINT:PRINT" Last amount must be 0."
4050 PRINT: PRINT: PRINT: PRINT
4060 INPUT" Two entries per line";EN$
4070 IF LEFT$(EN$,1)="N"THENEN$="N"
4080 PRINTCS: RETURN
9998 DATA 0
                                                          AICRO
9999 END
```



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Auto SAVE for the PET

by Louis F. Sander

This routine makes automatic cassette backup copies of your PET BASIC programs at regular intervals. Properly used, it can keep you from losing your temper and your creative programming work.

AUTOSAVE

requires:

PET with cassette

This short subroutine averts the anguish, anxiety, and anger that result from system crashes during BASIC program development. AUTOSAVE makes timely backup copies of the work in progress. So if you've ever seen your best creative effort swallowed up by a machine whose keyboard is locked out, read on for help.

You use the AUTOSAVE subroutine by putting it at the very end of your program, and by putting a GOSUB call at the very beginning. Then each time you RUN your main program, which you will certainly do as you check the lines you are writing, AUTOSAVE will be called first. If an hour or half hour point has passed since the last SAVE, AUTOSAVE will ask you if you'd like to make some backup copies. If you would, AUTOSAVE will make as many as you wish, using a 16-character program name consisting of the date and time of the SAVE, plus an eightcharacter identifier chosen earlier by you. AUTOSAVE will also give you special guidance in handling and marking your backup cassette.

Then, if disaster strikes, just load your most recent backup and repeat your work since the time of its SAVE. In the worst case, you might lose 30 minutes worth of work. Since each backup's name includes the time it was saved, the most recent one will be easy to find.

Key AUTOSAVE into your PET exactly as it is listed, then save it on tape for further use. It is important that lines 63630 to 63648 take up no more than 17 screen lines, so don't use any unnecessary spaces in them. The six characters immediately after 'BACK-UPS' in line 63638 and 'DATE' in line 63640, consist of three SHIFTED SPACEs, followed by three CURSOR LEFTs. Shifting the spaces is very important because it lets you answer an INPUT statement by pressing RETURN.

Line 63630 requires a bit of explanation. You will normally change this line, making TJ\$ = the eight-character name of your main program. TK is the number of saves to be made at backup time, which you might also want to change. TI% is a POKE location used by the subroutine, and TI% = 2 is fine unless your main program calls the rarely used USR function, which also

63626 REM

uses location 2. Possible (but untested) alternates in this case are TI% = 6 for original ROMs and TI% = 15 for all others.

If you are starting your main program from scratch, just load AUTO-SAVE before you begin; if the main program is already on tape, add AUTO-SAVE with an APPEND program. Or, just follow these steps:

- LOAD and LIST AUTOSAVE. Don't touch the keyboard after the LIST.
- Mount the main program tape in TAPE #1, so that the main program is the next one on the tape.
- 3. Type LOAD, but don't hit RETURN.
- 4. Press PLAY, then quickly hit RETURN.

```
63624 REM *** AUTOSAVE SUBROUTINE ***
```

LOUIS F. SANDER

63627 REM 153 MAYER DRIVE 63628 REM PITTSBURGH, PA 15237 63629 REM 63630 TJ\$="AUTOSAVE":TK=3:TI%=2:TJ=1080 00:I=INT(TI/TJ):REM *** AUTOSAVE *** 63632 IFI+1<PEEK(TI%)THENINPUT"@UTIME"; TK\$:TI\$=TK\$+"00":POKETI%,TI/TJ+1:RETURN 63634 IFPEEK(TI%)≈I+1THENRETURN 63635 POKETI%, I+1 63636 PRINT"随题試TIME IS "LEFT\$(TI\$,4); 63638 INPUT". SAVE BACKUPS **聖聖**:";TK\$: I FLEFT\$(TK\$,1)<>"Y"THENRETURN 63640 INPUT"NEEDATE IIII ;TK\$:PRINT IIII OUNT BACKUP TAPE, THEN HIT 'B'" 63642 GETA\$:IFA\$<>"B"THEN63642 63644 TJ\$=TK\$+LEFT\$(TI\$,4)+TJ\$:FORI=1T0 TK:SAVETJ\$:PRINT"MSAVED "TJ\$:NEXT 63646 PRINT"BELABEL TAPE, THEN HIT 'L'" :PRINT"DO";:GETA\$:IFA\$<>"L"THEN63646

63648 RETURN

5. When the main program has loaded, AUTOSAVE's line 63630 should be at the top of your screen. HOME your cursor and add the AUTOSAVE lines to your program by hitting RETURN eleven times.

Now edit line 63630 so that TJ% = the name of your main program, and add a GOSUB 63630 as the first line of your main program. If for some reason you don't expect to RUN your main program from the beginning much while writing code, put some GOSUB 63630's in the program sections you will be running. Also finish the main program with an END statement to keep it from falling through to AUTOSAVE.

Here are some final notes: Entering a GOSUB 63636 in immediate mode will generate an automatic save at any time. Notice that 63636 is a symmetrical number, and therefore easy to remember. When AUTOSAVE asks you for the time, it will only accept 4-digit numbers from 0000 to 2359. It will accept anything at all as a date, but it's really looking for four digits (e.g., 0101 for New Year's Dayl. AUTOSAVE's special tape handling instructions are designed to have you take the cassette out of the recorder and label it; both steps inspire good operating practice. AUTOSAVE has been thoroughly tested on an original ROM PET. Since it depends on TI\$ for its success, if your main program alters TI\$ or shuts off the jiffy clock, AUTOSAVE will not work properly.

If you'd like a detailed description of the theory behind AUTOSAVE, just drop me a line at the address provided.

Louis F. Sander designs and markets electronic systems for hospitals and other health care providers. He is the originator of COMPUTER KINDERGARTENTM, a computer familiarization course for adults, and has written computer-related articles for several publications. He has worked with electronics since pre-transistor days. You may contact Mr. Sander at 153 Mayer Drive, Pittsburgh, Pennsylvania 15237.

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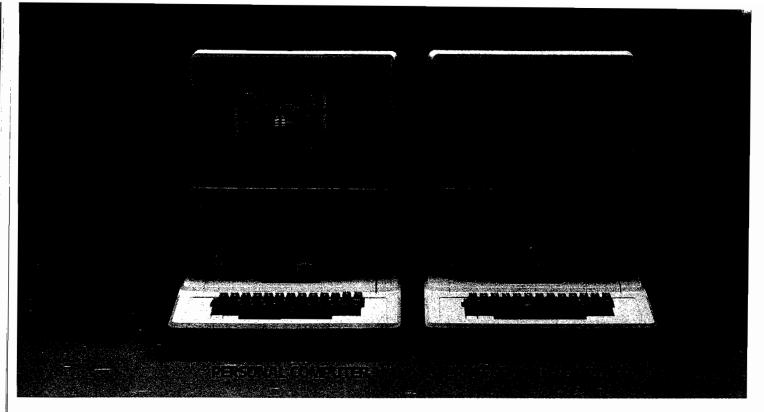


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New Publications

Implementing BASICs, by William Payne and Patricia Payne. Reston Publishing Co., Inc. |11480 Sunset Hills Rd., Reston, VA 22090| 1982, 210 pages, 9¼ × 6 inches, hard cover. ISBN: 0-8359-3045-9

This book is designed to help you achieve better software system designs and more programming techniques.

CONTENTS: Language Commands, Statements, and Their Variables; Microcomputer Data Structures; Variable Table Structure; Common Variables; Lexical Analysis, Text Atomization, and Syntax Analysis; Program Resolution; Program Text Coordinates; Interpreted Program Execution; Compiled BASICs; Verb Failures, User-Defined Verbs, and BASIC Line Editor; Timesharing Language Systems; Language System Code and Its Systems Verbs; How to Write a Language System; Conclusions and References; Appendix; Annotated Glossary of Techical Terms; Index.

Apple Pascal: A Hands-on Approach, by Arthur Luehrmann and Herbert Peckham. McGraw-Hill Book Company [New York, NY], 1981, 430 pages, 6½ × 9 inches, wire-o binding, paperback. ISBN: 0-07-049171-2 \$14.95

A series of sessions (chapters) designed to teach you Pascal, in a method similar to the way you learned English. The 14 sessions provide 30 to 40 hours of hands-on activities for the Pascal novice.

CONTENTS: Preface; Introduction; Getting Started; Typing in Programs — The EDITOR; Writing, Running, and Changing Programs; Generating Sound; Inventing New Words: Procedures; More Invented Words: Functions; Drawing Pictures; Branching Statements: IF and CASE; String Variables and WHILE Loops; Number Types and Arithmetic; Scalar Data Types and Sets; Arrays; Records and Files; Recursion; Where Do You Go From Here! Appendixes; Solutions to Problems; Index; Compiler Error Messages.

Elementary BASIC: Learning to Program Your Computer in BASIC with Sherlock Holmes, by Henry Ledgard and Andrew Singer. Vintage Books (201 E. 50th St., NY, NY 10022), 1982, 264

pages, 9¼ × 6 inches, paperback. ISBN: 0-394-70789-3 \$12.95

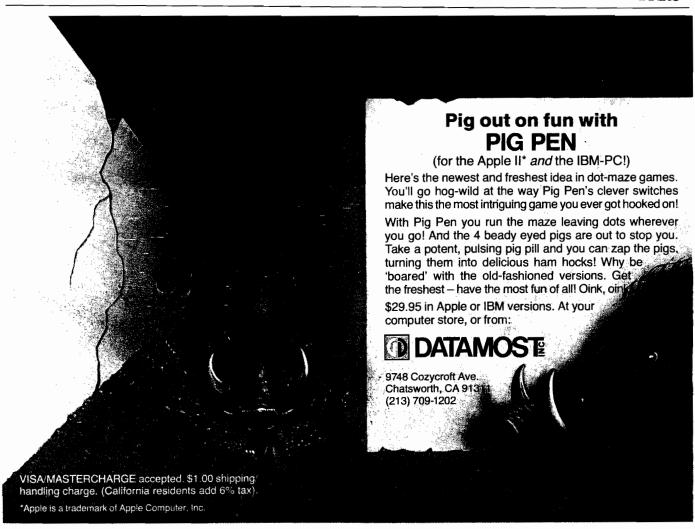
In these new Sherlock Holmes Stories, the great detective uses a computer to analyze clues. Holmes instructs Dr. Watson (and the reader) in a way that illuminates the mysteries of computer programming.

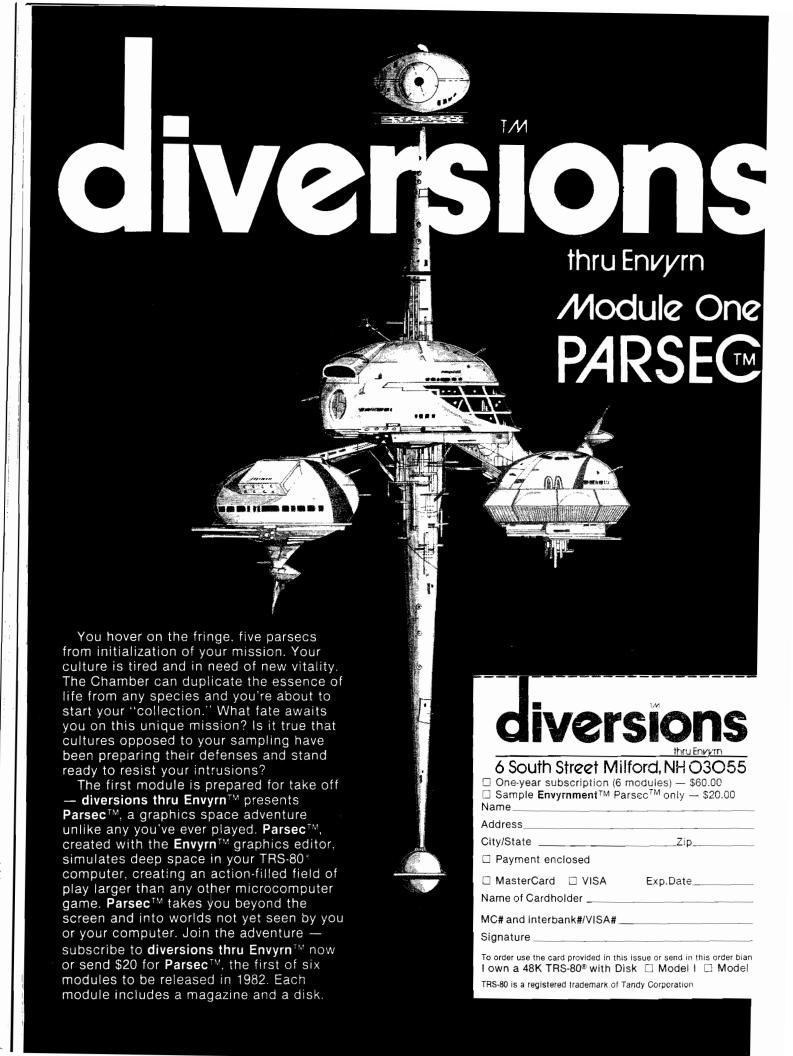
CONTENTS: The Analytical Engine; Murder at the Metropolitan Club; Holmes Gives a Demonstration; The Adventure of the Bathing Machine; A Study in Cigar Ash; The Adventure of Clergyman Peter; Holmes's Method Revealed; The Ciphered Message; An Advertisement in the Times; A Study in Chemistry; The Coroner's Report; The Adventure of the Gold Chip; Holmes Delivers a Lecture; The Final Programme; Appendix; Postscript; Index.

DP Directory, (P.O. Box 562, Bloomfield, CT 06002), monthly, 8½ × 11 inches, paperback. ISSN: 0730-6806 \$48.00/year

A collection of Table of Contents from DP magazines. Provides data processing managers, analysts and researchers with a tool to identify important articles. Over fifty magazines listed.

MICRO"





COMPRESS — An Applesoft Optimizer

by Barton M. Bauers

This machine-language utility will not only reduce Applesoft program memory requirements by up to 30% or more, but will increase execution and load speed also.

COMPRESS

requires:

Apple II or Apple II Plus 48K, Applesoft BASIC

Note: While the listing for COMPRESS is longer than we normally publish, MICRO feels this valuable program should be available to our readers. However, if you prefer not to key it in, MICRO will supply the program on DOS 3.3 disk for \$10.00, plus \$2.00 for shipping and handling. See page 113 for order information.

Recently announced Applesoft compilers are advertised to be up to twenty times faster than the Applesoft interpreter. Most reviewers have found, however, that in practice these compilers perform at about twice the operating speed of the interpreter. Recognizing that program compilation involves a significant amount of overhead, many programmers have avoided them or reserved them for specific applications. Since compilers do not dramatically increase speed, and since some programs require that variables and arrays be dimensioned while the program is being executed (a feature most compilers don't support), the Applesoft interpreter is often the better compromise.

COMPRESS is a machine-language program that will optimize most programs in less than ten seconds. To ensure clarity, COMPRESS allows the use of many REM statements in source programs. Variables can be dimensioned within programs, and can appear in the program before they are dimensioned [another feature the compilers can't

Listing 1: COMPRESS

9000		1		ORG	\$9000	; SET HIMEM UALUE TO ; PROTECT COMPRESS ; RETURN TO A/S ; INIT POINTERS AND ; REGISTERS USED ; BY COMPRESS ; PUT DEC-HEX CONVERSION ; CONSTANTS IN MEMORY ; TO PERMIT A/S DECIMALIZED ; TOKENS TO BE CONVERTED ; TO HEX LINE NUMBERS ; START ROUTINE TO ; BLANK PAGE \$8D ; BLANK PAGE \$8D
2000		2		OBJ	\$800	
9000	0577	3		CTO	#300	; SE! HIMEM VALUE TO
9004	6000	-		LDG	##OD	3 PRUTEUT COMPRESS
9004	8574	5		STE	##OU #74	
9008	4CD003	7		IMP	#300	PETHEN TO GVC
900E	A900	8		LDA	#\$00	: INIT POINTERS AND
9000	AA .	9		TAX	##65	: REGISTERS USED
900E	A8	10		TAY		: BY COMPRESS
900F	18	11		CLC		, p. 6611 (222)
9010	8506	12		STA	\$86	
9012	8508	13		STR	\$0 8	
9014	8DD095	14		STR	\$95D@	
9017	8DE095	15		STA	\$95E0	
901 A	85FB	16		STA	\$FB	
901 C	A908	17		LDA	# \$08	
901E	8507	18		STA	\$ 07	
9020	R980	19		LDA	#\$8D	
9822	8509	20		STR	\$09	
7024	H907	21		LDH	#\$07	
2076	9515	22		SIH	\$1F	
207.0	DEC	23		CTO	#\$01	
9020	9988	25		210	≯ EF	
9025	ODETOE	25		CTO	#900 #0551	
9071	RDE295	27		CTO	#7UE1	
9834	8DE 795	28		STA	*7352 *9557	
9837	1 8983	29		IDE	##3	PUT DECULEY CONDEDCTOR
9039	SDE495	30		STA	#95F4	* CONCIONTS IN MEMORY
9030	6927	31		LDA	#\$27	TO DEDMIT OVE DECIMALIZED
903E	8DE595	32		STA	#95F5	: TOKENS TO BE CONDECTED
9041	fi901	33		LDA	#\$1	: TO MEXITURE NUMBERS
9043	SDD195	34		STR	\$95D1	> TO THEM EXTRE MORDERS
9046	A90A	35		LDA	#\$A	
9048	8DD295	36		STA	\$95D2	
904E	H964	37		LDA	#\$64	
9040	SDD395	38		STR	\$95 03	
9050	A9ES	39		LDA	#\$E8	
9052	800495	46		STA	\$ 9504	
9055	A910	41		LOA	#\$10	
9057	800595	42		STA	\$95D5	
905R	A200	43		LDX	#\$00	; START ROUTINE TO
9050	A9FF	44		LDA	#\$FF	; BLANK TABLES
905E		45		PAG		
905E	9D003D	46	CLR1	STA	\$8000.X	; BLANK PAGE \$8D
9061	E0FF	47		CPX	#\$FF	
9063	F004	48		BEQ	CLR2	
9065	E8	49		INX		
9066	4C5E90	50		JMP	CLR1	
9669	H200	51	CLR2	LDX	#\$00	
3668	POURSE	52	CLR3	STR	\$8E00.X	; BLANK PAGE \$8E
7055	E0FF	3ú		CPX	# # FF	
טושה מלמפ	F004	34 EE		BEU	CLR4	
9077	404500	50		INA	. CL D7	
9976	900000 9000	57	CLEA	JMF	##GG	
9879	90009F	50	CLES	CTO	##### #OE040 V	• DLONK DOCK #OF
9076	FOFF	50	CERJ	CENT	#0F0077	J BLARM. PAUE #SF
9970	FORE	66		PEG	##1 1 NGU	
907F	ES	61		INX	1100	
9880	407890	62		IMP	CLR5	
9083	C8	63	INCY	INV	OL.	: SUBBOUTINE TO INCREMENT
9084	C:000	64		CF'\	#\$66	: Y-REG AND CHECK TO
9086	DOOS	65		BNE	GOBAK	SEE IF PAGE BOUNDARY
9088	86ED	66		STX	\$ED	: CROSSED
903A	R61F	67		LDX	\$1F	
9080	F600	68		INC	\$00,X	
90SE	H6ED	69		LDX	\$ED	
9090	60	70	GOBAK	RTS		
9091	200DFB	71	ERROR	JSR	\$FBUD	: ERROR ROUTINE TO
9094	7950 1965	72		LDA	#\$U5 *EDEG	PRINT ERROR CODE
9999	20000	74		JOK	≱ たいつ	; BLANK PAGE \$8E ; BLANK PAGE \$8F ; SUBROUTINE TO INCREMENT ; V-REG AND CHECK TO ; SEE IF PAGE BOUNDARY ; CROSSED ; ERROR ROUTINE TO ; PRINT ERROR CODE ; # AND RETURN ; TO A/S ABORTING ; COMPRESS
9000	20F0FD	75		LUH	₩₽₽₽ ¢ENEG	* TU RAD HBURTING * COMPRESS
2070	ZUFUFU	15		JOR	∌FUFU	, CUPIFRESS
						(Continu

(Continued)

match]. But actual control during execution must pass to those statement numbers after they are dimensioned. So COMPRESS does not interfere with any normal interpreter capabilities.

Although COMPRESS can be thought of as a poor man's compiler, it is not a compiler at all. It merely reduces Applesoft programs to their minimum configuration.

COMPRESS occupies 2.3 kilobytes of free RAM memory, just below DOS in a 48K Apple II Plus. It also uses about 30 bytes of zero-page storage for pointers. Typical programs can be reduced 15% or more in memory requirements, with up to 30% or more for certain programs that contain a large number of REMs or other redundant code. But COMPRESS is not simply a REM remover. The programmer is encouraged to insert a great deal of narrative in the original source program so that any modifications required later can be easily made to the source code and then recompressed.

What COMPRESS Does

COMPRESS removes line numbers, redundant statements, etc., and concatenates as many Applesoft statements as can be legally assigned to one line number. For example, consider this program:

10 LET J = 5 20 LET K = 10 30 END

Although this simple program does nothing, it is a legal Applesoft program and could be run through COMPRESS. After running COMPRESS, the program would appear as:

10 J = 5 : K = 10 : END

COMPRESS has removed 45% of the memory requirements of this program!

The original program would have appeared in memory as:

0A 08 0A 00 AA 4A D0 OΩ 00 14 08 14 00 AA **4**B 35 D0 31 30 00 1A 08 1**E** 00 00 00 00

The compressed program in memory would appear as:

00 10 08 0A 00 4A D0 35 3A 4B D0 31 30 3A 80 00 00 00

Note how many tokens are removed after the original program is cleaned up.

Listing 1 (continued)

```
909E 20F0FD
                                       JSR $FDF0
                                       LDA #$CF
9083 20F0FD
9086 89D2
                                       JSR $FDF0
LDA #$D2
90AS 20F0FD
90AS A9A0
                                        JSR ≸FDF0
LDA #≸A0
                      81
90AD 20F0FD
90B0 A9A3
                                        JSR #FDF0
                                       LDA ##A3
                      83
9082
9085
       20F0FD
85F9
                      85
                                        LDA $F9
9087
9088
       200AFD
       4CD003
                      87
                                        JMP $300
                                        LDA ($86), Y
 90BD B106
                            NOW
                                                                      : PROGRAM STARTS HERE
                      88
988F 208390
98C2 C900
                                       JSR INCY
CMP #$00
                                                                      ; YES - CHECK FOR END OF PROGRAM
; IS IT 'THEN'?
; YES
                      89
                                                                      ; GET A BYTE - INC Y-REG
                      90
90C4 F03C
90C6 C9C4
90C8 F013
90CA C9AB
                                       BEQ TSTR
CMP #$C4
BEQ THEN
CMP #$AB
                      93
94
                                                                      : IS IT 'GOTO'?
                            NTHG
90CC F055
                                        PAG
                      96
97
                                        BEQ LOAD
90CE C9B0
90D0 F051
                                                                      IS IT (GOSUBY?
                                        BEQ LOAD
                                                                      ; YES
                      99
90D2 C9BC
90D4 F04D
                                       CMF #$BC
BEG LOAD
                                                                      ; IS IT 'LIST'?
                     100
                                                                      ; YES
; IS IT TOELT?
9006 C985
9008 F049
                                        CMF #$85
                                       BEG LOAD
JMP NOW
                                                                      : YES
: GO GET ANOTHER BYTE
                     102
900A 4CBD90
900D S5FD
                                                                      ; PUT A-REG ASIDE
; GET ANOTHER BYTE
; INC Y-REG
                    104
                            THEN
                                       STA #FD
900F B106
                                        LDA ($06),∀
90E1 208390
90E4 C9AB
90E6 F03B
                                       JSR INCY
CMP ##AB
                    106
                                                                      ; IS IT 'GOTO'?
                     103
                                        BEQ LOAD
90E8 38
                     109
                                       SEC
                                                                      ; FIND OUT IF THE TOKEN IS
90E9 E930
90EB 3005
                     110
                                        SBC #$30
                                                                      # A LINE NUMBER REF.
                                       BMI CLRIT
                    111
90ED 38
90EE E90A
                                        SEC:
                    113
                                       SBC #$ØR
90F0 3008
90F2 208893
                                                                      ; YES IT IS
                           CLRIT
                                                                      ; BACK UP ONE
; GET A-REG BACK
                    115
                                       JSR DECY
90F5 A5F0
90F7 4CBD90
                                       LDA #FD
JMP NOW
                    116
117
                                                                      ; FALSE ALARM
; BACK UP ONE
90FA 208893
90FD A5FD
90FF 4C2391
9102 B106
                    118
                            GOTIT
                    119
                                       LDA $FO
                                                                      ; GET A-REG BACK
                                       JMP LOAD
LDA ($06),Y
                                                                      ; GO FORWARD
                                                                      ; GET ANOTHER BYTE
; INC Y-REG
                            TSTR
9104 208390
9107 C900
                                       JSR INCY
CMP #$00
                                                                      ; INC Y-REG
; IS THIS @ ALSO?
; VES - 2 IN A ROW
; NO.1NC Y-REG
; INC Y REG AGAIN
; AND AGAIN
; CONTINUE ON
                    123
9109 F00C
910B 208390
910E 208390
                                       BEQ PEND
JSR INCY
                                        JSR INCY
                            NOTND
9111 208390
                                        JSR INCY
9114
9117
                                       JMP NOW
LDA ($06),Y
                            PEND
       B106
                    129
                                                                      ; LOOK AT NEXT BYTE
; INC Y-REG
                                        JSR INCY
       C900
                                       CMF #$00
BNE NOTND
                                                                      ; IS THIS THE THIRD 0?
; NO-THE PROGRAM IS NOT ENDED
911C:
911E DØEE
                    133
134
                                       JMP QEND
LDA ($06),Y
                                                                      ; YES - IT'S ALL OVER
; GET NEXT BYTE
9120 402292
9123 B106
9125 C900
9127 F02E
                            LOAD
                                       CMF #$00
BEQ NUMDUN
                     135
                                                                      : YES THE STATEMENT IS ENDED

: INC Y-REG

: IS IT A 'COLON'?

: YES THE STATEMENT IS ENDED
                     136
9129 208390
912C C93A
                                       JSR INCY
CMP #$3A
                     138
912E
       F027
                                       BEQ NUMDUN
CMP #$20
9130 C92C
                    149
                                                                      : IS IT A COMMA?
       F000
                                        BEG COMMA
9134 R6EF
                    142
143
                                                                      ; GET THE STATEMENT BUFFER OFFSET
                                       LDX SEF
9137 E930
                     144
                                        SBC #$30
                                                                      ; DECMALIZE THE TOKEN
                                       PAG
STA $95F@,X
INC $EF
9139
9139 9DF095
                    145
146
                                                                      ; PUT IT ASIDE
; ADD 1
                                                                      GO GET ANOTHER BYTE
COMMA MEANS LINE NUMBER ENDED
 913E 4C2391
                                       JMP LOAD
DEC $EF
                            COMMA
9143 206391
                                        JSR CONU
LDA #≸01
                                                                          GO CONVERT STMT #
9146 A901
9148 85EF
                                                                      # REINIT
                     152
                                        STA #FF
914A B106
                                        LDA ($06),Y
                                                                      ; GET ANOTHER BYTE
                     154
                                                                      : IS IT 0?
: YES - LINE IS DONE
9140 0900
                                        CMP #$80
 914E FØ10
                     155
                                        BEQ COMFIN
                                                                       ; YES - LINE IS DONE
; IS IT COLON?
; YES - LINE IS DONE
; NO - CONTINUE ON
; ALL OF # IS FOUND
9150 C93A
9152 F00C
                     156
157
                                       CMP #$3A
BEQ COMFIN
                                        JMP LOAD
9154 402391
9157 C6EF
                            NUMBUN DEC SEF
                     159
9159 206391
9150 A901
                                        JSR CONU
                                                                          GO CONVERT STMT #
                     161
                                       LDA #$01
                                                                      : REINIT
                            STA $EF
COMFIN JMP NOW
 915E S5EF
9160 4CB090
                                                                      ; BEGIN SEARCH OF NEXT LINE
                     163
                                                                      ; PUT Y-REG ASIDE
; GET $95F0 OFFSET
9163 84FC
9165 86EF
                                       STY #FC
                                       LDX SEF
                     165
                                                                      ; SAVE IT
9169 8900
                                        LDR #$00
STR $FE
                     167
9166 85FE
916D 35FA
                     169
                                        STA #FA
 916F A6FF
                            GCI
                                                                      ; GET F1RST DEC DIGIT
; DECREMENT OFFSET
; INCREMENT COUNTER
9171 BCF095
9174 C6FF
                    171
172
                                        LDY $95F0,X
DEC $FF
9176 E6FE
                                        INC SEE
9178 C000
                                        CPV ##00
                                                                      : IS THE DEC # 07
```

```
Listing 1 (continued)
9178 F019
917C R6FE
                                                               BEQ NEXT
                                                                                                                 ; YES - GET NEXT
                                                               LDX $FE
LDA $9500,X
                                                                                                                 ; NO - INC COUNTER
; GET APPROPRIATE DEC/HEX CONSTANT
 917E BDD095
9181 88
9182 C000
                                 178
                                             CONTA
                                                                                                                      COUNT DOWN 1
                                                                                                                 ; IS Y 8?
; YES - GET NEXT
; NO - CONVERT
; CONVERSION OF CONSTANT
                                                               CPY #$00
                                 179
9184 F00F
9186 18
9187 7DD095
9188 B003
                                                               BEQ NEXT
                                  181
                                                               CLC
                                                              ADC $95D0,X
BCS INCR
JMP CONTA
INC $95E0
                                 183
                                                                                                                      IF OVERFLOW BRANCH
918C 4C8191
918F EEE095
                                                                                                                     CONTINUE
RDD 1 TO HEX MSB
CONTINUE
                                 135
                                             INCR
9192 408191
                                                                JMP CONTA
9195 18
                                  187
                                             NEXT
                                                               CLC
                                                                                                                  GET NEXT DIGIT
 9196 6DDØ95
                                                               ADC $9500
                                 183
9199 8DD095
919C B028
                                                               STA $9500
                                                                                                                 STORE RESULT
                                                                                                                JOSEPH STORE RESULT
JOSEPH SUBSTRANCH
JOSEPH SUB
                                                              BCS INCS
LDX SEF
                                 190
919E R6EF
                                             CONTC
91A0 BCF095
                                                              LDY $95F0,X
                                 192
9183 C6EF
9185 E6F8
                                 194
                                                               INC $FA
9187
                                 195
                                                               PAG
9187 C000
9189 F034
                                 196
197
                                                                                                                 : IS Y 0?
: YES - QUIT
: NO - GET POINTER
: GET DEC -> HEX CONSTANT
                                                               BEQ OUT
91AB A6FA
                                                                LDX ≸FA
91AC: BDE095
                                                               LDA $95E0,X
9180 88
9181 0000
9183 F017
9185 18
                                  200
                                             CONTE
                                                               DEV
                                                                                                                  ; DECREMENT COUNTER
; IS V 0?
                                 291
                                                               CPY ##00
BE0 ADDM
                                                                                                                 : YES - EXIT
: NO - CONVERT
                                                               CLC
ADC $95E0,X
9186 7DE095
9189 8003
                                 204
                                 205
                                                                                                                      BRANCH IF OVERFLOW
 91BB 40B091
                                 206
                                                                JMF CONTB
                                                                                                                  ; CONTINUE
91BE 18
                                  207
                                              INCH
918F A902
                                 208
                                                              LDA ###O
                                                                                                                 ; GET ERROR CODE
9101 85F9
9103 409190
                                                                                                                 ; HEX > #FFFF
; GO FRINT ERROR MSG
                                 210
211
212
                                                                JMP ERROR
9106 EEE095
9109 409E91
                                                               INC $95E0
                                              INCS
                                                                                                                  ; ADD $1 TO MSB
                                                                JMF CONTO
                                                                                                                  ; CONTINUE
9100 18
                                                               CLC
91CD 6DE095
                                                               ADC $95E0
BCS INCB
9100 8006
9102 80E095
9105 4CDF91
                                                                                                                  # BRANCH IF OVERFLOW
                                                               STA $95E0
JMP OUT
                                 216
                                                                                                                      STORE RESULT
                                                                                                                      CONTINUE
91D8 A901
91DA 85F9
                                            INCB
                                                               LDA ##01
STA #F9
                                 218
                                                                                                                 # ERROR #1
                                 219
91DC 409190
91DF ASFF
                                                               JMF ERROR
                                                                                                                 # GO PRINT ERROR MSG
                                 221
                                             OUT
                                                               LDA #FF
                                                                                                                 ; ARE WE DONE?
91E1 C900
91E3 F005
                                                               CMF #$00
                                                               BEQ WRITE
LDA #≸00
                                                                                                                 ; YES -
91E5 A900
91E7 4C6F91
                                                                                                                  ; NO
                                 225
226
                                                               JMF GO
LDA #$09
91ER R909
91EC 851F
                                              WRITE
                                                                                                                  ; SET UP POINTERS + OFFSETS
                                                              STA $1F
LDY $FB
LDA $9500
STA ($08),Y
91EE 64FB
91F0 ADD095
                                                                                                                     GET LSB OF HEX LINENUM
91F3 9108
                                 230
                                                                                                                  ; STORE HEX VALUE
; INCREMENT Y
91F5 208390
91F8 ADE095
                                                                JSR INCY
                                                                                                                     GET MSB OF HEX LINENUM INCREMENT Y
                                                               LDA $95E0
91FB 9108
91FD 8509
                                 233
234
                                                               STA ($08),Y
                                                              LDA $09
CMP #$8E
BNE OKNOW
CFY #$FF
                                                                                                                     LOOK AT MSB OF ADDRESS
CHECK IF ROOM LEFT
91FF C98E
9201 D00B
                                 236
237
                                                                                                                      YES
9203 COFF
9205 DOO7
                                                                                                                  ; CHECK IF ROOM LEFT
                                  238
                                                                BNE CKNOW
 9207
             A903
                                  239
                                                               LDA ##03
                                                                                                                  # ERROR CODE
9209 85F9
                                 246
                                                               STA $F9
JMP ERROR
 920B 4C9190
                                 241
                                                                                                                 ; GO PRINT ERROR MSG
920E 208390
9211 84F8
                                                               JSR INCV
STY #FB
                                             OKNOW
                                                                                                                  ; RESET POINTERS
9213 A907
                                 244
                                                                LDA #$07
 9215
                                  245
                                                                PAG
                                                               STA $1F
LDY $FC
LDA #$00
STA $9500
 9215 851F
                                  246
9217 64FC
9219 6900
                                  248
                                                                                                                  ; GET ZERO
; STORE IN LSB REGISTER
; STORE IN MSB REGISTER
                                                                STA ≸95E0
RTS
 921E 8DE095
                                  259
                                  251
 9221 60
                                                                                                                   ; RETURN
                                                                                                                  ; END OF TABLECREATE
; PRINT MESSAGE TO SCREEN
 9222
            84FC
                                              QEND
                                                                STY #FC
  9224 A9C5
                                                                LDA #$05
                                                               JSR $FDF0
LDA #$CE
JSR $FDF0
LDA #$C4
JSR $FDF0
LDA #$A0
 9226 20F0FD
9229 A9CE
                                 254
255
 922B 20F0FD
922E A9C4
 9230 20F0FD
9233 A9A0
                                 258
259
                                                               JSR $FDF0
LDA #$CF
JSR $FDF0
LDA #$C6
 9235
             20F0F0
 9238 A9CF
                                  261
            20F0F0
8906
 923A
 923D
                                  263
 923F
             20F0F0
                                                                JSR ≸FDF0
 9242 6960
                                                               LDA ##A@
JSR #FDF@
LDA ##D@
JSR #FDF@
                                  265
             20F0FD
                                 266
             A9D0
 9247
                                  267
             20F0FD
                                  268
                                 269
270
                                                                LDA #$C1
JSR $FDF0
 9240
             A9C1
 924E
             20F0FD
 9251
             A903
                                                                LDA ##D3
             20F0F0
                                                                JSR ≴FDF0
```

(Continued)

This example illustrates the basic action of the program COMPRESS.

Why COMPRESS Works

Applesoft programs are models of efficiency. There is no extraneous space or character storage in memory. Each command, character, or number is represented within the computer by a token. The token for the command PRINT is "BA", a hexadecimal number equivalent to the decimal value 186. There are 234 different tokens that Applesoft can recognize, represented by hexadecimal numbers \$00 through \$EA. (Following the normal convention, we will refer to all hexadecimal numbers with a prefix of '\$' so they can be distinguished from normal decimal numbers.

Continuing with the example PRINT (\$BA), use of the token allows storage of the command PRINT in just one byte, rather than spread out over five. In effect, a great deal of optimizing has already been done in the design of the Apple!

Other computers carry blanks into the program memory, but the Apple only does this with strings, where each blank is significant. The token representation for the words "CUSTOMER NAME" would be:

43 55 53 54 4F 4D 45 52 20 4E 41 4D 45

In this case, since the programmer desires the string typed out exactly as entered, no changes would be acceptable within it, and Applesoft makes none.

Despite the fact that the Applesoft programs are already condensed in memory, additional optimizing is possible. Between each line in memory, for example, the computer stores the value \$00. This null character, called a delimiter, tells the Applesoft interpreter that the previous line has ended. After \$00, two bytes indicate the address in memory of the statement following the next statement. These bytes (\$0A \$08 in the example at the beginning) are stored in low-byte/high-byte format. The actual address in this example is therefore \$080A.

The line number is stored in two bytes, again with the least significant byte first. Ever wonder why the largest line number which can be used in Applesoft is 63999? One reason is that two bytes can describe a maximum of \$FFFF integers — the equivalent of decimal 65535.

Each Applesoft statement, then, requires five bytes — a null byte, two address bytes, and two line-number bytes.

As noted in the Applesoft manual, it is possible to put two or more statements together by separating each with a colon (:). If two statements are thus joined, only one byte is used (\$3A, the token for the colon). Separate lines would require four additional bytes. Applesoft imposes only the limitation that no line can have more than 255 characters.

Applesoft BASIC contains other opportunities for optimization. For example, COMPRESS eliminates the unnecessary LET, thus saving one byte. And COMPRESS will delete any variable name after NEXT. This could save many bytes per line. REM statements can also be dropped, unless of course some branching statement jumps to a line number that happens to be a REM statement (a bad programming habit).

Even though Applesoft is a very efficient language, there is ample opportunity for further reduction in memory use when storing programs.

How COMPRESS Works

The first step in compressing a program is to look for called line numbers. COMPRESS will eliminate lines by appending them to earlier lines. But it is important to preserve lines that are referred to elsewhere in the program with commands such as GOTO, GOSUB, etc., to permit proper transfer of control. Therefore, the first action COMPRESS takes is the creation of a Called Line Number Table.

The following Applesoft commands refer control to another line number:

IF ... GOTO GOTO GOSUB LIST ON ... GOTO DEL

COMPRESS searches the program for these commands. Line numbers that are identified in these statements are stored in a Called Line Number Table at memory locations \$8D00 to \$8EFF. When compression takes place, this table is checked so no line number that appears in the table as a called line number will be eliminated.

The table will contain all of the called line numbers in the order in which they appeared in the original program.

Listing 1

LIST	ng 1						INITIALIZE UALUES FOR POINTERS AND OFFSETS FOR COMPRESSION PORTION OF PROGRAM READ BYTE OF A/S PROG IS IT 0? YES - TEST IT NO - WRITE !T DID COMPRESS' JUST START? NO YES - STORE LOCATION OF A/S ADDRESS REFERENCE IN COMPRESSED STATEMENT RESET INDICATOR GET ANOTHER BYTE IS IT 0 ALSO? NO - GO ON YES - CHECK FOR END GET ANOTHER BYTE IS THIS NEW LINE? YES GO STORE NO SAUE LINE NUMBER PUT ASIDE POINTERS TO CHECK CHILLED LINE NUMBER TABLE! ARE WE DONE CHECKING? YES LOOK FOR LSB OF LINE # FOUND IT!
9259	A9A0	274		LDA	#\$fi0		
9256	20F0FD	275		JSR	\$FDF@		
925E	A9A3	276		LDA	##A3		
9260	20F0FD	277		JSR	\$FDFØ		
9265	20F0FD	279		JSR	\$FDFØ		
9268	2086FD	280		JSR	\$FD8B		
926B	A900	281	PGMWRT	LDA	#\$00	;	INITIALIZE VALUES FOR
9260	H8 9594	282		CTO	*04	÷	POINTERS AND OFFSETS
9270	85FF	284		STA	≱⊍© \$FF	•	PORTION OF PROGRAM
9272	35EB	285		STA	\$EB	•	OKTON OF PROGRAM
9274	85EF	286		STA	\$EF		
9276	8506	287		STA	\$ 06		
9276	850E	288		SIH	¥CE ¢G≎		
927C	8DFE8F	290		STA	\$SFFE		
927F	A93A	291		LDA	#\$3A		
9281	8DFF8F	292		STA	\$8FFF		
9286	8507	293		STA	# \$ 03		
9288		295		PAG			
9288	85EC	296		STA	\$EC		
928A	R907	297		LDA	#\$07		
928C	851F	298		STR	\$1F		
9290	85CF	300		STA	#≠or \$CF		
9292	A903	301		LDA	#\$03		
9294	85FA	302		STA	\$FA		
9296	B106	303	CMPPB	LDR	(\$06),Y	;	READ BYTE OF A/S PROG
929B	200390 C900	305		CMP	#\$00		IS IT 02
929D	F003	306		BEQ	TSTS	;	YES - TEST IT
929F	4C9E93	307		JMP	PROG	;	NO - WRITE IT
9282 0004	A6FA	30 8	TSTS	LDX	\$FA		
92H4	DOOL.	310		DNE	# \$ 03	:	DID COMPRESS JUST START?
92A8	8419	311		STY	\$19	:	YES - STORE LOCATION OF
928A	R4EC	312		LDY	\$EC	į	A/S ADDRESS REFERENCE
92AC	841A	313		STY	*1A	į	IN COMPRESSED STATEMENT
92RE	M419	314		LDY	\$19 ##90		DECET THETCOTOR
92B2	86FA	316		STX	\$FA	•	RESET INDICATOR
9284	841B	317	TSTY	STY	\$1B		
92B6	C618	318		DEC	\$1B		
9288	200790	319		LDA	(\$06),Y	;	GET ANOTHER BYTE
92BD	C900	321		CMP	#\$00	:	IS IT A BISO2
926F	D003	322		BNE	GOON	í	NO - GO ON
92C1	409293	323		JMP	REND	;	YES - CHECK FOR END
92C4	208390	324	GOON	JSR	INCY		
9209	208390	325		JSR	INCV	į	GET HNUTHER BYTE
92CC	R6FF	327		LDX	\$FF		
92CE	E000	328		CPX	#\$00	;	IS THIS NEW LINE?
9200	F003	329		BEQ	STRA	;	YES GO STORE
9202	951D	330	STER	STE	MDIK 41D	:	NU SOUR I THE NUMBER
9207	84FD	332	NSTR	STY	\$FD	6	PUT ASIDE POINTERS
9 2D9	A08D	333		LDY	#\$8D	;	TO CHECK CALLED LINE
920B	84D7	334		STY	\$D7	;	NUMBER TABLE!
9200 920F	861F	335		STY	#\$U/ \$1E		
92E1	FI000	337		LDY	#\$00		
92E3	C4FB	338	ANSTR	CPY	\$FB	;	ARE WE DONE CHECKING?
92E5	F011	339		BEQ	ENDTST	;	YES
92E7	D106	340	NOMHT	CMP	(\$D6),Y	:	LOOK FOR LSB OF LINE #
92EB	208390	342		JSR	INCY	•	FUOND IT:
92EE	C4FB	343		CPY	\$FB	į	ARE WE DONE?
92F0	F006	344		BEQ	ENDTST	;	YES
92F2	000700	345		PAG			
92FE	405390	346		JSR	INCY		NO - CONTINUE OFFICERS
92F8	A609	348	ENDTST	LDX	\$09	•	HO - CONTINUE CHECKING
92FA	E407	349		CPX	\$ D7	;	CHECK LIMITS OF CALLED
92FC	D0E9	350		BNE	NOMAT	;	LINE # TABLE
9276	6202	351		LDY	##07		
9302	861F	353		STX	\$1F		
9304	B106	354		LDA	(\$06),Y	;	GET ANOTHER A/S BYTE
9306	208390	355		JSR	INCY		
9309	FOOG	356		LDX	\$FF		IC THRICOTOR CLEORO
9300	F003	358		BEQ	STAE	;	YES
930F	409692	359		JMF	CMPPB	;	NO - GET A BYTE
9312	851E	360	STRE	STA	\$1E	:	STORE MSB OF LINE #
9316	85FA	362		STA	##64 \$FA	,	CHINGE PUINTER
9318	409692	363		JMP	CMPPB	;	GO GET NEXT BYTE
931B	208390	364	STRB	JSR	INCY	•	
931E	84FE	365		STY	\$FE	;	SET UP POINTERS FOR
9322	R207	367		FDX	#≠07	į	MSR OF LINE NUMBER
9324	861F	368		STX	\$1F	•	or Earle Horiber
9326	851C	369		STA	\$1C		ARE WE DONE CHECKING? YES LOOK FOR LSB OF LINE * FOUND IT! ARE WE DONE? YES NO - CONTINUE CHECKING CHECK LIMITS OF CALLED LINE * TABLE GET ANOTHER A/S BYTE IS INDICATOR CLEAR? YES NO - GET A BYTE STORE MSB OF LINE * CHANGE POINTER GO GET NEXT BYTE SET UP POINTERS FOR ADDITIONAL CHECKING OF MSB OF LINE NUMBER GET A BYTE OF A/S
9328	206200 B100	370		LDFI	(¥UB),Y TNC∪	;	UET FIBYTE OF A/S
932D	A6FF	372		LDX	\$FF		

Listing 1 (continued)

```
932F E000
9331 F003
                                            CPX #$00
                                                                                ; IS INDICATOR CLEAR?
                        374
                                            BEG STAF
JMP CNTXX
                                                                                  YES -
9333 4C3893
9336 851E
                       375
376
                                STAF
                                            STA $1E
                                                                                ; PUT MSB AWAY
9338 84FD
                                            STY SFD
                       377
                                CNTXX
                                                                                START LOOKING FOR
933A A2D7
                       378
379
                                            LDX #$D7
STX $1F
                                                                                ; A MATCH OF THE MSB
; OF THE LINE NUMBER
933C 861F
933E #4FE
9340 D1D6
                                            LDY #FE
CMP (#D6),Y
                       380
                       381
9342 F01B
9344 208390
                       382
383
                                            BEG SET
JSR INCY
9347 84ED
9349 R007
                       384
385
                                            STY SED
LDY #$07
                                            STY $1F
LDY $FD
934B 841F
934D FI4FD
                        387
                                            JSR DECY
STY $FD
LDY #$D7
STY $1F
LDY $ED
934F 208893
9352 84FD
                        389
9354 A@D7
9356 841F
                        390
                       391
                       392
393
394
9358 A4ED
935A A510
                                            LDA $10
JMP ANSTR
935C 4CE392
935F,
935F
                       395
                                            PRG
935F A4FD
9361 A207
9363 861F
9365 A205
9367 86FA
9369 A6FF
                       396
397
                                            LDY #FD
LDX ##07
                                SET
                                                                                # RETURN WITH A MATCH
                       299
298
                                            STX $1F
                                            LDX #$05
STX $FR
LDX $FF
CPX #$00
                                                                                # RESET FLAG
                       401
                                                                                IS THIS A NEW LINE?
926D
926E
        D662
E666
                                            BNE GOGO
JMP CMPPB
                       403
936F 409692
9372 A961
                                                                                   YES - GET ANOTHER BYTE
                                            LDA ##01
STA #FA
JSR DECY
JSR DECY
                        405
                               G060
                                                                                # RESET FLAG
9374
9376
        85FA
208693
                       407
                                                                                # BACK UP POINTER
9379
         208893
9377 208893
937F 208893
9382 208893
9385 4CD894
9388 88
9389 COFF
                                            JSR DECY
JSR DECY
JSR DECY
JMP CERTA
                       409
                       410
                                                                                # MATCH MADE - GO WRITE LINE
                       413
414
                                            DEY
CFY ##FF
                                                                                   SUBROUTINE TO DECREMENT Y REG AND CHECK IF PAGE
                               DECY
938B D004
938D A61F
938F D600
9391 60
                                            BHE RETHH
                                                                                # BOUNDARY IS CROSSED
                                            LDX $1F
DEC $00,X
RTS
                       416
417
418
                                RETHN
                                            LDA ($06),Y
CMP #$00
BNE GONO
JMP FEND
                                                                                J GET ANOTHER BYTE
J IS THIS ONE 0 ALSO?
J NO - IT IS NOT THE END
J YES - PROGRAM ENDED
9392 B106
9394 C900
                       420
9394 C900
9396 D003
9398 4C6995
939B 4CC492
939E 8DFE8F
9381 C938
9383 F00D
                       423
424
425
                                             JMP GOON
                                                                                   GO ON
                                GONO
                                                                                # CHECK TO SEE IF WE
                                            STA $8FFE
CMP #$3A
BEQ FLAG
                                PROG
                       426
                                                                                   YES
                                           LDA $8FF.
CMP #$3A
REQ CLRFLG
*8FFE
9385 ADFF8F
9388 C938
9388 F02C
                                                                                # NO-CHECK THIS ADDRESS
                       428
429
93AF ACE093
93AF 4CE093
93B2 ADFF9F
93B5 C93A
93B7 D00D
93B9 B106
                                            LDA $8FFE
JMP ONWARD
                        430
                       432
433
                                            LDA $8FFF
CMP #$3A
                                                                                ; CHECK TO SEE
; IF WE HAVE A COLON
                                FLAG
                                            BNE SETZ
LDA ($06),Y
CMP #$00
BEQ RNGFIX
                       434
435
                                                                                I NO
                                                                                J YES GET ANOTHER BYTE
93BB C900
                       436
437
                                                                                1 IS IT 02
                                                                                 : YES
93BF 4C9692
93C2 CR
                                                   CMPPB
                                                                                 # NO GO CONTINUE
                        439
                                RNGFIX DEX
                                                                                J BACK UP ONE
9302 CH
9303 409494
9306 B106
9308 0900
9300 0003
9300 409494
                       440
441
442
                                             JMP CNTAX
                                            LDA ($06),Y
CMP #$00
BNE SETZ1
JMP CNTAX
                                SETZ
                                                                                : GET NEXT BYTE
                                                                                   IS IT 07
                       443
444
                                                                                YES
93CF ROFESF
                       445
                                            PAG
LDA #8FFE
                               SET21
                       446
447
                                                                                # SET FLAG
93D2 8DFF8F
93D5 4CF893
                                            STA
                                                   $8FFF
PROGWT
                                                                                ; WRITE COMPRESSED CODE
9308 A900
930A 80FF8F
                       449
450
                                CLRFLG
                                            LDA #$00
                                            STA $8FFF
LDA $8FFE
CMP #$AB
9200 BDEE8E
                                            LDA
                                DNIJARD
                                                                                : IS IT 'GOTO'?
                       452
93E0 C948
93E2 F039
93E4 C94D
93E6 F050
93E8 C981
93EA F04C
93EC C982
                                            BEQ GOTO
                                                                                  IS IT 'IF'?
                       454
455
                                            CMP #$AD
BEQ IF
                       456
457
                                                   #$B1
                                                                                : IS IT 'RETURN'?
                                            CME
                                            BEG IF
                                                                                ; IS IT 'NEXT'?
                                            CMP ##82
BEQ NXTT
                       458
93EE F053
                       459
                                                                                ; YES
                                            CMP #$AA
BEQ LET
CMP #$B2
BEQ REM
93F0 C9AA
93F2 F066
                                                                                ; IS IT 'LET'?
                                                                                   YES
                       461
93F4 C9B2
93F6 F06A
93F8 9D00SF
93FB 861C
93FD A6FA
93FF E006
                                                                                   IS IT 'REM'?
                                                                                  YES
                       463
                               PROGWT
                                            STR $8F00.X
                                                                                   STORE THE BYTE - NOT A TERM CMD
                                                                                  PUT X ASIDE
GET FLAG
                       465
                                            STX $10
                       466
467
                                            LDX $FA
                                                                                ; IS IT 6?
; YES
; GET X BACK
                                            CPX #$06
9401 F005
9403 R610
                                            BEQ ELIM
                        469
                                            LDX $10
                                                                                ; SUBROUTINE TO ELIMINATE
9408 R201
                       471
                               ELIM
                                            LDX ##@1
                                                                                                                     (Continued)
```

Consider the following program:

10 GOTO 50 50 END

A simple concatenation would be:

10 GOTO 50 : END

Any attempt to run the shortened program would result in an Applesoft error. Consider a slight modification of this example:

10 GOTO 50 20 J = 5 50 END

By appending the program together into one statement, it would appear as:

10 GOTO 50 : J = 5 : END

Again there would be an Applesoft error. Note that the first part of the statement [GOTO 50] immediately transfers control to a later line number.

Assume for a moment that a called line number table has been constructed, and the 'GOTO 50' statement identified. Recognizing that line 50 could not be eliminated, COMPRESS would preserve it. After compression the program would look like this:

10 GOTO 50 : J = 5 50 END

While the execution of this short program would not create a computer error, the transfer of control to line 50 takes place *before* the 'J = 5' is executed. So 'J = 5' would *never* be executed at all.

Another condition that the compression program must address is Terminal Commands. Commands that not only transfer control, but do it irrevocably (as shown in the example above), are called Unconditional Terminal Commands. Commands that may or may not transfer control are called Conditional Terminal Commands. Unconditional Terminal Commands include:

GOTO
ON ... GOTO
RETURN
REM (see the discussion later in the article on REM)

Conditional Terminal Commands include:

IF ... GOTO
IF ... THEN GOTO
IF ... THEN ...

Conditional commands behave in a special way. For example:

Executed without compression, the $^{\prime}C=6^{\prime}$ assignment will be executed regardless of the outcome of statement 10. If these lines are concatenated, they appear as:

10 IF
$$A = 2$$
 THEN $B = 5 : C = 6$

Applesoft will evaluate the 'IF A = 2' part of the line. If it is false, then no further action will take place on that line; control will immediately fall through to the next line. Conditional Terminal Commands are different from Unconditional Terminal Commands because control is transferred immediately only if the condition in the statement is false. Nevertheless, COMPRESS cannot append any subsequent statements to terminal commands of any type, because doing so may subvert the programmer's intent.

It is legal, however, for the programmer to concatenate many statements after a conditional command. This is a method of saving space and speeding up execution time, but it can only be done by the programmer. COMPRESS cannot possibly know the initial intent. A Conditional Terminal Command then, is one where control may or may not be passed to the next statement, or to some numbered statement earlier or later in the program.

COMPRESS takes two actions with respect to conditional commands. First, any referenced line number is recorded in a Called Line Number Table so that the line number will not be eliminated. Second, as with any terminal command, there will be nothing appended to the Conditional Terminal Command, unless the programmer added something in the initial program. COMPRESS always appends the last statement to the line. Unconditional Terminal Commands will be treated in exactly the same manner.

REM Statements

Applesoft treats REM in a special way. When a REM token (\$B2) is encountered in a program, everything after the REM is considered part of the remark until the \$00 delimiter is encountered. The statement

10 REM THIS IS A REMARK: J = 10

would not be evaluated past the first

Listing 1 (continued)

	· (COL	umue	,,,				EUERYTHING AFTER "REM" GET ANOTHER BYTE IS IT 0? NO - LOOP YES - REM IS DONE PUT BYTE ASIDE IS X 0? YES NO - BACK UP GET LAST BYTE WAS IT 'THEN'? NO YES RESET X AGAIN RESET FLAG GET ACC BACK GO WRITE BYTE PUT BYTE ASIDE RESET FLAG GET BYTE BACK GO WRITE BYTE PUT BYTE ASIDE RESET FLAG GET BYTE BACK GO WRITE BYTE PUT BYTE ASIDE GET NO UAR NAME IS IT COLON? YES - NO UAR NAME GO GET ANOTHER GET BYTE BACK GO WRITE BYTE GOT ANOTHER RESET FLAG GET X BACK IS IT A NEW LINE? YES NO - ROUANCE 1 GO WRITE BYTE GET X BACK GET RNOTHER BYS BYTE IS IT 0? YES GO SEE IF END ADUANCE POINTER CONTINUE IS THIS A NEW LINE? NO RESET FLAG GO GET ANOTHER BYTE BACK UP X REG GET ANOTHER BYS BYTE BACK UP
940A	86FA	472		STX	\$FA	;	EVERYTHING AFTER "REM"
940C 940E	R61C	473		LDX	\$1C		
9411	208390	475	EL IM2	JSR	INCY		
9414	B106	476	ELIM3	LDA	< \$06 >.∀	;	GET ANOTHER BYTE
9418 9418	DOF7	477		RNE	#\$00 Film2	;	IS IT 0?
941A	4C9594	479		JMP	CNTRR	;	YES - REM IS DONE
941D	85ED	480	GOTO	STA	\$ED	;	PUT BYTE ASIDE
9421	F88C	482		BEQ	THRU	:	IS X 67? YES
9423	CA	483		DEX		;	NO - BACK UP
9424 9427	E0008F C9C4	484 485		CMP	\$8F00.X	:	GET LAST BYTE
9429	2000	486		BNE	INCC	;	NO
942B	4C2F94	487	****	JMP	THRU	;	YES
942F	E8 A901	488	THRU	INX	#\$91	;	RESET X AGAIN
9431	85FA	490		STA	\$FA	,	RESET FERG
9433	ACEDO7	491		LDA	\$ED	;	GET ACC BACK
9438	85ED	493	IF	STA	\$ED	;	BUT BYTE ASIDE
943A	A901	494		LDA	#\$01	;	RESET FLAG
943C	9550	495		PAG	≠ E6		
943E	ASED	497		LDA	\$ED	:	GET BYTE BACK
9440	4CF893	498		JMP	PROGWT	;	GO WRITE BYTE
9445	B106	499 500	NXTX	STH LDA	\$ED (\$96).V	;	PUT BYTE ASIDE
9447	C93A	501	11/1/1/	CMF	#\$3A	;	IS IT COLON?
9449	F886	502		BEQ	LORDM		YES - NO VAR NAME
944D	F006	504		BEO	LOADM	;	15 IT END OF STMT?
944F	208390	505		JSR	INCY	-	TES THE THE
9452	4C4594	506 507	LOSOM	JMP	NXTX *ED	:	GO GET ANOTHER
9457	4CF893	508	LONDIN	JMP	PROGWT	,	GO WRITE BYTE
945A	B106	509	LET	LDB	(\$0€),∀	;	GET NEXT A/S BYTE
945F	40F893	511		JMP	PROGLIT		GO WRITE IT
9462	E000	512	REM	CPX	#\$00	;	IS THIS NEW LINE?
9464	F001	513		BEQ	REMA	:	VES
9467	861C	515	REMA	STX	\$1C	,	PUT X ASIDE
9469	R6FA	516		LDX	≸FA	1	LOOK AT FLAG
946D	DOGE DOGE	518		BNE	#¥U5 REMC:		
946F	A206	519		LDX	#\$06	;	RESET FLAG
9471 9473	86FR	520 521		STX	\$FA		CET V DOCK
9475	E000	522		CPX	#\$00	,	IS IT A NEW LINE?
9477	F001	523		BEQ	REMB	;	YES
9479 9479	40E893	524 525	DEMB	INX	PPOGUT	:	NO - ADVANCE 1
947D	R61C	526	REMC	LDX	\$1C	í,	GET X BACK
947F	B106	527		LDA	(\$06), Ч		GET ANOTHER AZS BYTE
9483	F006	529		BEQ	CHTSB	;	YES GO SEE IF END
9485	208390	530		JSR	INCY	;	ADVANCE POINTER
9488 948B	407094 E000	532	CNTSB	JMP CPX	REMC #\$ØØ	;	CONTINUE
948D	D005	533		BNE	CNTAX	į,	NO NO
948F	86FA 409492	534 535		STX	\$FA	:	RESET FLAG
9494	CA	536	CNTFIX	DEX	airrb	;	BACK UP X REG
9495	B106	537	CNTAA	LDA	(\$06),Y	;	GET ANOTHER A/S BYTE
9497	FØØB	539		REG	##EE	:	MRE WE OUT OF ROOM?
949B	E8	540		INX	D. 101101	•	TES EFFERSI
949C	C900 F016	541 542		CMF	#\$00 ENDEM	:	IS BYTE 0? VES = 60 END
94Fi@	208390	543		JSR	INCY	,	725 - GO END
94F3	409E93	544		JMP	PROG	;	CONTINUE
94A6	208893	546	BACKUP	JSR	DECY	:	BACK UP Y REG
9469	C41B	547		CPY	\$1B	,	
946B	00F9 84FC	548 549		STV	BACKUP **FC	;	CONTINUE SET V ACIDE
94FIF	R201	550		LDX	#\$01	;	RESET FLAG
94B1	86FA 400994	551 550		STX	\$FA		
94B6	EØEE	553	ENDEM	CPX	##EE	;	ARE WE OUT OF ROOM?
94B8	FØEC	554		BEQ	BACKUP	;	YES
94BC	90008F	556		STR	I#\$5H I\$8F00.X	;	GET COLON
94BF	SDFF3F	5 57		STR	\$8FFF	í	STORE IN FLAG
9402 9404	86EE	558 556		STX	\$EE	;	SET X ASIDE
94C5	86FF	560		STX	\$FF		
9407	R6FA	561		LDX	\$FA	;	GET FLAG
94CB	D004	562 563		BNF	・ #季05 END2		TIME TO END?
94CD	A200	564		LDX	#\$00	í	RESET FLAG
940F	S6FA F0A1	565 564	EMDO	STX	\$FA	_	16 61 66 10
9403	FØ03	567	EMUZ	BEQ	CERTA	;	YES GO WRITE LINE
94D5	409692	568	CEC	JMF	CMPPB	;	RESET FLHG GO GET ANOTHER BYTE BACK UP X REG GET ANOTHER A'S BYTE ARE WE OUT OF ROOM? VES - BACKUP IS BYTE 0? VES - GO END CONTINUE BACK UP Y REG CONTINUE SET Y ASIDE RESET FLAG ARE WE OUT OF ROOM? VES GET COLON WRITE COLON STORE IN FLAG SET X ASIDE GET FLAG IS FLAG IS FLAG IS FLAG IS FLAG IS FLAG IS FLAG IS FLAG IS FLAG IS FLAG OVERWRITE LINE NO CONTINUE GET X BACK OVERWRITE POINTER IS X 0?
94DA	86FF	570	CERTH	STX	≱FF	;	GET X BRCK OVERWRITE POINTER
94DC	E000	571		CPX	#\$00	;	I5 × 0?

Listing 1 (continued)

```
94DE D009
94E0 R5FR
94E2 C902
94E4 D003
                                     BNE WRTLN
                                                                  : NO
                                    LDA $FA
CMP #$02
                                                                  GET FLAG
                                     BNE WRTLN
JMP FINAL
                                                                  ; GO WRITE LINE
94E6 4C7195
                   576
                                                                  ; GO END PROGRAM
94F9 84FC
                   577
578
                          WRTLN
                                     STY $FC
                                                                  # SET Y ASIDE
94EB R4EF
                                     LDY SEF
94ED A9EC
                                     LDA #$EC
94EF 851F
                   580
                                     STA $1F
94F1 R200
94F3 8R
                   581
                                    LDX #$00
94F4 91EB
                   583
                                     STA ($EB), Y
                                                                  ; WRITE COMP A/S PROG
94F6 208390
94F9 208390
                   585
                                     JSR INCY
94FC 203390
94FF 851D
                   587
                                     LDR $1D
                                                                  ; GET LINE NO
9501 91EB
9503 208390
9506 A51E
                                     STA ($EB),Y
                                                                 ; STORE IT ; GET MSB OF LINE
                   589
                                     JSR INCY
                   590
                                     LDA $1E
                                                                  ; AND STORE IT ALSO
9508 91EB
950A 208390
950D BD00SF
                                     STA
                                          ($EB),Y
1NCY
                   592
593
                                     JSR
                                    LDA $8F00.X
STA ($EB).Y
                          GONOW
                                                                  ; START LOADING COMPRESSED
9510 91EB
9512
                                                                  ; LINE AND STORING IT
                   595
                                    PAG
CPX $FF
9512 E4FF
9514 F007
                                                                  ; IN LOMEM
                   597
                                     BEO BAKHOW
9516 208390
9519 E8
                                     JSR INCY
                                     INX
9518 4C0D95
951D 8900
                   600
                                          CONOM
                                     JME
                                                                  ; CONTINUE
                   601.
                          BAKNOW
                                     LDA
                                          #$00
                                                                  ; WRITE A $00 DELIMITER IN
; THE PROGRAM TO END LINE
951F 91EB
                   692
                                     STA ($EB),Y
9521 84EF
9523 208390
9526 98
9527 8018
                                     STY
                                          ≸EF
                   604
                                     JSR INCY
                                     TYA
                                    LDV #$1A
STV $1F
                   606
9529 841F
952B 8000
                                    LDY #$00
STA ($19),Y
                   608
952D 9119
952F 85FD
                   609
                                                                  ; BLANK ADDRESS
                  610
611
                                    STA #FD
JSR INCY
9531 208390
9534 RSEC
                   612
613
                                    LDA $EC
STA ($19),Y
9536 9119
                                                                  ; WRITE ADDRESS
9538 A907
                   614
615
                                     LDA ##97
953A 851F
                                     STA $1F
953C R200
953E 86FF
                   616
617
                                     LDX #$00
                                     STX $FF
9540 86EE
9542 84FC
                                     STX SEE
                                     LDY #FC
                   619
9544 A6FA
9546 E001
                                     LDX $FA
                                                                 # CHECK FLAG
                   621
                                     CPX #$01
9548 F007
9548 E002
                   622
623
                                    BEQ LINECG
                                    CPX ##02
BEQ FINAL
954C F023
954E 409692
9551 A5FD
                                          CMPPE
                                                                  ; GO START NEW LINE
                         LINECG LDA SFD
                   626
                                                                  GET A/S ADDRESS
9553 8519
9555 ASEC
9557 851A
9559 ASED
                                     STA $19
                                    LDA $EC
STA $1A
LDA $FD
                   628
                                                                 ; MSB OF A/S ADDRESS
                  629
630
                                                                 S GET FIDDR
955B C900
955D D002
955F C6EC
9561 R900
                   631
                                    CMP #$00
BNE CNTU
                                                                 # ARE WE AT PAGE BONDARY?
                                                                 ; NO
                   634
                          CNTU
                                    LDA ##00
                                                                 ; RESET FLAG
9563 85FA
9565 409692
                                     STR #FR
                  636
637
                                    JMP CMPPB
STY $FC
                                                                 ; GO START NEW LINE
9568 84FC
9568 8902
                          FEND
                                                                 ; SET Y ASIDE
; RESET FLAG
                   638
                                    LDA #$02
STA $FA
956C 85FA
956E 4CD894
9571 F4EF
                                    JMF CERTA
                          FINAL
                   641
9573 A9EC
9575 851F
9577 A900
                                     LDA #$EC
                                    STR $1F
LDR #$00
                                                                 ; GET #$00
9579
                   645
                                    PAG
9579 91EB
957B 208390
957E 91EB
9580 208390
                                    STA (#EB),Y
                                                                 ; WRITE THREE BYTES
                                    JSR INCY
STA (#EB),Y
                                                                 ; A/S PROGRAM
                                    JSR INCY
9583 91EB
                                    STA ($EB),Y
9585 208390
                   651
652
                                    JSR INCY
STY #AF
9588 84AF
                                                                 ; SET PROPER VALUES
                  653
654
                                    STV $69
STV $68
9588 8469
                                                                        ZERO PAGE A/S
958C 846B
                                                                 # POINTERS WHICH
958E 846D
9590 A5EC
                  655
656
                                    STY $60
LDA $E0
                                                                    COMPRESSED PROGRAM
                                                                    WILL NEED WHEN
9592 8580
9594 8568
                  657
658
                                     STA $B0
                                                                 ; RUNNING
                                    STR $68
9596 8560
9598 856E
                   659
                                    STA ≴60
                   66.0
                                    STA $6E
959A A9C5
959C 20F0FD
                                     LDA #$C5
                   662
                                     JSR ≴FDF0
                                                                 ; WRITE (END) ON SCREEN
959F
95A1
      A9CE
20F0FD
                                    LDA #$CE
                                    JSR $FDF@
LDR #$C4
                  664
                  665
9586 20F0F0
9589 20DDFB
                  666
667
                                    JSR $FDF@
JSR $FBDD
95AC
      400003
                  668
                                     JMP
                                                                 # RETURN COMPUTER TO USER
```

REM. Everything, including the colon and the second statement, is considered part of the remark. Nothing can therefore be appended to REM statements; they are considered terminal commands.

REM statements will be entirely eliminated by COMPRESS to save space unless they are on called line numbers. Programmers who want maximum compression should avoid branching to REM statements. COMPRESS cannot eliminate a REM statement if it is the result of a branch from another line; however, it will eliminate everything after the REM (\$B2), reducing the statement to just one token.

Executing COMPRESS

Three steps are required to execute COMPRESS:

- 1. Type BRUN COMPRESS
- When the cursor reappears, LOAD your Applesoft program.
- 3. When the cursor appears again, type CALL 36875.

COMPRESS will respond with:

END OF PASS #1 END

Your program is now compressed! To see how much memory-saving compression took place, try it again, but follow these steps:

Type BRUN COMPRESS

When the cursor reappears, LOAD your Applesoft program.

Type CALL - 151 Type AF.B0

The computer will give you the address of the highest memory location being used by your current program. The high order byte is last, and the low order byte first. If, for example, the computer responds with:

00AF - AA 00B0 - 1B

then the top of your Applesoft program resides at location \$1BAA, and occupies the space between \$0800 (where all Applesoft programs start) and \$1BAA.

Now, type CTRL-C to get back into Applesoft. Again type CALL 36875 to invoke COMPRESS. After COMPRESS is finished, enter the monitor again with a CALL -151, type AF.B0 again, and record the new memory extents.

958E

BSAUE ATOT.PROG.A\$800.L\$600

END

DOM "BSAVE ATOT.PROG.A\$800.L\$600"

By converting these addresses to decimal you can see how effective COMPRESS is! [More about that later.]

One final note on running COM-PRESS. Once you have typed BRUN COMPRESS and the cursor reappears, it is unnecessary to execute this step again as long as the computer is not turned off, or you do not run any other machine-language programs that operate up near HIMEM. Simply type CALL 36875 to invoke COMPRESS. But don't forget that COMPRESS occupies nine pages of RAM memory, which may be valuable space depending on the function you wish to perform. A PR#6 will erase COMPRESS.

The Three Steps of COMPRESS

Step 1: Set HIMEM

Program steps 3 to 7 tell the computer that COMPRESS is in the computer, located just below DOS between locations \$9000 and \$95FF. Also, the space \$8D00 and \$8EFF is reserved for the Called Line Number Table, and \$8F00 to \$8FFF for the Compressed Statement Buffer. HIMEM is an Apple II pointer whose address is stored at location \$73 and \$74 on the zero-page of memory. These two bytes will be set to \$00 and \$8D, respectively, following the format we have been using all along of placing the least significant byte first.

After HIMEM is set, control is returned to the user and the Applesoft cursor [] reappears. The 'JMP \$3D0' accomplishes this. At that location, Applesoft is reinvoked.

Step 2: TABLECREATE

Lines 8 to 62 initialize the memory pointers for COMPRESS. While this may not be necessary immediately, all of the initialization routines are contained here.

Lines 63 to 70 are subroutine INCY, which is used throughout the program to increment the Y register by 1. If the value of the Y register is forced to \$00 (that is, it passes \$FF), then the high-order byte of the sixteen bit address being incremented (which is stored in \$1F and loaded into the X register) is itself incremented with the command INC \$00, X.

Lines 71 to 87 are an exit from the program and contain the ERROR subroutine. Notice that line 87 returns control to the user with a JMP to \$3D0. Prior to that, however, the message ER-

Listing 2: HEXDEC

```
REM HEX CONVERSION FOUTINE
                                                                            LET AD. = UAL (A#)
                                                                            GOTO 520
REM CONVERT THE HEX NUMBER
      PRINT
PRINT
                                                                             TO A DECIMAL NUMBER IF IT WA
S BETWEEN A AND F
      INVERSE
PRINT " HEXADECIMAL DECIMAL C
56
                                                                    386
                                                                            LET 62 = 10
        ONWERSION ROUTINE
                                                                             GOTO 529
                                                                             LET AV. = 11
      NORMAL
                                                                             GOT0 520
      PRIMI
                                                                     420
                                                                             LET A% = 12
GOTO 520
       INPUT "ENTER HEXADECIMAL NUM
                                                                     430
        BER + ":HXX
REM GET THE LENGTH OF THE S
                                                                             LET A% = 13
GOTO 520
                                                                     440
                                                                     450
        TRING TO SEE IF IT IS TOO LO
NG FOR THE CONVERSION.
LET K = LEN (HX#)
                                                                             LET A:
                                                                             GOTO 520
                                                                     470
                                                                             LET AX = GOTO 520
       LET K = LEM (HX%)

REM IF THE STRING IS OVER 5

DIGITS IT IS TOO LONG.

IF K > 5 THEN GOTO 140

GOTO 220

REM PRINT THE ERROR MESSAGE
                                                                     490
                                                                             GOTO 520
GOTO 520
REM ACCUMULATE THE ANSWER.
                                                                     519
                                                                            REM ACCUMULATE THE ANSWER.
LET ANS = ANS + (MPVR * AL.)
135
                                                                     520
          AND BRANCH TO THE RETURN LO
                                                                     540
                                                                             PR INT
        PRINT
                                                                             PRINT
150
        PRINT
                                                                             PRINT
                                                                             REM PRINT THE ANSWER
        PRINT "INPUT STRING TOO LONG
FOR THIS PROGRAM. YOU ARE L
                                                                             PRINT "THE ANSWER IN DECIMAL
FOR YOUR IMPUT STRING IS
- ";ANS
179
                                                                     570
        IMITED TO FIVE DIGITS.
                                                                             G0TO 700
189
        PRINT.
                                                                             PRINT
        PRINT "YOU ATTEMPTED TO ENTE
                                                                    600
200
        G0T0 720
210
                                                                     620
                                                                             PRINT
                                                                             REM INDICATE THAT ONE OF THE CHARACTERS READ IN WAS NOT A VALID HEX DIGIT - I.E. BE
      REM INITIALIZE.
ANS = 0
220
230
       ANS = 0
FOR I = 1 TO K
LET G% = K + 1 - I
LET A$ = MID$ (HX$,G%,1)
REM SET UP THE PROPER MULTI
                                                                             TWEEN A AND F.
PRINT "YOUR INPUT INCLUDED T
HE CHARACTER ";A$
                                                                     630
         PLIER FOR EACH DIGIT OF THE
       HEX NUMBER
LET MPYR = 16 ↑ (I - 1)
REM IF THE HEX VALUE IS NUM
ERIC, IT IS OK TO BRANCH AND
                                                                             PRINT "THIS OCCURRED IN POSI
TION NUMBER ":I
                                                                     650
                                                                             PRINT
                                                                             PRINT "KTHE FIRST POSITION I
                                                                     670
         USE AS IS.
(F VAL (A$) > 0 THEN GOTO
                                                                             S NEXT TO THE OINT.)"
                                                                                                              DECIMAL P
270
                                                                            OINT. F
PRINT "THIS IS NOT A HEXADEC
IMAL DIGIT. PLEASECHECK YOU
R INPUT AND TRY AGAIN."
         36.61
                                                                     680
        REM IF THE VALUE FALLS BETW
EEN A AND F THE DECIMAL NUMB
                                                                     690
         ER MUST BE CALCULATED FROM T
        HE TABLE.

IF As = "A" THEN
                                                                             PRINT
                                       G0T0 380
                                                                             PRINT
        IF H$ = "H" | HEN

IF A$ = "B" THEN

IF A$ = "C" THEN

IF A$ = "C" THEN

IF A$ = "F" THEN

IF A$ = "F" THEN

IF A$ = "G" THEN
                                       G0T0 400
G0T0 420
                                                                             REM CREATE RETURN LOOP.
INPUT "DO YOU WANT TO ENTER
ANOTHER VALUE? - "JUYS
                                                                     720
                                       G010 446
G010 468
310
                                                                              IF UY$ = "Y" THEN GOTO 20
                                                                     730
                                       G010 460
                                                                              END
                                       G0T0 500
        G0T0 598
```

ROR #xx appears on the screen. COM-PRESS recognizes three error messages:

ERROR #01 or ERROR #02: A line number that exceeds \$FFFF has been detected. This can occur during the conversion routine if a partial line number is followed by an illegal code in a branching statement, or a line number is specified that exceeds decimal 65535. Note: line numbers greater than 63999 will generate an Applesoft error. Tokens \$30 through \$39 are the only legal numbers that can be used with a program line to represent a line number. The program (uncompressed) should be RUN to locate the source of the Applesoft error.

ERROR #03: More than 255 Called Line Numbers appear in the program. Note that these do not have to be different numbers to generate an error. More

than 255 repetitions of the same called line number will create an error condition. COMPRESS is limited to the number of 16-bit (two-byte) addresses that it can fit between \$8D00 and \$8EFF, minus 1.

Line 88, which appears severa times throughout the program, loads single byte of the uncompressed Apple soft program. The command 'LDa (\$06), Y' instructs the computer to loainto the accumulator the contents of memory location whose 16-bit (two byte) address is stored starting at loca tion \$06, least significant byte first The address at \$06 and \$07 (initial) \$00 \$08, or \$0800) is offset or indexe by the contents of the Y register, intiall \$0, and incremented by subroutin INCY described above. When Y = A? for example, the address read into th accumulator will be \$08A3.

Lines 90 to 141 evaluate the byte read-in, and if indeed it is a called line number, begin the conversion routine.

Applesoft stores everything as tokens except for addresses and actual line numbers, which are stored in hex. Even called line numbers appear as tokens, occupying the range \$30 to \$39. Applesoft interprets tokens \$30 to \$39 as the decimals 0 through 9 with one byte (token) per digit. The statement 'GOTO 456' would appear as '\$AB \$34 \$35 \$36'. Since the actual line numbers in the program appear as hexadecimal numbers and the called line numbers do not, COMPRESS must go through a conversion routine. The steps required

- a. Subtract \$30 from the token.
- b. If the value of the result is less than zero, then the initial token was less than \$30, and the result could not have been a called line number.
- c. If the value is higher than \$0A (hex for 10), then the token was greater than \$39, and was not part of the line number.

d. The field is thus narrowed down to the tokens that were originally in the range of \$30 to \$39, and that will, after subtracting \$30, create a decimal digit between 0 and 9. The actual conversion of these decimal digits to hex can then begin.

Lines 142 through 220 convert these decimalized tokens to their hexadecimal equivalents. Each digit in a decimal number has a corresponding hex value. The units digit, for example, has a value of \$1 for each decimal 1. Decimal 9 therefore equals \$9. The tens digit has a hex value of \$A for each decimal 1. Decimal 40, therefore, has the same value as A + A + A + A. The hundreds digit has the value of \$64 for each decimal 1: the thousands \$3E8, and the ten-thousands \$2710.

Since the computer can add hexadecimal numbers quite easily, a register is incremented by the hex equivalent of each decimal digit as many times as there is value in the decimal number. For example, the line number 30000 would be converted by incrementing the ten thousands equivalent three times: \$2710 + \$2710 + \$2710.

Lines 221 through 251 write the called line number to the storage area now that it has been converted to hex. Since the line numbers in the program that precede each statement are already hex, the numbers can now be directly matched. This takes place after the entire program is scanned.

Lines 252 through 280 print the message END OF PART #1.

Step 3: APPEND

Lines 281 through 302 initialize pointers and registers for the Append process. Certain values carried over from step 2, Tablecreate, are not changed, but many working registers must be reinitialized for Append.

Lines 303 to 330 read and evaluate each byte of the Applesoft program and take certain action if \$00 appears. One occurrence of \$00 indicates a line delimiter. If two appear, the computer must test to see what the next (third) byte may be. There may be two \$00 bytes because the low-order byte of the address of the next statement in memory happens to be \$00. It is also

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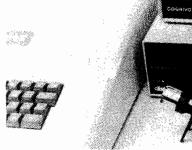
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possible that the computer has encountered the end of the program. This condition is detected in Applesoft by the occurrence of three \$00 bytes.

Lines 331 through 404 search the Called Line Number Table residing at \$8D00 to \$8EFF and preserve the line number if it appears. All elements must be checked since the called line num-

MICRObits

(Continued from page 38)
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bers are stored in the order they occur in the program.

Checking a 16-bit address with an 8-bit processor like the 6502 involves some complexity. The low-order bytes of the Called Line Number Table are checked against the low-order byte of the line number being read. If there is a match, which is quite likely, the high-order bytes are checked. If they match also, the line number must be saved.

Lines 405 through 547 look for terminal commands, REM statements, etc., and act to terminate a line if any are found.

Lines 548 through 554 back up if more than 239 bytes have been written and a natural end-of-line has not been located. In such cases the computer must search backwards until the previous end of statement is encountered.

Lines 579 to 638 write the compressed statement to the normal RAM storage location for Applesoft programs [\$800 upward]. Note that after compression the program is written directly over the original uncompressed program. Since COMPRESS can only shorten a program and can never lengthen it, there is never danger that a program will be written over part of the unread original program.

Lines 639 through 669 terminate the COMPRESS program and write the final pointers into the normal Applesoft zero-page locations, as well as print 'END' on the screen.

Line 670 returns control to the user with a JMP to \$3D0.

Final Notes

COMPRESS works with most Applesoft programs. Certain conventions used by some programmers may make COMPRESS unable to do its job. Some authors have sprinkled \$20 tokens (\$20 is the Applesoft representation for space) into called line number areas of their programs to discourage unauthorized copying. While this is not the only condition that will 'fool' COMPRESS, it is typical of unconventional programming techniques that COMPRESS cannot handle.

Included with this article as listing 2 is an Applesoft program named HEX-DEC. This program can serve two purposes: it is a good test program to run through COMPRESS, and it will also enable the user to convert the addresses located at \$AF and \$BO to

decimal. Then the user will be able to see how much memory usage has been eliminated through the use of COMPRESS.

To use HEXDEC, enter the values located at \$AF.BO [reverse them first of course] and get the decimal equivalent. Subtract the equivalent of \$800 (decimal 2048) to find out how many bytes of storage your program takes. Do the same after a compression run and then calculate the difference.

HEXDEC itself occupies 1906 bytes of storage before compression, and 964 after (\$0F72 - \$800 = decimal 1906: \$0BC4 - \$800 = decimal 964). This amounts to a 49.5% memory reduction!

While admittedly this is an extreme example, which was exaggerated solely to show what COMPRESS can do, the basic idea is not far-fetched. The programmer now has the liberty to write many REM statements and put one statement per line for later editing if necessary.

A wordy original Applesoft program now does not exact an execution penalty. COMPRESS can optimize the code prior to execution. All debugging should of course be done before compression. Leaving the original program intact allows the user to make later changes as necessary, and then recompress the code.

One final note of caution: the name under which the COMPRESSed program is saved to disk must be different than the original program or the original will be lost. One suggestion in this regard is to add ".COMP" to the old program name when SAVEing the compressed version. For example PAYROLL could be the original lengthy version, and PAYROLL.COMI the compressed code.

The compilers' promise of dramat ically increased execution speed has not been fulfilled to the extent that users expected. And the compiled code may actually require geater memory space. With COMPRESS, you can achieve many of the same goals at substantially lower cost, while preserving all of the important features of the Applesoft language originally envisioned by the designers of the Apple II.

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Book Reviews

by John Valente

68000 Assembly Language Programming, by Gerry Kane, Doug Hawkins and Lance Leventhal. Osborne/McGraw-Hill, 1981. ISBN: 0-931988-62-4. Paper, 680 pages, \$16.99.

68000 Microprocessor Handbook, by Gerry Kane. Osborne/McGraw-Hill, 1981. ISBN: 0-931988-41-1. Paper, 200 pages, \$6.99.

These two volumes are the only widely available texts on the 68000. Fortunately, they are excellent. As of this writing, the only 68000 computers available are expensive business-oriented systems. [Editor's note: Motorola has announced a 68000 Educational Computer Board (\$495.00) that will make it much easier to begin learning the 68000.) These two books should thoroughly prepare you for using the full power of the 68000. Even a casual glance shows this processor to be truly a "new generation" device.

Both volumes continue the tradition of quality in the Osborne series. They are well-written, concise, and clearly printed. Major points are in boldface, so the reader may find desired information quickly. Diagrams abound. Extensive indexes are included. The authors stick to the point and do not waste words; the reader should have some familiarity with microprocessor architecture and machine language.

68000 Assembly Language Programming begins with a discussion of the merits and deficiencies of assembly language. Some readers may have read similar material in other books on assembly-level programming. Still, the material is worth reading as an overview. A detailed description of a typical assembler follows; it is a very good tutorial, and applies to any processor. It also acquaints the reader with the terminology used throughout the text.

Chapter 3 gets to the heart of the matter: 68000 Machine Structure and Assembly Language. The author describes clearly the user/supervisor oper-

ating modes and the register set. Diagrams help to explain the various addressing modes, showing exactly what is happening in the machine.

The next few chapters develop familiarity with the instruction set through example programs that get progressively more difficult. The problems are typical of those selected in assembly-language texts: simple arithmetic, code conversion, tables, and lists. The reader, who may have seen these problems before, sees how they can be solved in the 68000 environment.

The third section presents "Advanced Topics" such as parameter passing and subroutines. The 68000 is particularly adept at these applications. Powerful instructions facilitate relocatable and re-entrant routines. I would have liked more coverage of the LINK and UNLINK instructions, which are both new and apparently very useful.

The next three chapters cover I/O using the 6821 PIA and the 6850 ACIA. Projects range from controlling a single LED to an A/D converter. The use of these chips is not new, and some readers may not need to read this section. However, the material is relevant, since new I/O chips for the 68000 are not yet available. In fact, the 68000 has special signals to communicate with the "older generation" 6800-series devices.

Chapter 15 is an important one, dealing with exception processing. Most of us have used NMI and IRQ interrupts, but these are merely two members of a whole class of exceptions. Others are bus and address error, reset, illegal operations, trace, trap, and multiple vectored interrupts. There is even a provision for extending the instruction set. Exception processing can service many peripherals in a very sophisticated system. The material covered here is complex, but close study will make it possible to take full advantage of the 68000.

Section 4 covers software development, from problem definition through testing and debugging. The insights presented here are essential to good programming practice, particularly for those inclined to jump from vague ideas to coding. The summary of typical errors is particularly enlightening, and can save you hours of frustration. In a rare departure from their serious tone, the authors offer some good advice: "Sometimes the following approach may be your best bet: turn off the computer, have a beer, and let your brain rest." (p. 19-21)

The remainder of the book alone would be worth the price. Each instruction is thoroughly described in alphabetical order. Accompanying diagrams show precisely the flow of data between registers and memory. Charts indicate the op-codes for the various addressing modes. This is very handy for quick reference while learning a new instruction set. And if that's not enough, there are three tabular appendices: the first is a listing of the instruction set by mnemonic that includes number of bytes, clock cycles, effects on registers, and a functional summary. The second is an alphabetical listing by mnemonic, with op-codes, byte count, and clock cycles. The third lists the op-code in ascending numerical order. These listings are a gold mine of information for the serious programmer.

I did notice a few errors in several programs and schematic diagrams, but they are minor. Each chapter concludes with a set of problems and a list of pertinent references. Some readers might wish the authors had omitted the generalized chapters on software development for the sake of a lower price. I think the price would be justified even without such material, given the quality of the text. Because the 68000 is a complex device, this book is not for casual reading. Fortunately, the authors and editors have made the work as painless as possible.

The 68000 Microprocessor Handbook is for the hardware designer and advanced programmer. If a microprocessor data sheet intimidates you, I do not recommend this book. On the other hand, if you can work from a very compact reference, and particularly if you are developing system hardware. this one is for you. Every pin and signal of the 68000 is concisely described. Diagrams are used extensively for bus cycle timings and data movement between registers and memory. There is no tutorial information on the instruction set, and no program examples are given. However, there is a brief description of the instruction groups, with emphasis on the more unusual ones. Two concise appendices list the entire instruction set by mnemonic and by opcode, as in the other book.

I especially liked the two-page introduction, which gives the reader a feel for the uniqueness of the 68000 and compares it to the 8086 and the Z8000. Since many of the new machines will use 16-bit processors, this will help you evaluate the various CPUs. The introduction convinced me that the 68000 is potentially the most powerful of the three.

The conventions used in the diagrams are clearly described before the text begins. A functional overview of the 68000 precedes chapters on bus timing and operation, exception processing, addressing modes and interfacing to 6800 peripherals. Selected data sheets are included at the end.

The text is very detailed, and very technical. The emphasis is on hardware design; all the material you need for programming can be found in the other volume. Only if you are capable of programming from a terse listing of a new instruction set should you rely on this volume for software development. Nonetheless, it is an excellent "standalone" guide to the 68000. For the experienced designer, it lives up to its title as a Handbook.

When you read about the 68000, I think you will be impressed. Users of the 6502 and 6809 will see some basic similarities, such as memory-mapped I/O and extensive addressing modes. However, the 68000 is a more complex processor with many new features. Its speed, 32-bit registers, and provision for multiple users, put it into the minicomputer class. Many analysts see a lack of software as a stumbling block to its acceptance. Perhaps readers of these two books will help solve this problem.

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Reviews in Brief

Product Name: SIGMON

Equip. req'd: TRS-80C Color Computer

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Price: \$29.95 Manufacturer: Datasoft, Inc.

> 19519 Business Center Dr. Northridge, California 91324

Description: SIGMON, a comprehensive assembly-language monitor for the Color Computer, is distributed on cassette. This versatile monitor incorporates a full disassembler and a line-by-line assembler, which doubles as a memory modify mode; it allows decimal, hexadecimal, and literal text to be entered while inserting the correct binary values in memory. Other abilities include the following: display memory in an 8-column format, showing both hexadecimal and ASCII characters. An optional display shows graphics characters for that range of binary numbers. The LIST command displays memory contents by memory location in a 25-column format. A data-search routine searches for text, hexadecimal values, or hexadecimal values specified as assembly-language mnemonic string.

Pluses: The large number of functions makes a very capable package. Extensive documentation is furnished, including a full assembly dump with comments. In particular, the string search is powerful and versatile.

Minuses: Some points of syntax are not covered in the instructions. The approximately 6.5K-byte program is not available in ROM, and is an inconvenient size for user-loading in ROM.

Skill level required: The user must be moderately familiar with assembly language before he can use any monitor effectively.

Reviewer: Ralph Tenny

Product Name: BUGOUT

Equip. req'd: TRS-80C Color Computer

with 16K memory

Price: \$19.95

Manufacturer: Spectral Associates 141 Harvard Avenue

Tacoma, Washington 98466

Description: BUGOUT is a small (<1800 bytes), well done assembly-language monitor for the Color Computer. Memory modification can be accomplished in two modes: normal examine/modify mode called (S)ubstitute, and (A)SCII, which enters ASCII codes in memory directly from keystrokes. A (F)ill mode allows entry of a given value in each location of a block of memory. The (M)ove memory command allows any memory move, including overlapping code. Two display modes are available: (D)isplay memory in normal screen dump format, and (T)ype, which displays memory contents in ASCII. The cassette handlers are (W)rite, which records assembly language in

CLOADM format, and (R)ead, which loads only files made with CSAVEM, W, and CSAVE. In addition, the filename, start address, file type, and last address are displayed. Also, the R command allows an offset to be specified to avoid conflict between programs. Debug and auxiliary commands are also available.

Pluses: Low cost, simple syntax, short learning curve, and convenient, comfortable operation. The S command is an excellent and unique enhancement.

Minuses: No printer output available; no listing furnished. The display scroll speed is too fast, and the V command should be able to verify cassette tapes.

Skill level required: As with any other monitor, the user should have some assembly-language experience. However, this monitor is perhaps the simplest to use and excellent for beginners.

Reviewer: Ralph Tenny

Product Name: PAL (Personal Assembly Language)

Equip. req'd: Versions for all Commodore

computers except the original BASIC 1.0 PET. Printer and Commodore disk

or tape optional.

Price: \$50

Manufacturer: Brad Templeton

271 Westcourt Pl. #201 Waterloo, Ontario N2L 2R8

Canada

Description: PAL is a complete 6502 assembler, entirely in machine language. Its syntax is like that of Commodore's own assembler, except that it uses ordinary program files instead of sequential data files. This assembler is used by some of the best known PET programmers.

Pluses: Like its companion program, POWER, *PAL* is extremely simple in concept, flexible in use, and powerful in skilled hands. It occupies only 4K of memory, and may easily be placed in EPROM to leave even more memory free. [It is able to hold its own 4K unassembled source code in a single file.] *PAL* is also extremely fast, assembling itself in under 10 seconds after disk access time. It uses the familiar BASIC screen editor, POWER, or any other programmer's aid, allowing highly flexible editing without learning a new set of commands. *PAL* includes an unassembler, and a converter for MAE assembler source files. The latest version is also able to create relocatable code.

Minuses: Doesn't handle macros, though that skill is planned in an update. BASIC doesn't preserve leading spaces in source files when they are edited. Thus, source files look a bit cramped before assembly.

Documentation: The manual is well printed and quite readable for skilled users. Beginners should probably buy another 6502 machine-language quide to go along with *PAL*'s manual.

Skill level required: Must be familiar with BASIC's screen editor and 6502 assembly language.

Reviewer: James Strasma

Product Name: Disk Utility Pack

Equip. req'd: Commodore computer and disk drive

Price: \$35

Manufacturer: Dick Immers 152 Brookfield Ct.

Adrian, Michigan 49221

Description: Has three main features: 1. includes a safe and versatile disk monitor called ZAP PET, which is much like Micromon, but for the disk rather than the computer; 2. attempts an automatic disk recovery after otherwise fatal data errors, (such as doing a short "new" to reformat the diskette, or leaving the disk in place while switching disk power off and on); and 3. catalogs up to 99 diskettes.

Pluses: ZAP PET is reliable in use. It has been carefully developed as a doctoral project over the past two years, and has overcome many of the limitations of similar programs. It is also the only such program that uses the hexadecimal and ASCII data used by the better computer monitors. ZAP PET alone is worth the price of the entire package. The disk recovery modules, MAYDAY and EAT MY DISK, are able to restore most or all of the data on diskettes that have been damaged accidentally. The additional data stored by the cataloging programs allows a lost directory to be restored quickly and easily from the catalog.

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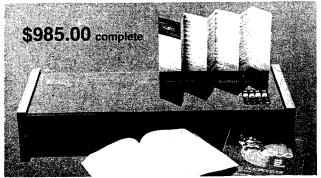
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For information call (714) 566-3911 Computer System Associates

7562 Trade Street, San Diego, CA 92121



Minuses: The cataloging modules are slow, compared to competing disk catalogers, due to the extra information they collect. The package needs an expert mode that allows skilled users to avoid the constant query, "Is this information correct?"

Documentation: Six printed pages, well done, but sketchy.

Skill Level Required: Beginners can use most of the package. However, only skilled users should use the edit option of ZAP PET.

Reviewer: James Strasma

Product Name: Munch-a-bug

Equip. req'd: Apple II Plus with 48K

Price: \$49.95

Manufacturer: Southwestern Data Systems

10159-I Mission George Rd. Santee, California 92071

Description: Munch-a-bug provides a variety of capabilities to aid in debugging assembly-language programs. A few of these are: Trace, a facility for listing 6502 registers as each instruction executes; Breakpoints, a facility to identify paths of execution; Examine, a facility allowing display and change of 6502 registers. There are many other features making the program a truly valuable utility.

Pluses: Assembly-language programming is greatly eased with this product. Large or complex programs are done much faster if intelligent use is made of the product's features. A particularly valuable capability is the Next command which allows tracing but ignores JSRs. A feature for making three back-up copies is built in and works well.

Minuses: A built-in assembler is provided, but it's somewhat primitive. Direct SAVEs of assembly source is impossible. This is, overall, a minor inconvenience; it can be worked around.

Documentation: Excellent. Some fairly original techniques are introduced, but the manual describes them clearly. There is, perhaps, a tendency towards making the examples provided overly simple.

Skill level required: Experienced assembly-language programmer.

Reviewer: Chris Williams

Product Name: OS-96

Equip. req'd: CBM 8032 with 8096 expansion

memory board and Commodore disk

Price: \$200

Manufacturer: A.B. Computers

252 Bethlehem Pike Colmar, Pennsylvania

Description: OS-96 is an extended BASIC interpreter. It expands BASIC workspace to an apparent 64K free (32K reserved for program text, and 32K for variables.) Also adds several commands and enhancements to BASIC, including most of those in the SM KIT programmer's aid. Further enhancements to the interpreter are planned later.

Pluses: Leaves *lots* of memory space free for large programs. Changing program lines, or chaining large modules

behind small ones, doesn't clear or overwrite variables. The BASIC stack has been lengthened, allowing deeper nesting of loops and subroutines. Key sequences access the 8032's extended editing commands. Program lines and variable data may now be up to 255 bytes long. Spaces are automatically deleted from program lines, and reinserted on LISTing, with a further option moving each statement to a new line, properly indented for structure. OS-96 is fully compatible with PET BASIC syntax and programs.

Minuses: Program load addresses differ from usual BASIC. Includes a "load at" command to help. The AUTO line number feature often turns on when not wanted. None of the usual SYS calls or enhancement ROMs work with OS-96.

Documentation: The instructions I saw were preliminary. I found them adequate for those who already know PET BASIC, though they hint at undocumented commands.

Skill level required: Beginner to expert.

Reviewer: James Strasma

Product Name: The WIZ Database Manager

Equip. req'd: CBM 8032 or 8096 computer with

8050 disk drive. CBM or ASCII printer optional. Uses phantom ROM at

\$9000.

Price: \$495

Manufacturer: Dr. Daley Software

Water Street

Darby, Montana 59329

Description: WIZ is a data manager package. It is limited to a single file, but the file may use up to 30 diskettes, each with 2,000 or more records. Each record may be up to 250 characters long, and contain up to 64 fields of up to 78 characters each.

Pluses: Handles vastly larger numbers of records than competing programs. Also includes built-in plotting. Like its best competitors, the WIZ uses CBM's recommended screen input editor, has programmable arithmetic, and handles simple reports. It also allows sorting on any field or combination of fields, and either fixed field or global searches, with or without regard to the case of the text. Results may be sent to screen, printer, or Word-Pro sequential disk file. Includes listing to allow reading and writing its data from other programs, and can jump from itself to other user programs. Better yet, it can read in data from sequential disk files, avoiding retyping when upgrading from a lesser data package.

Minuses: Moderate attempts are made to keep the user from halting, listing, or changing the programs. The phantom ROM will activate outside the WIZ, if its last two addresses are PEEKed; whether this is a problem depends on what other ROM shares the socket.

Documentation: Self-documenting. Includes a thick and nicely packaged manual, but you may not need it. The WIZ explains itself merely by typing ''help''. This works virtually anywhere in the program, providing a total of 125 help screens.

Skill level required: Even beginners should be able to use this package effectively.

Reviewer: James Strasma

Product Name: Write Away

Equip. req'd: Apple II or Apple II Plus, 48K, 1 disk

drive. Recommend modified shift key, lower-case adapter, 80-column card,

second disk drive.

Price: \$175.00

Manufacturer: Midwest Software Assoc.

P.O. Box 301 St. Ann, MO 63074 1-800-835-2246, Ext. 467

Description: A versatile word processor providing the user with a variety of levels of capability. Terminology and procedures are quite similar to DEC's PDP word processing software.

Pluses: The expandability is excellent. If one is familiar with DEC procedures, learning time is near zero.

Minuses: Very little can be found wrong with this program. There is a lengthy tutorial on the program disk that, perhaps, takes longer than it should to complete.

Documentation: Excellent. The booklet provided is concise and informative. A separate quick-reference card of commands is included in the package. Without it, the user would have to occasionally erase his screen of text in order to use the HELP command.

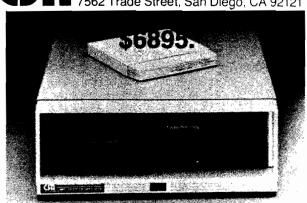
Skill level: No programming language knowledge needed. Some general computer familiarity is desirable.

Reviewer: Chris Williams (Continued)

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Product Name: Amper-Magic

Equip. req'd: 32K Apple II or Apple II Plus;

Applesoft in ROM or Language Card

and one Disk II with DOS 3.3.

Price: \$7

\$75.00

Manufacturer: Aurora Systems, Inc.

2040 E. Washington Ave.

Madison, WI 53704

Author:

Bob Nacon

Description: A menu-driven utility program that allows machine-language subroutines to be added to Applesoft programs. Each subroutine is given a name (see below) and is callable from the BASIC program using the Applesoft Ampersand (&) command. Twenty-three ready-to-go subroutines and their descriptions are included with the package. Volume II, with 26 routines, will be released by the time you read this.

Pluses: Amper-Magic provides a convenient way to package ampersand routines and Applesoft programs together. This allows the user to avoid extra BLOAD commands and possible adjustment of program pointers when setting up a program that uses ampersand subroutines. Many options are provided for ease of use. For example, any table of subroutines created for use by one program may be saved in a binary file and used as a unit in any other program desired.

Minuses: The names given to the subroutines are restricted to four bytes. A byte may be either a single ASCII character or an Applesoft token name (e.g., GOTO, NEXT, PRINT, etc.). This restricts the user to unmnemonic names if only ASCII characters are used.

Documentation: Good. A quick reference card summarizes system usage.

Skill level required: Facility with Applesoft. No knowledge of machine language is needed.

Reviewer: Richard C. Vile, Jr.

Product Name:

Apple Flasher

Equip. req'd:

Apple II with Applesoft in ROM or RAM card. One or more disk drives

with DOS 3.3.

Price: Distributor: \$34.40 plus \$1 for shipping Crow Ridge Associates, Inc.

P.O. Box 90

New Scotland, NY 12127

(518) 765-3620

Description: Apple Flasher provides a convenient, rapid way to find and display high-resolution pictures that have been previously stored on diskettes by other Apple II programs. It loads the names of assumed picture files into the program and offers four options for their display: 1. A single keystroke to present picture; 2. Each picture scanned for about two seconds in rapid succession; 3. Picture files displayed in an emulation of a manual slide drum slide projector; 4. Automatic projector mode. Back-up replacement is available for \$8.50 plus return of the damaged disk. One back-up copy will be sold in addition to the master disk for this price.

Pluses: The author has developed a very fast file recovery algorithm that loads frames in about two seconds. You can also load three files at once with instantaneous shuttle to either hi-res screen one or two. Files may be on two drives and "punched up" in any sequence.

Minuses: The program does not allow for reordering the presentation of the files differently from the order they are saved on the disk. The price of the program seems somewhat high in view of its limited function. Without carrying out the task of entering the projection times in each image file, the auto-display function is of limited use.

Reviewer: David P. Allen

Product Name:

Color Scribe

Equip. req'd:

TRS-80 Color Computer, 32K with

disk.

Price:

\$49.95

Manufacturer:

Computerware

Box 668

Encinitas, CA 92024

Description: This full-featured text editor/formatter includes full disk and cassette ASCII-file compatibility. The text editor is line-oriented, and can be used to edit BASIC files, as well as many other text applications. The editor includes edit, change, add, copy, move, and replace. The formatter is very powerful and automatically justifies left and right text. Headers and footers are allowed, as is underlining and centering. Indentation and sending of printer control codes are accepted. User remarks can be entered into files, and files larger than memory are easy to create.

Pluses: Easy to learn for simple applications, yet offers more powerful functions when required. The formatter is one of the best I have seen. The disk operating system works well now since bugs from an earlier version of the program have been corrected in this version. Files can be as large as a disk can hold by using the 'MORE' command. The READ command brings text from a file on disk into any desired position in the file in memory. Files are backed up automatically as they are loaded. A MACRO command allows the user to specify single-key entry of one or a series of commands. ESCape and CHR\$ commands can be specified by the formatter to allow you to use special print options, change character fonts, etc., in the printer. Commands can be entered in either upper or lower case.

Minuses: Since the program is line oriented, the user must press the ENTER key at least every 127 characters. Text is difficult to visualize on screen, and the screen print formatter does not use a "window" format to view the text. As a result, you must do a printout to actually view the result of formatting commands. When working on a file brought in from disk, the file is open. If you shut off the computer, or exit Scribe via Reset, or other unauthorized means, you will have an open file on the disk. You will probably not be able to read this file again. It is possible to end up in BASIC if you press the BREAK key while accessing the directory. The manual does warn of this. A minor bug does not allow using the slash bar as extension delimiter, a period is used as delimiter.

Documentation: The 40-page operation manual is of excellent quality. A demonstration file is included on the disk that assists in understanding the operation of the program.

Skill level required: The progam operation is easy to learn, even for the novice operator.

Reviewer: John Steiner

MICRO

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Petspeed — The Optimizing **Basic Compiler that runs Commodore** BASIC 40 times faster. You can dramatically reduce long processing times, tedious disk handling, and long print runs. No other compiler can offer the same speed, compatability and trouble-free compiling as Petspeed.

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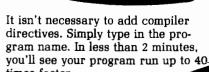
222 B. View St., Mountain View, CA 94041

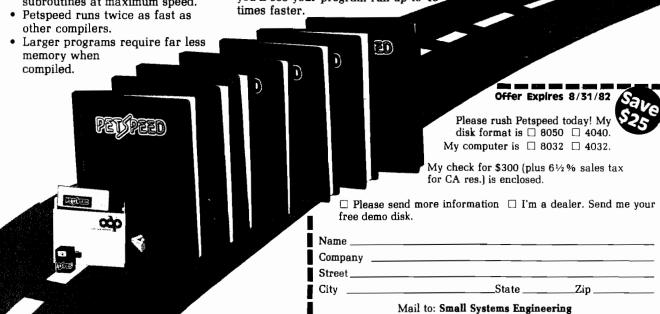
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6502 Bibliography

1. Commodore Magazine (April/May, 1982)

Scott, Dave, "An Easy Cursor Positioning Routine," pg. 82-85.

A machine-language program for the Commodore Microcomputers which allows printing a message at a specific x,y location on the screen. It is entered via the "SYS" command.

2. MICRO No. 48 (May, 1982)

Engstrom, Christer, "BASIC to Machine-Language Interface," pg. 13-17.

Since the AIM lacks a CALL or SYS function, it is difficult to communicate between BASIC and machine language. This interface routine makes possible the entry of the machine-language address through a BASIC variable.

3. Compute! 4, No. 5, Issue 24 (May, 1982)

Berlin, Stanley M., "Putting the Squeeze on Your VIC-20: Getting the Most Out of 5000 Bytes," pg. 136-144.

When you write a program so large that it will need every last memory cell in your computer, this article shows several ways to reduce the size of your program. In general, the suggestions may apply to other machines using BASIC.

4. The Commodore Transactor 3, Issue No. 5 (May, 1982)

Hook, David A., "VIC Loader for PET/CBM," pg. 41-48
This procedure. BASIC program, and machine-lan-

This procedure, BASIC program, and machine-language routine will make it possible for VIC-20 users to exchange tapes with PET/CBM users. These routines accommodate several possibilities of incompatibility.

5. Antic 1, No. 1 (April, 1982)

Capparell, James, "Systems Guide: Memory Map — Page 0," pg. 9-11, 15.

In the next few issues of this new publication serving the Atari micros, a complete memory map will be constructed giving the reserved memory locations in RAM and ROM with a description of the purpose of each location. The first 128 of page zero are reported in the current issue.

6. PEEK(65) 3, No. 5 (May, 1982)

Eidbo, Carl, "OS-65U Disassembly Aid," pg. 3-5.

The unavailability of an OS-65U disassembly manual led to a project to disassemble specific areas. Both BASIC and machine-language versions of a disassembler are given for OSI micros.

7. Creative Computing 8, No. 5 (May, 1982)

Ott, Jack P., "Listing/Copying Apple Text Files," pg. 144-154.

The lack of a simple command to list or copy text files on the Apple prompted design of a procedure to do this. Written in BASIC, simple editing of whole records is made possible, but is not as versatile as a true text editor.

8. Mini'App'Les 5, No. 5 (May, 1982)

Murrell, Mike, "Cursor Menu," pg. 9-10.

A cursor-oriented menu control program for the Apple. Written in Applesoft.

9. Call -A.P.P.L.E. 5, No. 5 (May, 1982)

So, Edward C., "Picture Compression," pg. 21-35.

A program in machine language for the Apple which provides a method of storing and retrieving an Apple hi-res picture in less than the 34 sectors it normally requires. The program consists of five separate routines. By examining for duplicated pixels within the picture, one can use less than 960 pixels to store the picture.

10. Byte 7, No. 5 (May, 1982)

Sweet, Jerry N., "Chedit," pg. 426-444.

A graphics-character editor enabling the Apple Pascal user to define his own character set, using the Turtle-graphics system.

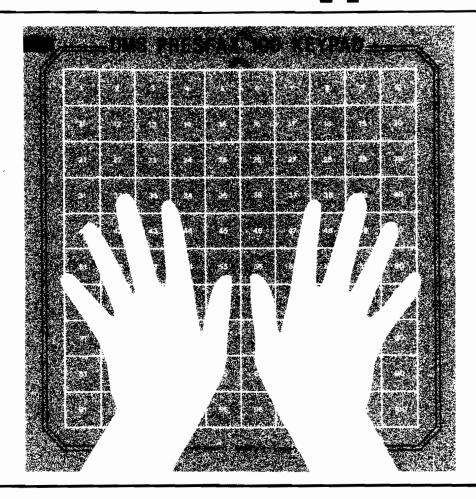
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Dealer Inquiries Invited



6809 Bibliography

64. BYTE 7, No. 5 (May, 1982)

Kocher, Christopher P. and Keith, Michael, "Six Personal

Computers from Japan," pg. 61-102.

Descriptions in detail of six Japanese computers including the 6809-based Canon CX-1, the Hitachi MB-6890, and the Fujitsu FM-8.

Barden, William, Jr., "Ports of Entry and Soft Breezes for the Color Computer and Model III," pg. 162-198.

Instructional article for the Radio Shack 6809-based Color Computer. A \$10 anemometer and other remote-sensing projects using the cassette interface.

65. MICRO No. 49 (June, 1982)

Tripp, Robert M., "Editorial," pg.5.

Discussion of new developments relating to the 6809 microprocessor.

Clements, William C., Jr., "Add a VIA and Speech Synthesizer to the Color Computer," pg. 19-21.

Hardware article for the TRS-80 Color Computer, adding two user-accessible, 8-bit bidirectional I/O ports to your computer and interfacing an inexpensive speech synthesizer.

Dial, William R., "Resource Update," pg. 99-100, 110. A list of magazines which contain information about the 6502/6809 microprocessors on a reasonably regular basis.

Staff, "Software Catalog," pg. 101-109. Description of some 50 different software items, including a number for 6809-based systems.

Staff, "Hardware Catalog," pg. 111-113. Some 20-odd items of hardware are described including several for 6809 systems.

Dial, William R., "6809 Bibliography," pg. 117. A number of literature references related to 6809-based systems.

66. Apple Assembly Lines 2, Issue 9 (June, 1982)

Sander-Cederlof, Bob, "Implementing New Opcodes Using 'BRK','' pg. 2-5

How to use the 6502 to simulate neat instructions from the 6809 like LEAX, BSR, BRA, etc.

67. Microcomputing 6, No. 6, Issue 66 (June, 1982)

Staff, "C Development Tools for the MC6809," pg. 162. An announcement of Introl-C, a set of software for 6809-based C programs, including a C compiler, a 6809 assembler, an object code linker, and an object code library manager.

68. BYTE 7, No. 6 (June, 1982)

Barden, William, Jr., "A General-Purpose I/O Board for the Color Computer," pg. 260-282.

This hardware article describes a low-cost interface which can be plugged into the ROM slot of the Radio Shack TRS-80 Color Computer.

69. The Rainbow 1, No. 12 (June, 1982)

Lewandowski, Dennis S., "The Assembly Corner," pg. 6-7. Discussion of indexed addressing for the Radio Shack TRS-80 Color Computer, also designated the 80C or CoCo. Several other instructions for this micro are also discussed.

Waclo, John, "80C Picks the National Football League," pg. 12-18.

Keep track of how the NFL is doing with this program for the TRS-80 Color Computer.

Shorter, Ted, "Ice Lander Can Be A Slick Run," pg. 25-27. A lunar lander type of game for the 80C.

Nolen, Bill, "Now a Whole Bag of Dice," pg. 38-40. How to replace your \$5.00 set of dice with a \$600 computer, with a few extras to boot. For the TRS-80 Color Computer.

Curtis, H. Allen, "Getting More from PMODE 4," pg. 42-46. Discussion of color graphics on the 80C demonstrating that PMODE 4 has six other color sets. Several short programs are given.

Scerbo, Fred B., Dobbert, Robert, and Haggerty, Dale, "Swamp Wars Can Win Your Croix de Lillypad," pg. 49-50 A color game for the TRS-80 Color Computer.

Mir, Jorge, "A Data File to Store All Your Information," pg. 57-62.

A flexible file system for the Color Computer, with search function, sorting, etc.

Moses, Ray, "Stars Fall on 80C," pg. 64-65. A program giving a display of the sky using a variety of times of the year.

70. '68' Micro Journal 4 No. 5 (May, 1982)

Anderson, Ronald W., "Flex Uer Notes," pg. 10-12. Discussion of the use of Pascal including a dump utility for 6809 based systems.

Nay, Robert L., "Color User Notes," pg. 13-14. Notes on the Super Color Writer and Flex9 for the Radio Shack 6809-based Color Computer.

Cadmus, Ray, "OS-9 Notes," pg. 15-16. Notes on the OS-9 operating system for the 6809 including a program HDIR, a hiarchial directory.

Rowley, Blair A., "Uniflex and RSTS," pg. 19-22. Intercommunication between Uniflex and RSTS developed in 6809 assembly language.

Lilly, Randal, "Transfer" pg. 22-24. A computer link program that allows a main computer (SWTCP 6809) to transfer and run programs on a remote CPU [6800 or 6809 computer].

Jones, Bob, "Othello," pg. 34-35. Othello for 6809 based systems.

Preble, Laurence D., "Error Messages," pg. 37. A patch for Version 17 of TSC's Extended BASIC for the 6809 to better handle error messages.

Liu, Bruce, "Overlay Systems," pg. 37-39. Improved Pascal routine for 6809 based systems.

71. 80 Micro Issue No. 30 (June/July, 1982)

Berenbon, Howard, "SDS80C Editor/Assembler/Monitor" pg. 70-71.

Review of a 6809 assembler/editor/monitor.

Commander, Jake, "Spiromania-Part II," pg. 106-115. Machine code routine for color graphics on 6809 based systems including the Radio Shack Color Computer.

Norman, Scott L., "Color Computer Utilities," pg. 116-120. Power Pack and Color Diagnostics, Editor and Assembler for the Radio Shack Color Computer are reviewed.

Leichtman, Kerry, "Bob Rosen - A Colorful Success Story," pg. 174-175.

Build a better bulletin board and the world will beat a path to your door. Using the 6809-based Color Computer.

Miller, Franklyn D., "Extended Color Basic," pg. 266-270. Discussion and hints for using this system for the Color Computer. MICRO



Software Catalog

Name:

Financial Records

Manager

System: Apple II, DOS 3.3

Memory: 48K

Applesoft and Language:

Machine

Hardware: Disk drive (printer

optional

Description: A menu-driven, user-friendly program to collect, sort, store, and analyze up to 750 checking/budget records and 500 tax records. Includes 30 user-defined budget categories and 50 user-defined tax categories, automatic payments, search and edit functions, average and percentage budget analysis, tax summary (including approximate taxes due), "What if?" analysis, and utilities.

Price: \$44.95

Includes typeset manual, program disk (copyable).

Author: D.M. Whittaker

Available: Cybertech

1500 W. Shaw, Suite 404

Fresno, CA 93711 (209) 222-9094

Name:

How to Operate the Apple II Plus

System:

Apple II Plus Hardware: One disk drive,

standard audio

cassette player

Description: An audio cassette mini-course for the first-time computer user. Three audio cassettes and Apple's own DOS 3.3 System Master diskette are used to teach the essentials of computer operation: how to run programs, enter data, save and organize files, make backup copies, modify programs, and much more. No programming or technical knowledge assumed.

Price: \$49.95

Includes three spoken-voice cassettes with 28-page Operator's Guide in a vinyl

Author: Howard Manthei and

Lee McFadden

Available: Fliptrack Training Tapes A Division of Mosaic Media

P.O. Box 711 Glen Ellyn, IL 60137 [312] 790-1117

Name:

Bowling Data System 2.0

System: Memory:

Apple II 48K minimum

Language: Applesoft ROM Hardware: 1 disk drive (2 recommended).

DOS 3.3.

80-column printer Description: A database program for bowling league secretaries. Stores and computes data for multiple (and/or mixed) leagues of up to 40 teams, each with up to six bowlers per team (not including substitutes). The reports generated are: weekly recap, score sheet, season average, and team listing. Many options are included. For each team a cumulative record is kept of all pertinent data.

Price: \$149.95

Includes documentation, one

diskette.

Author: Arnold Hooton, Published by Rainbow

Computing, Inc.

Available:

RCI Marketing 19517 Business Center Dr. Northridge, CA 91324

(213) 349-0300

Name:

The UCSD p-SystemTM

System:

The UCSD p-System

Memory:

48Kb runtime environment; 64Kb development

environment.

Written in UCSD Language:

Pascal and Assembly Support: UCSD Pascal, FORTRAN-77,

BASIC, and Assembly

and 68000

Hardware: 8086, Z80, 8080,

8085, 6502, 9900, LSI-11/PDP-11,

Description: The UCSD p-System is a stand-alone operating system for most microcomputers based on the 8086, 8080, Z80, 6502, 6809, 9900, 68000, and LSI-11/PDP-11 microprocessors. The UCSD p-System includes an operating system, file handler, screen-

oriented editor, line-oriented

editor, macro assembler, interpreter, and utilities.

Price: \$375.00

Includes object code for the UCSD p-System, UCSD p-System User Manual, Architecture Guide. Installation Guide, and Beginner's Guide to the UCSD Pascal System, with tutorial disk.

Available:

SofTech Microsystems, Inc. 9494 Black Mountain Rd. San Diego, CA 92126 (714) 578-6105

Waterloo microFORTRAN

Name: System:

Commodore SuperPET, Volker-Craig 2900, 3900, 4900, Northern Digital microWAT

Description: Waterloo micro-FORTRAN is a special dialect designed for teaching purposes. It has many of the characteristics and much of the flavour of normal FOR-TRAN, but varies significantly from established standards for that language. This language processor has many of the important characteristics of the WATFIV-S compiler which is widely used on IBM computers, plus some features from the new FORTRAN-77 definition. Examples of language features supported are FORMAT, subroutines and functions. multi-dimensional arrays, extended characterstring manipulation, structured programming control, and file input/output. In addition, the interpreter provides a powerful interactive debugging facility.

Available:

Waterloo Computing Systems Limited 158 University Ave. West Waterloo, Ontario Canada N2L 3E9

Name: System:

Storm Radio Shack Color

Computer Memory: 16K

Language: Assembly Language Hardware: Joysticks

Description: Storm brings the

excitement of the arcade to your home. A tempest of a game, it has 135 levels of play, fast action, graphics, and sound. It challenges the very best arcade experts and offers fun for the beginners too. There is no other highresolution graphics arcade game like it!

Price: \$24.95 on cassette \$29.95 on disk Includes program and instruction manual

Available: Computerware Box 668 Encinitas, CA 92024

(714) 436-3512

FORTH-79 Name:

Version 2 System:

Apple II or Apple II Plus (also Z-80 CP/M, NorthStar Advantage, and

NorthStar DOS

Memory: 48K

Language: Machine Language and FORTH-79

Hardware: 1-14 disk drives

(13- or 16-sector-

compatible) Description: FORTH-79 (Ver. 2) is a structured language suited for systems and applications programming where execution speed is important. It meets all provisions of the FORTH-79. Standard and programs are portable, faster than BASIC, and provide an interactive environment. Its highspeed compiled code is well suited for real-time applications. Includes screen editor, macro-assembler, strings, 32-bit integer arithmetic. Floating point and hi-res Turtle Graphics are also available.

Price: \$99.95 - BASIC \$139.95 - hi-res and floating point

Includes software and 200-page manual (including FORTH-79 and fig-FORTH).

Author: Martin Tracy and Philip Wasson

Available: MicroMotion

12077 Wilshire Blvd. Los Angeles, CA 90025 (213) 821-4340 (continued)

Name:

BASIC

CompilerTM

System:

The UCSD p-System

Memory:

48Kb runtime environment; 64Kb development

environment

Language: BASIC

Hardware: 8086, Z80, 8080,

8085, 6502, 9900, 6809, 68000, LSI-11/PDP-11

Description: The BASIC Compiler is a powerful and flexible BASIC offering you: expanded syntax, including IF THEN ELSE construct; INCLUDE files; and virtual arrays. The BASIC Compiler is fully in-

tegrated and compatible with

PascalTM UCSD FORTRAN-77.

Price: \$225.00 Includes object code for the BASIC Compiler and BASIC Reference Manual.

Available:

SofTech Microsystems, Inc. 9494 Black Mountain Rd. San Diego, CA 92126 (714) 578-6105

Name: Cash + Plus

Apple II Plus with System:

DOS 3.3

Memory: 48K

Language: Applesoft BASIC Description: Point-of-sale program with inventory controller and management reporter. Can also be used with optional cash drawer.

Price: \$395.00

Includes programs and

manual.

Author: Lynn Graybiel

Available:

Southeast Computer Concepts Corp.

500 E. Spanish River Blvd. Boca Raton, FL 33431

Name: **Bond Yielder** System: Apple II Plus

Memory: 48K Language: Applesoft Hardware: Disk drive,

optional printer Description: Bond Yielder performs fast and accurate calcu-

lations on the profitability of fixed income investments. This package can replace the

\$1200 desk-top calculator now standard in the investment

industry Price: \$149.95

Author: Donald Brown

Available: CE Software 801 73rd Street Des Moines, IA 50312

Name: TRA/C1

System:

OSI Superboard II,

C₁P

4K or more Memory: BASIC and 6502 Language:

ASM

Hardware: Cassette tape Description: TRA/C1 single steps and traces machine-code programs and displays all registers, processor status, stack pointer, program counter, instruction and operand. Does not disturb any registers or the stack. This program is an essential aid to OSI machine-code or asssemblycode writers.

Price: \$17.95

Includes casette tape and

instructions. Available:

D. Wolf, Ph.D.

Box 565

Port Hueneme, CA 93041

Name: Universal Mailing

List

Any Commodore System:

6502 or 64

3.5 (Vic 20) -Memory:

96K on the CBM

BASIC Language:

Hardware: Disk drive or tape

drive

Description: The program allows you to change, view and update addresses. It allows you to print one or all addresses onto mailing labels. It will build a list and save it onto a cassette tape for recall. The program is designed for the VIC 20, but it runs on the 4016, 4032 and the 8032. Capacity ranges from 21 addresses on 3.5k to 359 addresses on 32k.

Price: \$29.95

Includes 3 ring binder, users' manual and a cassette page that holds six cassettes for the program and data files.

Author: S. Mark Vanderbilt

Available:

Computer Barn Old Town Salinas 319 Main Street #2 Salinas, CA 93901 (408) 757-0788

(Continued)

CSE means OSI

Custom After Market Software for C1P and C4P machines

*Basic Enhancer:

Renumber, Auto Sequencer, Screen Control functions, and tape 1/0 system that is faster and has file

> C4P \$29.95

Modified Monitor Rom Chip:

Now get indirect jump-capabilities just like those in the C1P and for no extra charge CSE will burn in your machines serial number \$16.95

*NOTE: The C4P version of the Basic Enhancer includes the modified monitor Rom chip required for proper program functioning.

This is only a partial listing of our products. Write us for information on new disk programs or send \$2 for catalog. Please include \$2.00 shipping and handling with orders.

Computer **Science** Engineering

Box 50 • 291 Huntington Ave. Boston 02115

Decision

Systems

Decision Systems P.O. Box 13006 Denton, TX 76203

SOFTWARE FOR THE APPLE II.

ISAM-DS is an integrated set of Applesoft routines that gives indexed file capabilities to your BASIC programs. Retrieve by key, partial key or sequentially. Space from deleted records is automatically reused. Capabilities and performance that match products costing twice as much. \$50 Disk. Applesoft.

PBASIC-DS is a sophisticated preprocessor for structured BASIC. Use advanced logic constructs such as IF...ELSE..., CASE, SELECT, and many more. Develop programs for Integer or Applesoft. Enjoy the power of structured logic at a fraction of the cost of PASCAL.

\$35. Disk, Applesoft (48K, ROM or Language Card)

DSA—DS is a dis-assembler for 6502 code. Now you can easily dis-assemble any machine language program for the Apple and use the dis-assembled code directly as input to your assembler. Dis-assembles instructions and data. Produces code comth the 5-C Assembler (version 4.0), Apple's Toolkit assembler and others. \$25 Disk, Applesoft (32K, ROM or Language Card)

FORM-DS is a complete system for the definition of input and output froms. FORM-DS supplies the automatic checking of numeric input for acceptable range of values, automatic formatting of numeric output, and many more features \$25 Disk, Applesoft (32K, ROM or Language Card).

UTIL-DS is a set of routines for use with Applesoft to format numeric output, selectively clear variables (Applesoft's CLEAR gets everything), improve error handling, and interface machine language with Applesoft programs. Includes a special load routine for placing machine language routines underneath Applesoft programs. \$25 Disk, Applesoft

SPEED-DS is a routine to modify the statement linkage in an Applesoft program to speed its execution. Improvements of 5-20% are common. As a bonus, SPEED-OS includes machine language routines to speed string handling and reduce the need for irbage clean-up. Author: Lee Meador

\$15 Disk, Applesoft (32K, ROM or Language Card)

(Add \$4.00 for Foreign Mail)

*Apple II is a registered trademark of the Apple Computer Co

Name:

Softerm

Apple II or Apple System:

II Plus

Memory:

48K, Autostart ROM

Language: Assembly - 6502

Hardware: Serial

communications.

optional

80-column video, optional printer

Description: High-speed CRT emulator up to 9600 baud for following terminals: IBM 3101 series, DEC VT-100 and VT-52, Data General D-200, ADDS Regent Series, Lear Siegler ADM-3A and ADM-5, Hazeltine 1400 and 1500 series, TeleVideo 900 series. Also includes file transfer with data compression, keyboard macros, automatic dialer with built-in phone book, local connect or remote operation using standard modems.

Price: \$150.00

Includes progam diskette, configuration diskette,

manual. Available:

Softronics 6626 Prince Edward Place Memphis, TN 38119 (901) 755-5006

FLEX - OS9 Copy Name: Utility Program

Description: Metacopy 130 is a file-copy utility that transfers any type of OS9 file to a FLEX sequential file, or FLEX sequential files to an OS9 file. This utility also provides a directory of FLEX disk while in the OS9 operating system. The program is menu driven for ease of use.

Price: \$135.00 Available: Meta Lab P.O. Box 1559 Suite 106 2888 Bluff St. Boulder, CO 80301 (303) 499-4236

Name:

Mojave Desert Adventure

System: VIC-20 Memory: 3.5KLanguage: BASIC

Hardware: Cassette, joystick

(optional)

Description: Explore the Mojave Desert in search for gold. You find gold but trouble finds you. Crunched and randomized programming makes this treasure hunt last hours. Game is over when you find all the gold or die of thirst. Random placement of key objects makes this a new adventure each time played.

Price: \$9.95

Includes cassette tape,

instructions.

Author: Dennis McCormack

Available: The Byte House Box 981 Salem, NH 03079

Name:

Memory:

Roterm

System: OSI Superboard II. C₁P

8K

Language: BASIC and 6502

Assembly

Hardware: Modem

Description: Roterm turns the computer into a high performance cipher terminal with special enhancements for convenient encryption and decryption of electronic mail. Roterm emulates a classic rotor cipher machine and can operate at continuous data rates of 300 baud. Roterm is also available specially configured for Cegmon users or users of the new model Superboard.

Available: D. Wolf Ph.D. Box 565 Port Hueneme, CA 93041

Name:

CW Morse

VIC-20 Computers

Memory:

5K VIC (expansion memory optional)

Description: CW Morse allows your computer to become a Morse terminal for an amateur radio station. It is capable of sending and receiving Morse code at speeds of up to 25 wpm. Includes multiple 255 character message buffers, special function keys, typeahead keyboard buffer, and automatic speed control on receive. Package includes documentation, interface schematic, and I/O connector. Send SASE and type of computer for more information on this and other software available.

Price: \$19.95 Includes cassette.

Available:

Kinetic Designs 401 Monument Rd. #171 Jacksonville, FL 32211

TIRED OF TYPING?

MICRO has the solution.

Order a diskette of three recent utility programs for the Apple. For only \$10.00, plus \$2.00 shipping and handling, you will receive a DOS 3.3 diskette containing the source and assembled listings of:

Applesoft Variable Dump by Philippe Francois (MICRO, April 1982)

Straightforward Garbage Collection for the Apple by Cornelis Bongers (MICRO, August 19821

COMPRESS by Barton Bauers (this issue, page 89)

Please send check, money order, or VISA or MasterCard number. Only prepaid orders accepted.

If you missed April or August, order one now! Include \$2.50 for each issue.

Send orders to:

High-Resolution

Plotting Routine

VIC 20

Hardware: 1515 printer, tape

2K

Language: BASIC

Apple Utility Disk **MICRO** P.O. Box 6502 Chelmsford, MA 01824

System:

Atari 400 or 800

16K - Atari

Hardware: Two transistor, 1 IC interface

player Description: This routine turns the VIC 1515 printer into a plotter with the highest possible resolution (72 dots/inch horizontally and vertically). Will plot the results of user-supplied programs, functions, or data logging routines. Size of the plot and automatic scaling with number labels and tic marks are continuously useradjustable. Length of the plot may be infinite, allowing strip chart recording. Price: \$10.00 Includes cassette and

Name:

System:

Memory:

instructions Author: Tim Bowker Available:

> Scientific Software 525 Lohnes Dr. Fairborn, OH 45324

Name:

Legend Slide

Select System: Apple II Memory: 48K

Language: Assembly Hardware: Legend 64K or 128KDE Card

Description: Legend Slide Select offers a way to control the display of hi-res pictures. The program permits the Apple II to act as a slide projector, and allows the user to flip forward or backward. Control of the displayed pictures is userselectable and can be controlled from the keyboard, game paddles, or set to run automatically.

Price: Free with card (\$349 retail) Includes program and manual.

Available:

Legend Industries 2220 Scott Lake Road Pontiac, MI 48054 (313) 674-0953

(continued)

Name: Access Apple II Plus System: 48K Memory:

Language: Applesoft, Machine Code

Description: Access is a general-purpose database manager. Special features include: word processor-style editor [delete/insert characters, etc.); free format screen design; supports most 40- or 80-column upper- and lowercase hardware modifications; record retrieval time by index less than three seconds and using any search parameters less than 23 seconds; up to 1521 characters per record; up to 39 fields per record; up to 39 characters per field; up to 20 calculated fields per record; short forms option, and much, much more.

Price: \$320.00

Includes manual, two disks.

Author: Nik Spicer and Mike Slaughter

Available:

Spider Software Ltd. 98 Avondale Road South Croydon Surrey, England Tel. 01-680 0267

Name:

SBCS AGRI-LEDGER

System:

Apple II or Apple II+, DOS 3.3

48K Memory:

Language: Applesoft Hardware: 2 disk drives, 80

column printer Description: Organize farm financial records with the SBCS Agri-Ledger! It is Menu driven, a user oriented program with clear, easy to follow documentation. Numerous reports, budgeting, large flexible chart of accounts, yearly history, enterprise analysis, current market valuation can be done. Warranty, free program updates, and friendly customer service are provided.

Price: \$395.00 Includes program disk, program backup, data disk and documentation. Demo available for \$30.

Author: David McFarling

Available:

Small Business Computer

Systems 4140 Greenwood

Lincoln, NE 68504 (402) 467-1878

Name:

Strobe Business

Graphics Apple DOS 3.3 System:

CBM DOS

Language: BASIC

Hardware: Apple II, Apple III, or CBM/PET with

Strobe 100 Plotter

Description: Strobe Business Graphics provides excellent high-resolution (.002") graphics for business and scientific applications. Cost of plotter, interface, and software is approximately \$1000.

Price: \$145.00

for software only

Includes line, bar, and pie chart routines (menu driven) and alphanumerics.

Available:

Strobe, Inc. 897 Independence Ave.

Bldg. 5A

Mountain View, CA 94043

Name: System:

Specsystem

Apple II & Apple

II Plus

Memory: 48K

Integer BASIC or Language:

Applesoft BASIC

Hardware: Specsystem disk

for use in conjunction with

the Eventide A1B232 real-time audio spectrum

analyzer Description: Specsystem is for use with the Eventide A1B232 RTA and allows you to perform 3-dimensional spectral analysis and analyze reverb decay (RT-60). In addition, the new hi-res 31-band real-time analysis display yields far greater resolution and more dynamic range options than previously available. Variable period averaging and range switching are available along with other new functions.

Price: \$199.00

Includes Specsystem disk and complete instruction

manual. Author: Eventide

Available: Eventide, Inc. 265 West 54 Street New York, NY 10019

(212) 581-9290

GL-PLUS Name: System: Apple III 128K Memory:

Language: Business BASIC Hardware: 132-column

printer and either second diskette drive or hard drive.

Description: GL-PLUS is an extremely flexible and easy to operate general ledger with built in receivables and payables. Reports include general ledger, month's journal, balance sheet, income statement, aged receivables and payables, receivable and payable detail, and more!

Price: \$495.00 Includes operator's manual, programs, and sample

company data.

Author: Dan Sargent

Available:

Great Divide Software 8060 W. Woodard Dr. Lakewood, CO 80227

Name: System: Astroids

VIC 20 or TRS-80 Color Computer

Memory:

5K (VIC), 4K (TRS-80)

Language: BASIC Hardware: Cassette

Description: This program is an arcade-style ''dodge-the-

asteroids" game for children 7-13 years old.

Price: \$6.65 - U.S. \$7.90 - Canada

Includes program on tape,

shipping.

Author: Peter and Mike James, Colin Helsby

Available:

MFJ Electro-Enterprises P.O. Box 13076 Kanata, Ont.

K2K 1X3 (613) 592-2962 (Canada)

Name: Snack Attack Apple II Plus System: Memory: 48K

Language: Apple Machine

Language Hardware: Disk Drive

Description: An a-mazing three-maze game that's more habit forming than eating peanuts. Anyone who can play just one game deserves a medal for super self-control. You feel a Snack Attack coming on your only hope is to gobble up as many gumdrops as you can grab away from the greedy Gumdrop Guards! Our best seller for months; one try and you're hooked. Happy Snacking!

Price: \$29.95

Author: Dan Illowsky

Available: Datamost, Inc. 9748 Cozycroft Avenue Chatsworth, CA 91311 (213) 709-1202 or your local computer store

Name: Hockey Atari 400/800 System: Memory: 16K RAM Language: Assembler

(Machine) Hardware: Two, three, or

four joysticks, cassette recorder or disk drive

Description: Hockey is a highspeed video action game for two, three, or four players. It is played on an enclosed rink, with scoreboard including clock overhead. Game players use joysticks to control the action. Offensive players skate with the puck, pass, and shoot. Defensive players steal the puck and intercept passes. Goalies block shots. Hockey includes "smart" players who perform automatically.

Price: \$29.95 Available: Gamma Software P.O. Box 25625

Los Angeles, CA 90025 (213) 473-7441

Name: Adventure to Atlantis

System: Apple II 48K Memory:

Language: Machine Code Hardware: One disk drive Description: This game combines the best features of adventure games, arcade games, and fantasy roleplaying games. Adventure to Atlantis is a sequel to Odyssey: The Complete Adventure. The struggle between the forces of magic (The High One) versus the forces of science (The Atlanteans | continues. The game uses four methods to grab the player's attention: high-resolution color graphics and animation, sound effects to enhance the action, random events at all stages of the

Price: \$40.00

arcade-like action.

Includes one floppy disk. Author: Robert C. Clardy

adventure, and embedded

Synergistic Software 830 N. Riverside Drive Suite 201

Renton, WA 98055

(Continued)

Name: System: Nightmare Gallery

Apple II Memory: 48K

Language: Machine code Hardware: Disk Drive [paddles/joystick

optional

Description: This new fast-action game for the Apple Computer combines sound effects and high-speed arcade action into one challenging computer game. Players of Nightmare Gallery are trapped in a spooky graveyard with only a revolver to protect themselves against werewolves, ghouls, ghosts, vampire bats, and mummies. Firing a stream of silver bullets, players dodge around the graveyard. Points are awarded for hitting the supernatural terrors. A scoreboard is built into the game.

Price: \$34.95

Includes one floppy disk. Author: Ron Aldrich

Available:

Synergistic Software 830 N. Riverside Drive, Suite 201

Renton, Washington 98055

Name: System: Choplifter

Apple II Apple II Plus

Memory:

Language: Assembly Hardware: Joystick with two

buttons

Description: With a mission to rescue hostages from behind enemy lines, Apple owners can now take control of a helicopter in this new arcade-style game. With realistic throttle action, players can maneuver their helicopter (in high resolution and simulated 3-DI through a sky of enemy jet fighters and air mines. Once you have successfully landed your chopper, hostages will run toward you through a barrage of tank fire and air-toground missiles while you frantically try to hold off the enemy. Each flight back to safety is especially nerve racking, as a full load of hostages will perish if you go down!

Price: \$34.95

Includes software package.

Author: Dan Gorlin Available:

Broderbund Software 1938 Fourth Street San Rafael, CA 94901 1415| 456-6424

or dealers and distributors

Name:

Starship Chameleon

System:

TRS-80 Color Computer

Memory: 16K

Language: Assembly Hardware: Cassette or disk Description: In this graphics arcade game, you are the Starship Chameleon, a special intergalaxian vessel with the assignment of protecting the planet below from the aerial attack of enemy invaders. You have the unique capability to change color at the push of a button in order to destroy the on-coming super bombs and anti-matter bombs that have been launched by the enemy Gabolatoks above. But watch out for the semi-intelligent aerial bombs! They home in on your every move, seeking to destroy you! Starship Chameleon has nine levels.

Price: \$24.95 - cassette

\$29.95 - disk

Includes cassette or disk and

instructions.

Author: Kenneth Kalish

Available:

Computerware P.O. Box 668 4403 Manchester Ave. Encinitas, CA 92024 (714) 436-3512

Name:

MIMIC Speech Processors

System: Apple II Plus Description: Low-cost speech I/O. Based on newly patented technique for digital recording and reproduction of voice signals.

Price: \$199.00 for plug-in hardware.

\$34.95 for MIMTALK software (prepares user vocabulary).

Available: MIMIC, Inc. P.O. Box 921 Acton, MA 01720

Name:

Audex

System:

Apple II (with 16K RAM Card or

Apple II Plus

Memory: 48K Language: Applesoft

Hardware: Optional tape recorder

Description: Audex is a collection of utility programs that allows you to create sounds, edit them, and play them back from your own BASIC or assembly-language programs.

(Continued)



Computer Stop 16K Ram Board \$79.95

This high quality 16K Ram board acts like a Language Card when plugged into slot 0 of your APPLE II. Compatible with Basic, DOS 3.3, CP/M, PASCAL, LISA 2.5, and VISICALC. With this card you get high quality, low price, compatibility, and a 1 Year Manufacturer Warranty all for \$79.95.

LAZER Lower Case +Plus III \$34.95

The best lower case adapter available for the APPLE II. This feature packed board has twice the features of competing boards. Lazer Lower Case +Plus III is the only lower case adapter that works with VISICALC and is recommended by Stoneware for DB MASTER. The Lower Case Plus III is expandable to 4 character sets (2 on board), has inverse lower case, includes ASCII and Graphics, and is compatible with most word processors. NOTE: For REV. 6 and earlier order Lower Case +Plus at \$44.95.

LAZER Keyboard 🛨 Plus \$69.95

The Lazer Microsystems Keyboard + Plus has a 64 character type ahead buffer. The buffer can be cleared or disabled. The Keyboard + Plus lets you use the shiftkey as a typewriter shift-key, allowing you to enter directly the 128 ASCII character set from the APPLE keyboard. The Keyboard +Plus may be installed on any APPLE II.

Computer Stop **Omnivision** \$129.95

Looking for an 80-column card? Look no further! Now all your Basic, CP/M, and Pascal programs can take advantage of a full 80column display.

Wizard-BPO (Buffered Printer Interface) \$139.95

This outstanding parallel interface card comes with 16K of Ram installed, expandable to 32K This card also has Graftrax-like features. So now you no longer need to wait to use your APPLE II while your printer is busy. Compatible with Basic, CP/M, Pascal.

Microtek Parallel Printer Interface \$59.95

This popular printer interface card, manufactured by Microtek, is a steal at \$59.95. The printer card comes complete with a cable and a Centronics compatible connector (Ampheno!). Works with basic, CP/M, and Pascal. This card also has graphics capabilities.

ORDERING INFORMATION

We accept: VISA/MASTERCARD (include card #, expiration date, and signature), Cashier or Certified Checks. Money Orders, or Personnal Checks (please allow 10 business days to clear). We also accept COD's (please include \$2.00 COD charge).

Please add 3% for shipping and handling (minimum \$2.00). Foreign orders please add 10% for shipping and handling (minimum \$10.00).

California residents add 6% sales tax. All equipment is subject to price change and availability without notice. All equipment is new and complete with manufacturer's warrantv.

(714) **735-2250**

Price: \$29.95 Includes instructions. Author: Pete Kosel Available:

Sirius Software 10364 Rockingham Drive Sacramento, CA 95827

(916) 366-1195

Name: Hi-Res Ultra

Plotter Package MP-10

Any PET/CBM System:

Series 2001, 3000, 4000, 8000

Computer (specify)

Memory: 16K minimum Language: BASIC Original,

Upgrade, or 4.0

Hardware: Any Commodore graphics printer

w/programmable line spacing

Description: This is a beautiful plotting package that takes advantage of the screen graphics and printer's ultra-high resolution graphics. Equations must be entered in BASIC. Domain and/or range may be restricted, magnified, and manipulated. Printer graphing may be very small to full page and longer having a resolution of 420 by an infinite number of dots lengthwise. Multiple equations can also be plotted. Excellent for lab or school.

Price: \$12.95 cassette \$16.95 on 4040/2031 disk Includes full instructions and step-by-step guiding through program. Author: Louis F. Roehrs Available: TELE-TREX Software Systems

4 Waring Lane Littleton, CO 80121

Name: Firebug System: Apple 48K Memory:

Language: Applesoft ROM Hardware: Disk drive (dual DOS 3.2/3.3)

Description: Guide your mechanical Firebug through a five-level maze using your Apple keyboard or joystick, to the exit on Level 1. Your Firebug must race against time and fire to escape. A fuse is burning behind your Firebug, so your time is short! Add points for destroying the maze as you guide the Firebug through it. Fantasy effects include screen graphics of gas cans exploding in color, the sound of fire crackling, and the tic-toc of time running out.

Price: \$24.95

Author: Silas S. Warner

Available:

Direct from Muse and computer stores nationwide

SpeedCOBOL and Name: ReadCOBOL.

Memory: 64K

Language: COBOL (ANSI 74) Hardware: Any micro, mini,

Description: SpeedCOBOL is

or mainframe

Incorporated 7219 Shea Court

an abbreviation expander for COBOL-reserved words and phrases, data names, condition names, file names, and any other string of one to 30 characters. A direct 50% of coding and keying productivity improvements can be realized and measured with Speed-COBOL. Working with abbreviations of one or two characters for COBOL words and phrases, SpeedCOBOL precompiles these abbreviations their full length. Read-COBOL addresses the seemingly ever-present programming resource drain of COBOL program maintenance by establishing program structure standards. These standards are maintained by ReadCOBOL automatically, not by the programmer. Paragraph sequencing, standard structuring and stylizing are an automatic and direct result of ReadCOBOL. An optional step of renaming data names, file names, etc. is

statements are all direct benefits to the user. Price: \$500 for microcomputers \$5000 for mainframes Includes documentation and

available for improved pro-

gram quality. Inspecting the

results of ReadCOBOL makes

the improvement in quality

obvious. Paragraph sequenc-

ing, installation-wide standard

programming structure and

common stylizing of source

Author: Foundation for Software Engineering

Computers Et Cetera 524 W. Broadway #111 Tempe, AZ 85282 (602) 829-0888

Price: \$625 Includes assemblers, debuggers, manuals.

Name:

screen editor.

Name:

System:

Memory:

Language:

Hardware: 6809 or

QUNIX, "C"

Screen Editor

Ounix

64K

Compiler, Full

"C" (Kernighan

and Ritchie full

8088-based

Description: The Qunix oper-

ating system is a UNIXTM-like

operating system optimized for

micros. Single-user, single-

tasking or multi-user, multi-

tasking versions are available

together with a full "C" com-

piler (supports 8087 on IBM

PC) and ultra fast macro full-

implementation|

microcomputers

Available: Quantum Software Systems

San Jose, CA 95139

Audio Spectrum Analyzer

System: TRS-80 Color Computer

Description: This program provides a colorful and graphic display of energy in sound. Audio enthusiasts can "watch the music;" color bargraphs show the relative power distribution over a nine-octave audio range. A built-in "audio kaleidoscope" mode presents random changing visual images, keyed to the tonal distribution and intensity ("color," in musical terms) of the input signal.

Price: \$19.95 Includes Program Pak, instruction manual.

Available:

Radio Shack, Computer Centers and participating dealers

Name: Computer Business Software Ohio Scientific

System: Language: BASIC

Description: This complete business package features order entry, inventory control, mailing labels, sales reports/ history graphs, monthly cash reports, G/L distribution report, multi-state, multi-pay period payroll [with file locking), print spooling, and much

more.

Price: \$64.70 Available:

MicroSoftware International,

Incorporated 3300 S. Madelyn Sioux Falls, SD 57106 1-800-843-9838

Name: Jinsam Executive Memory: 64K

Language: Machine Hardware: CBM w/8050, IBM-PC

Description: This fast, powerful, flexible data-management system contains file handling, retrieval, modeling, statistical analysis, reports and labels, password entry, multi-user command files, merge, link, and join bases. It holds 65,000 records, unlimited fields, unlimited record lengths. Retrieves by scanning record, number, or key.

Price: \$40 - User's Manual \$1295 entire unit Entire unit includes interfaces, wordprocessing, VisiCalc, statistics, ROM, disks, pen, binder, and manual.

Author: Jim and Nancy Iscaro

Available:

Jini Micro-Systems Inc. Box 274 Kingsbridge Station Riverdale, NY 10463

ClassifiedTM Name: System: Apple II, Apple II

48K Memory: Language: Machine Hardware: One disk drive minimum

Description: ClassifiedTM encrypts and decrypts the information stored in any standard DOS 3.3 diskette file, such as a file created by VisiCalc. The data sectors are overwritten by cipher text, leaving no trace of plain text.

Price: \$39.50 Includes 24-page encryption

manual.

Available: Passage Research 945 Turquoise St. Suite G

San Diego, CA 92109 (714) 488-5358

/AICRO

Hardware Catalog

Name: System:

Dual-Comm Plus Apple II or Apple

II Plus

Language: Apple Pascal, **BASIC**

Description: This two-port serial I/O card combines the features of the Apple High Speed Serial Card and the Apple Communications Card in one card with handshaking. Thumbwheel switches select the Apple slot locations. Selectable baud rates range from 50 to 19200 baud. Onboard firmware provides extensive printer and upper/lower case terminal modem support. Combined with an 80-column card, such as the FULL-VIEW 80, it turns an Apple into a full-operation terminal for a computer network system.

Price: \$239.00 Includes all available features.

Available:

BIT 3 Computer Corporation 8120 Penn Ave. S. Suite 548 Minneapolis, MN 55431 (612) 881-6955

Name:

Model 122 Industrial Trade **Graphics Printer**

System: All computers Description: Heavy-duty 120 CPS desktop graphics printer. 132 columns with pin-addressable graphics. Designed to fulfill applications such as business analysis, computeraided designs, data processing, and timesharing computer processing.

Price: \$1195 list price Includes all standard features.

Available:

Centronics Data Computer Corporation 1 Wall St. Hudson, NH 03051

Name: System: Disk-O-Mate

Commodore PET/CBM

Hardware: 2040/4040 disk

drive

Description: Disk-O-Mate provides the user with writeprotect switches/indicators for each drive, a power ON indicator, and audible error beeper. The ease of write protecting drives by switch versus diskette labels encourages good operating procedure to prevent accidentally erasing files. All switches and indicators are mounted in a small control box that may be placed on the disk unit or near the computer. Installation is accomplished without special tools or skills or any soldering. Price: \$70 - \$80

Includes assembled unit plus installation/user instructions.

Available: Optimized Data Systems P.O. Box 595 Placentia, CA 92670

Name: Olympia ESW 102 Memory: 4K Bytes

Description: This is a highquality, cost-effective printer developed for the letter quality printer market. Standard lettering with varying type styles, proportional spacing, four pitches, bi-directional printing, and print wheel cassette insertion. Print speed is 17.5 characters per second. Lasersharp print quality, a choice of ribbon systems and economical pricing for end users and OEMs.

Price: \$1590.00 Includes interface.

Available:

Olympia USA Inc. Box 22. Somerville, NJ 08876

Name:

Model T Computer Slide System Apple II Plus

System: Memory:

Language: BASIC Hardware: Two disk drives,

48K

graphics tablet, and color monitor

Description: Product allows user to produce 35mm color slides. Sold as add-on to existing Apple II Plus with two disk drives, graphics tablet, and monitor. Product consists of a film recording device, software, and overlay for tablet.

Images are created and previewed with the system. Once the desired image is created the computer transfers it to 35mm color film. The film is then removed from the film recording device and processed. The system produces business informational slides and artistic slides. Five type styles are currently available in two sizes. The color on the slides is verv rich.

Price: \$3495.00 (without Apple components Includes film recording device, software, graphics tablet overlay, interface card, and manual.

Available:

Toucan Visual Production Systems 1033 Battery Street San Francisco, CA 94111 (415) 392-2970

Name:

Model 85 Digital Memory Oscilloscope

System:

Compatible with Apple II or Apple II Plus

11.5K Bytes Memory:

Language: BASIC or assembly language

Description: Laboratory and automated measurements were never easier with this completely programmable digital memory oscilloscope controlled by a co-resident BASIC program. Two channels with 50 MHz bandwidth combined with full 8-bit A/D conversion provide high accuracy, high frequency measurements. Unlimited storage time, waveform storage on disk, waveform averaging, cursor DVM, hard copy output, compare to reference waveforms, and more.

Price: \$995.00 Includes Model 85, user's manual, software on one 5¼" floppy disk.

Available:

Northwest Instrument Systems, Inc. P.O. Box 1309 Beaverton, OR 97075 (800) 547-4445

Name:

I/OX-122 I/O Expansion Board

System: 6500

Description: I/O expansion for the SYM-1, AIM-1, and other microcomputers that use 6522 VIAs for I/O and do not provide full address decoding onboard. This board has physical space for four additional 6522 VIAs, and provides additional decoding for a total of 16 devices at intervals of your choice. Connectors for all I/O lines, and further expansion are included. All 6522 functions are available, with no interference with previous functions of the original VIA. Two versions of this board are available. The I/OX-122 mounts above, and directly plugs into, an on-board 6522 socket, and relocates the original VIA to the expansion board. Where there are space limitations, the I/OX-222 uses a dip header and cable for remote installation.

Price: \$49.95

OEM and dealer inquiries

welcome.

Includes expansion board and one 6522.

Available:

Alternative Energy Products P.O. Box 1019 Whittier, CA 90609

ISAAC Name: System: Apple Memory: 48K

Labsoft (Extended Language:

Applesoft BASIC

Hardware: Disk drive,

monitor, printer

Description: ISAAC provides a hardware interface and Labsoft language to allow the Apple II Plus to interface with real world signals. Used for laboratory automation, electronic tests, or process control.

Price: \$3950.00

as addition to current Apple. \$6000-\$9000 for total system with Apple and various

peripherals. Available:

Cyborg Corporation 55 Chapel Street Newton, MA 02158

(Continued)

Hardware Catalog (continued)

Name:

AMDEK 13-inch "Color III" Monitor

Apple II, Apple III System: Hardware: Monitors, plotter,

disk drive

Description: The AMDEK 13-inch color monitor provides affordable high-plus resolution graphics. It is designed with RGB video input and a commercial grade CRT; sharp and crisp color separation, 260 (H) × 300 (V) line resolution; 80-x 24-character display capability.

Price: \$569.00 Available:

AMDEK Corporation 2201 Lively Blvd. Elk Grove Village, IL 60007

(312) 364-1180 TLX: 25-4786

Name:

UP-9705 5¼" Winchester Disk

Subsystem

System:

Compatible with many systems including Apple, IBM PC, Q-bus, \$100, and multibus systems.

Description: This is a 5-megabyte universal 51/4" Winchester mass-storage subsystem that features interchangeable host adaptor personality cards. The unit measures $4.75 \times 6.5 \times 14.2$ inches and weighs 16 lbs.

Price: \$2.995 for one Includes one host adaptor and documentation.

Available:

United Peripherals 432 Lakeside Drive Sunnyvale, CA 94086 (408) 730-4440 TWX: 910-339-9359

Name: System: Memory: Appli-Card Apple II 64K on-board

memory

Hardware: Z80

Microprocessor Description: The Appli-CardTM is a single card that uses the CP/M operating system and can execute Word-StarTM and use its full features. The hardware contributions of the Appli-Card are 64K of oncard memory for application development and execution. The Appli-Card comes standard with a 4 MHz Z80A or a 6 MHz Z80B. Also, the Z80A or Z80B CPUs can run at their maximum speed. One can

have from 2K to 8K of EPROM on the card and a real time clock is supported. An expansion interface port is available for added extra memory for future versions of CP/M and to support other Z80 peripherals. The 6502 and the Z80A or Z80B processors are able to run simultaneously and at full speed. The Appli-Card is designed to support CP/M applications with only one card. The unique SoftVideoTM features include upper and lower case, 40-column to 255-column horizontal scrolling, and also 70 columns by 24 lines is available using the high-resolution graphics on the Apple II.

Price: \$595.00

Includes Appli-Card, CP/M,

and WordStar.

Available:

Personal Computer Products, Inc.

16776 Bernardo Center Dr.

Suite 203

San Diego, CA 92128

Name:

Ultimate RS-4/OV-1 Computer

System: Memory: Dedicated 64K - 48K ROM, 16K RAM

Language: Machine Hardware: TeleVideo 912-C

Terminal w/builtin board (64K) by Cannella Corp.

Description: The Ultimate computer is designed to handicap horse racing — makes a pro out of even an amateur. The Ultimate computes and rates according to the principle of established class, assigns each horse a specific rating, handicaps, summarizes data for up to 25 horses per race, and displays them in order of betting qualification.

Price: \$6,000

\$5,000 for the computer \$1,000 for Epson MX-80

Printer

Available: Cannella Corp. Armond F. Cannella, Pres. 420 E. Genesee St.

Suite 208 Syracuse, NY 13202

Name: System: Memory:

Slim 81-260 Apple II 2K RAM, 4K

EPROM expandable to 32K Hardware: [BE Parallel

Interface Card and **EPROM** Programmer

Description: Slim is a 4.5 × 6.5 single-board large-scale integration microcomputer using a 6502 microprocessor, two 6522 VIAs, four 2114 RAMs, and 2516, 2716, or 2532 EPROM. The fully-buffered 22/44-pin bus is similar to the KIM, SYM, and AIM expansion connector. Four 8-bit I/O ports connect through 16-pin

Price: \$199.95 Available: John Bell Engineering 1014 Center Street

DIP sockets.

San Carlos, CA 94070

Name:

Apple Interface Breadboard

System: Memory:

Apple II 16K Read/write,

minimum configuration

Language: BASIC or assembly

Description: The Apple Interface Breadboard provides controlled access to those signals needed to control external devices. User can design and operate custom interfaces using BASIC. Data bus is fully buffered and protected. Up to eight decoded outputs available; up to 256 I/O devices may be addressed. A probe circuit detects logic signals and pulse edges. All interface signals available through solderless breadboard for performing experiments using hookup wire.

Price: \$235.00 — Kit (BG-125K) \$310.00 — Assembled (BG-125A) for 110V operation. Includes power supply, all parts, BG-110 Cablecard, and text Apple Interfacing w/Exp. Required but not included — interconnect cable (BG-Cable (\$14.95). Available:

Group Technology, Ltd. P.O. Box 87 Check, VA 24072 (703) 651-3153

212LP Direct Name: Connect Modem

RS-232 System: Description: The 212LP is a 1200 bps-only full duplex, asynchronous modem. This manual answer unit requires no external AC power. All operating power is from the telephone line. It is FCC certified for direct connection to the public-switched telephone network and compatible with the high-speed channel of the Bell 212A

Price: \$495.00

Includes operating manual, phone connect cable, and warranty.

Available:

Universal Data Systems 5000 Bradford Drive Huntsville, AL 35805

Name:

PromQueen VIC Cartridge

Commodore System:

VIC-20

Memory: 4K up Hardware: Requires

expansion chassis

if used with other expansions. Language: 6502 machine

code, BASIC, other uP machine

codes

Description: A self contained cartridge mating to the VIC's expansion port equips the VIC-20 for 8-bit microprocessor system development. The cartridge contains 4K's of RAM accessible both to the VIC and to an external microprocessor system under development. DIP switch sets to any of 4 expansion blocks of the VIC.

Price: \$169.50 Includes 24 pin textool socket and built-in 2716, 2732 and 2732A EPROM programmer with power supply. Development and EPROM utility software on 2732A EPROM, 32 page manual.

Available:

Gloucester Computer Bus Co., Inc. 6 Brooks Road Gloucester, MA 01930

(617) 283-7719

Name:

MPC Bubble Memory Board Apple II, Apple II

System: Plus

Memory: 1 Megabit Language: BASIC, Pascal, CP/M

Description: Bubble Memory Board has 128K bytes of nonvolatile memory. It comes with software to emulate one floppy disk; autoboot PROM on board is for stand-alone pseudo disk operation.

(Continued)

Hardware Catalog (continued)

Price: \$895.00 Includes board/disk emulator software.

Available:

MPC Peripherals Corp. 9424 Chesapeake Drive San Diego, CA 92123

Name: System: MetaCard Apple II or Apple

II Plus Memory: 48K min. Hardware: Disk II

Language: CP/M-86 Supplied Description: MetaCard is a secondary processor card with: 5Mhz. Intel 8088 microprocessor, 64K bytes dynamic RAM expandable to 128K, external power supply, 2716 EPROM with power-up diagnostics and initialization logic, real-time clock, CP/M-86 operating system with optional MS-DOS or UCSD Pascal IV.0, provision for integration with future products, including 8087 piggy-back board and additional 256K memory card.

Price: Introductory prices: 64K-\$980, 128K-\$1230 (U.S. and Canada) Includes power supply, 64K RAM (pre-tested), CP/M-86 on Apple disks, complete documentation.

Available:

Metamorphic Systems, Inc. P.O. Box 1541 Boulder, CO 80306

Pi-1. Pi-2. Pi-3 Name: Video Monitors

Description: Low-cost, high resolution monitors are available in 9" or 12" versions. Features are: 80x24 character display, resolution of 1000 lines at center, band width 20 Mhz. A front metal case provides complete shielding allowing stackability controls. It is compatible with all small business computers. Price: Pi-1, \$249; Pi-2, \$275;

Pi-3, \$289 retail. Available:

USI International, Computer Products Division 71 Park Lane, Brisbane, CA 94005 (515) 468-2900

Name:

Electric Typing Fingers (ETF-80)

Hardware: Comes with own power supply and cables

A peripheral that turns an IBM Selectric* (or similar electronic typewriter) into a fast, letter-quality, economical printer. It is a sensible alternative to purchasing the more expansive daisy wheel printer and there are no typewriter modifications necessary.

*Selectric is a registered trademark of IBM, Incorporated.

Price: \$495.00

Includes power supply and connecting cable.

Available:

Personal Micro Computers,

475 Ellis Street Mt. View, CA 94043 (415) 962-0220

Name:

PKASO ID12 -Color Interface

System:

Apple II or Apple III Computers

Hardware: ID12 - Color Interface

Description: The ID12 - color Interface converts your Apple Computer and IDS Color Prism printer to a complete text and graphic output system. Features include: full snapshot dump of any screen image, hi-res and lo-res screen prints using accurate screen colors, quick commands to swap and rearrange the color set, quick commands to change printing colors even within the word processor text.

Price: \$195.00 - \$225.00 Includes cable, instructional diskette and comprehensive manual.

Available:

Interactive Structures 146 Montgomery Ave. P.O. Box 404 Bala Cynwyd, PA 19004 (215) 667-1713

Name:

Model 140-RS Computer Interface

System:

Accepts serial data from computer to operate random access slide projector, Kodak or Mast

Description: This interface is designed to convert an asynchronous-serial data stream into signals usable by Master Kodak random-access slide projectors.

(Continued)



Microtek Parallel **Printer Interface** \$59.95

This popular printer interface card, manufactured by Microtek, is a steal at \$59.95. The Printer card comes complete with cable and a Centronics compatible connector (Amphenol). Works with Basic, CP/M, and Pascai. This card also has graphics capabilities.

Diskettes w/Hubring 10 \$19.95

High quality diskettes at a bargain price. Everyone needs diskettes for backing up other disks, saving programs, etc. We buy these diskettes in bulk and then pass the savings onto you. Remember, they do have hubring and come with a 1 year guaranty. NOTE: Please call for quanities of 100 or more for special pricing.

Auto-Repeat Device \$14.95

For those who want the feature that many Main-Frame Computers have, here is the Auto-Repeat device. This device does not take up a slot or crowd your APPLE II. Auto-Repeat fits right on the newer style Apple Keyboards. The speed of the Auto-Repeat can be varied to suit your needs. NOTE: Auto-Repeat device will only work on newer APPLE II keyboards.

Lazer Lower Case + Plus II \$19.95

For the budget minded user with quality in mind. This lower case adapter will work with all Rev. 7 and later AP-PLE II's. The Lower Case + Plus II includes Basic and Pascal software. Works with many popular word processors.

Also Available From LAZER

ANIX 1.0 \$34.95, This software program is a set of incredible disk utilities with a UNIX-like operating system. LAZER Pascal \$29.95 A unique systems programming language for Anix 1.0 with many features of the 'C' programming language.

Disk Drive Cables \$24.95

These cables replace the cables that are connected to the Apple Disk Drive already. If you feel that can not put your Disk Drives where you want them, here's your answer. The Cables are 4' long and are pre-tested for your assurance.

ORDERING INFORMATION

We accept: VISA/MASTERCARD (include card #, expiration date, and signature), Cashier or Certified Checks, Money Orders, or Personal Checks (please allow 10 business days to clear). We also accept COD's (please include \$2.00 COD charge).

Please add 3% for shipping and handling (minimum \$2.00). Foreign orders please add 10% for shipping and handling (minimum (\$10.00).

California residents add 6% sales tax. All equipment is subject to price change and availability without notice. All equipment is new and complete with manufacturer's warranty.

(714) **735-2250**

Hardware Catalog (continued)

Price: \$621.00

Includes interface package, connectings cable.

Available:

Mast Development Co. 2212 East 12th Street Davenport, IO 52803

Name:

Corvus Systems Disks

Atari 800 System: Memory: 6 Megabits Hardware: Winchester Disk Description: The Corvus System 6 MB hard-disk drive interfaces with Atari 800. It has two plotters and four data surfaces: the diameter is 51/4 inches.

Price: \$3,195 Includes Z80 microprocessor, controller, managing bidirectional data transfer.

Available:

Corvus Systems 2029 O'Toole Ave. San Jose, CA 95131 (408) 946-7700

Name: System:

Slot 8 Apple II, Apple II

Plus

Memory: 16K

BASIC, Pascal, Language: CP/M, Assembly (6809, 6502, etc.)

Description: Slot 8 is a spacesaver for the Apple II and provides the user with an extra slot for peripheral use. Slot 8 plugs into slot 7 and provides two slots on the Apple motherboard. Most peripheral cards for the Apple will work in Slot 8.

Includes Slot 8 card and manual of operation.

Available: Legend Industries, Ltd. 2220 Scott Lake Rd. Pontiac, MI 48054 (313) 674-0954

Name:

DATASAVERTM **AC Power Backup** Unit

System: Anv

Description: DATASAVERTM provides uninterrupted AC power to microcomputer systems and instruments. The 90 watt capacity unit is for Apple II/III and TRS-80 III systems. The 200 watt unit covers larger systems including 54" fixed disk drives. The desk-top unit is just $4'' \times 6'' \times 9\frac{1}{2}$ and has internal battery for five minutes of automatic power hold-up.

Price: \$395 - 90 watt \$695 - 200 watt Includes overvoltage protection and power status indicator LED, buzzer, and interrupt signal.

Available:

Cuesta Systems, Inc. 3440 Roberto Court San Luis Obispo, CA 93401 (805) 541-4160

Name:

2809 Softboard System

6809 SS50 Bus System:

computers (SWTPC, Gimix, Smoke Signal

Description: The 2809 softboard system enables 6809 SS50 Bus users to immediately run over 2000 CP/M application programs on their computers

Price: \$595.00 Includes 50 pin processor board, CP/M 2.2, editor,

assembler, debugger, and complete reference manuals.

Available: Meta Lab P.O. Box 1559 Suite 106 2888 Bluff Street Boulder, CO 80301 (303) 499-4236

Name: M 78-5 TrimForm Printer

Memory: Line buffer, or 1K data buffer

Language: ASCII

Description: This printer has 140 characters per second, tractor feed for forms up to 9.5' wide, 96-character set (upper/ lower), OCR-A (optional), bar code, vertical forms control unit, quiet 56 dbA, RS-232 serial interface, integral form cutting device under host control, and self test.

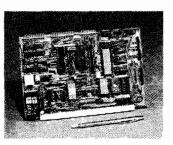
Price: \$4345 Includes all hardware. Mannesmann Tally

8301 South 180th St. Kent, WA 98031 (206) 251-5524

MICRO

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- Keyboard / Parallel Port
- 24K Bytes of Memory **OBUG** Resident Monitor
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 - **★ Memory Test**
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- ★ Memory Exam & Exchange ★ Zero Memory ★ Fill Memory Memory Dump
- * Jump to User Program * Register Display & Change
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How to run a listing in Software/Hardware

The Software and Hardware Catalogs are provided as a service both to our readers and to the manufacturers. These entries are not MICRO reviews, but descriptions provided by the manufacturer.

To run a free listing in either catalog, a company fills out the appropriate form or merely mails in their material in the same format that appears in the magazine.

We try to limit entries to one company per month, on a first-come-first-serve basis.

If you sell products our readers should know about, write to Software/Hardware Catalog, MICRO, P.O. Box 6502, Chelmsford, MA 01824.

READER SURVEY: SEPTEMBER 1982

WE NEED YOUR HELP! To keep MICRO in touch with the rapidly changing computer world so that we can give you the information you need, please take a minute to fill in this questionnaire and mail it back to us. THANK YOU for your time.

1.	How long have you subscribed to or read MICRO? Less than 6 months
2.	How did you get your current issue? ☐ Subscription ☐ Computer store ☐ Newsstand ☐ Bookstore ☐ Borrowed ☐ Library
DE	MOGRAPHICS
3.	What is your age? □ - 17 □ 18-25 □ 26-35 □ 36-45 □ 46-55 □ 56-65 □ 65 +
4.	What is your occupation? Programmer/analyst
5.	What is your formal educational level? ☐ Fewer than 12 years ☐ High school graduate ☐ Associate degree ☐ Bachelor's degree ☐ Para-professional degree ☐ Advanced degree
6.	What is your annual household income before taxes? $\ \square$ Less than \$15,000 $\ \square$ \$15,000-19,999 $\ \square$ \$20,000-24,999 $\ \square$ \$25,000-34,999 $\ \square$ \$35,000-44,999 $\ \square$ \$45,000-54,999 $\ \square$ \$55,000 +
CO	MPUTER INFORMATION
7.	What microcomputer(s) do you use? AIM Apple II Apple III Atari (please specify model) CBM/PET SuperPET VIC SYM TRS-80 Color Computer Other 6502 Other 6809 Other processor(s)
8.	Where do you use the above computer(s)? Home Work School Other
9.	Approximately how much have you spent on your computer equipment so far? □ - \$500 □ \$500-999 □ \$1,000-1,999 □ \$2,000-2,999 □ \$3,000-3,999 □ \$4,000-4,999 □ \$5,000-9,999 □ \$10,000 +
10.	Approximately how much do you expect to spend on your computer equipment in the next year? □ - \$500 □ \$500-999 □ \$1,000-1,999 □ \$2,000-2,999 □ \$3,000-3,999 □ \$4,000-4,999 □ \$5,000-9,999 □ \$10,000 +
11.	Approximately how much have you spent on your computer software so far? □ - \$200 □ \$200-499 □ \$500-999 □ \$1,000-1,999 □ \$2,000 +
12.	Approximately how much do you expect to spend on computer software in the next year? - \$200
13.	What sorts of additions to your basic system have you made? Disk Drives Modem Serial Interface Parallel Interface RAM cards 6809 card 68000 card Z80 card Other
14.	What additional changes or upgrades would you like to add to your system? Disk Drives Modem Serial Interface Parallel Interface RAM cards 6809 card 68000 card Z80 card Other
15.	How do you use your computer equipment? Business Software Development Hardware Development Telecommunications Entertainment Hobby Education Communications Word Processing (type) Database Management
16.	What languages do you use? BASIC Pascal FORTH COBOL APL LOGO LISP 6502 Assembler 6809 Assembler 68000 Assembler Other
17.	In an average week, about how many hours do you spend on a microcomputer performing the following operations? 0-2 2-4 4-8 8-10 More
	Programming for fun or self-education
18.	If you write programs, what type of programming do you spend most of your time developing? ☐ Business applications ☐ Games ☐ Software development utilities
19.	In an average month how much time do you spend with MICRO? Less than 2 hours
	How would you rate your present microcomputer knowledge? Software:
21.	Have you ever constructed a computer or computer equipment? Yes No If yes, describe
22.	Have you switched from one computer to another? ☐ Yes ☐ No If yes, explain

23 To what of		to 10 and 40 a		:0								
☐ BYTE ☐ Popular	ther computer Compute! Computing	☐ ☐[r. Dobbs [Kilobaud N	Microcomputing icrocomputing		☐ Softside ☐ Interfac	□ Softalk e Age □ Transac	tor			
24. Please rate	e the following	parts of I	MICRO as to t	neir interest,	, with 5 being v	ery interesting	and 1 not at a	all interesting.				
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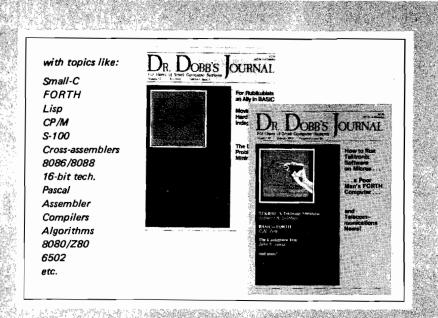
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MC68000 16-bit Microprocessor

Manufactured by Motorola. 16-bit design increases speed and addressing capability. Well-suited to high-level operating systems, including UNIX, and to modular, reentrant, and relocatable programming.

Design based on PDP-11 minicomputer, includes:

- 32-bit data and address registers
- 16 megabyte direct addressing range
- 56 different instructions
- 14 different addressing modes for most instructions
- 5 main data types: bit, byte, word, long word, and decimal Memory-mapped I/O

Programming model includes two 32-bit stack pointers, 32-bit program counter, 8-bit status register, eight 32-bit data registers, and seven 32-bit address registers. The eight data registers, seven address registers, and two stack pointers may all be used as index registers.

Included in many new computers, including TRS-80 Model 16, Corvus Concept, SAGE II, and Fortune 32:16.

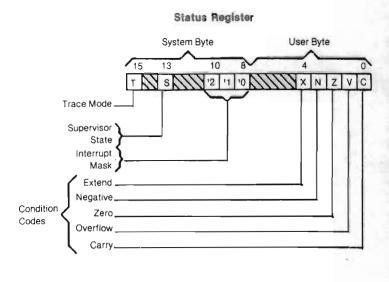
Included in add-on boards manufactured by DTACK Grounded, Motorola, and others for Apple, PET, and other 8-bit computers.

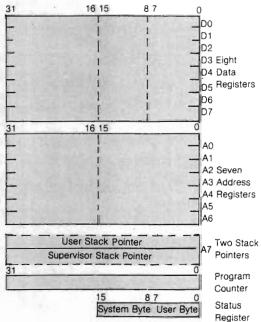
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15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
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Programming Model





Mnemonic	Description
ABCD	Add Decimal with Extend
ADD	Add
AND	Logical And
ASL	Arithmetic Shift Left
ASR	Arithmetic Shift Right
Всс	Branch Conditional
BCHG	Bit Test and Change
BCLR	Bit Test and Clear
BRA	Branch Always
BSET	Bit Test and Set
BSR	Branch to Subroutine
BTST	Bit Test
СНК	Check Register Against Bounds
CLR	Clear Operand
CMP	Compare
DBcc	Test Condition, Decrement and Branch
DIVS	Signed Divide
DIVII	Unsigned Divide

Mnemonic	Description
EOR EXG EXT	Exclusive OR Exchange Registers Sign Extend
JMP	Jump
JSR	Jump to Subroutine
LEA	Load Effective Address
LINK	Link Stack
LSL	Logical Shift Left
LSR	Logical Shift Right
MOVE	Move
MOVEM	Move Multiple Registers
MOVEP	Move Peripheral Data
MULS	Signed Multiply
MULU	Unsigned Multiply
NBCD	Negate Decimal with Extend
NEG	Negate
NOP	No Operation
NO	Ones Complement

Mnemonic	Description
OR	Logical OR
PEA	Push Effective Address
RESET	Reset External Devices
ROL	Rotate Left without Extend
ROR	Rotate Right without Extend
ROXL	Rotate Left with Extend
ROXR	Rotate Right with Extend
ATE	Return from Exception
RTR	Return and Restore
FITS	Return from Subroutine
SBCD	Subtract Decimal with Extend
Scc	Set Conditional
STOP	Stop
SUB	Subtract
SWAP	Swap Data Register Halves
TAS	Test and Set Operand
TRAP	Trap
TRAPV	Trap on Overflow
TST	Test
UNLK	Unlink

Variations of Instruction Types*

Instruction Type	Variation	Description						
ADD	ADD ADDA ADDQ ADDI ADDX	Add Add Address Add Quick Add Immediate Add with Extend						
AND	AND ANDI ANDI to CCR ANDI to SR	Logical AND AND Immediate AND Immediate to Condition Code AND Immediate to Status Register						
СМР	CMP CMPA CMPM CMPI	Compare Compare Address Compare Memory Compare Immediate						
EOR	EOR EORI EORI to CCR EORI to SR	Exclusive OR Exclusive OR Immediate Exclusive Immediate to Condition Codes Exclusive OR Immediate to Status Register						

*Additional instructions actually s	special variations of those above.
-------------------------------------	------------------------------------

Instruction Type	Variation	Description
MOVE	MOVE MOVEA MOVEQ MOVE to CCR MOVE to SR MOVE from SR MOVE to USP	Move Move Address Move Quick Move to Condition Codes Move to Status Register Move from Status Register Move to User Stack Pointer
NEG	NEG NEGX	Negate Negate with Extend
OR	OR ORI ORI to CCR ORI to SR	Logical OR OR Immediate OR Immediate to Condition Codes OR Immediate to Status Register
SUB	SUB SUBA SUBI SUBQ SUBX	Subtract Subtract Address Subtract Immediate Subtract Ouick Subtract with Extend

Data Organization In Memory Bit Data

Mode	Generation
Register Direct Addressing Data Register Direct Address Register Direct	EA = Dn EA = An
Absolute Data Addressing Absolute Short Absolute Long	EA = (Next Word) EA = (Next Two Words)
Program Counter Relative Addressing Relative with Offset Relative with Index and Offset	$EA = (PC) + d_{10}$ $EA = (PC) + (Xn) + d_{0}$
Register Indirect Addressing Register Indirect Postincrement Register Indirect Predecrement Register Indirect Register Indirect Indirect with Offset Indexed Register Indirect with Offset	$\begin{aligned} EA &= (An) \\ EA &= (An), An \twoheadleftarrow An + N \\ An &\longleftarrow An - N, EA = (An) \\ EA &= (An) + d_i_0 \\ EA &= (An) + (Xn) + d_i_0 \end{aligned}$
Immediate Data Addressing Immediate Quick Immediate	DATA = Next Word(s) Inherent Data
Implied Addressing Implied Register	EA = SR, USP, SP, PC

Data Addressing Modes

Notes:

EA = Effective Address An Dn = Address Register

= Status Register

Data RegisterAddress or Data Register used as Index Register

= Replaces

= Program Counter = Contents of = 8-bit Offset (displacement) = 16-bit Offset (displacement) = 1 for Byte, 2 for Words and 4 for Long Words

						1 Byte = 8 Bits									
	_ 7	7	6	5	2	4		3	2	2	1		0	_	
														J	
							ger Date = 8								
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	
MSB			By	yte 0			LSB				8	/te 1			
					1	Work	d = 16	Bits							
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MSB						٧	Vord ()							LSB
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MSB				100		High	n Orde	er .							
-		Word ress 0)				- 4								-	-
	,	,				Low	Orde	r							LSB

2

BCD 3

BCD 2

10

BCD 1

BCD 0

MSD

13

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Authors note to players - I wrote this one with a concordance in hand. It is very accurate and a lot of fun. It was nice to wander around the ship instead of watching it on T.V.

CIRCLE WORLD by Bob Anderson Alien culture has built a huge world in the shape of a ring circling their sun. They left NUCLEAR SUB by Bob Retelle - You start behind some strange creatures and a lot of advanced technology. Unfortunately, the world is headed for destruction and it is your job to save it before it plunges into the sun!

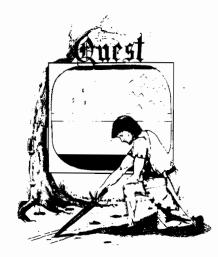
Editors note to players - In keeping with the large scale of Circle World, the author wrote a very large adventure. It has a lot of rooms and a lot of objects in them. It is a very convoluted, very complex adventure. One of our largest. Not available on OSI.

HAUNTED HOUSE by Bob Anderson - This one is for the kids. The house has ghosts, goblins, vampires and treasures - and problems designed for the 8 to 13 year old. This is a real adventure and does require some thinking and problem solving - but only for kids.

Authors note to players - This one was fun to write. The vocabulary and characters were designed for younger players and lots of things happen when they give the computer commands. This one teaches logical thought, mapping skills, and creativity while keeping their help others first if he/she is to survive - I like interest.

son — For Wealth and Glory, you have to ran-sack a thousand year old space ship. You'll through the Pyramid is 50 to 70 hours. The have to learn to speak their language and operate the machinery they left behind. The hardest problem of all is to live through it.

is the new winner in the "Toughest Adventure at Aardvark Sweepstakes". Our most difficult problem in writing the adventure was to keep it logical and realistic. There are no irrational traps and sudden senseless deaths in Derelict. This ship was designed to be perfectly safe for its' builders. It just happens to be deadly to alien invaders like you.



at the bottom of the ocean in a wrecked Nuclear Sub. There is literally no way to go but up. Save the ship, raise her, or get out of her before she blows or start WWIII

Editors note to players - This was actually plotted by Rodger Olsen, Bob Retelle, and someone you don't know - Three of the nastiest minds in adventure writing. It is devious, wicked, and kills you often. The TRS-80 Color version has nice sound and special effects.

EARTHQUAKE by Bob Anderson and Rodger Olsen - A second kids adventure. You are trapped in a shopping center during an earthquake. There is a way out, but you need help. To save yourself, you have to be a hero and save others first.

Authors note to players - This one feels good. Not only is it designed for the younger set (see note on Haunted House), but it also plays nicely. Instead of killing, you have to save lives to win this one. The player must

old boys who built this Pyramid did not mean for it to be ransacked by people like you.

Authors note to players — This is a very

Authors note to players — This adventure he new winner in the "Toughest Adventure Aardvark Sweepstakes". Our most difficult of the macked by people like you.

Authors note to players — This is a very entertaining and very tough adventure. I left clues everywhere but came up with some ingenous problems. This one has captivated people so much that I get calls daily from as people so much that I get calls daily from as far away as New Zealand and France from bleary eyed people who are stuck in the Pyramid and desperate for more clues.

> QUEST by Bob Retelle and Rodger Olsen THIS IS DIFFERENT FROM ALL THE OTHER GAMES OF ADVENTURE!!!! It is played on a computer generated map of Alesia. You lead a small band of adventurers on a mission to conquer the Citadel of Moorlock. You have to build an army and then arm and feed them by combat, bargaining, exploration of ruins and temples, and outright oanditry. The game takes 2 to 5 hours to play and is different each time. The TRS-80 Color version has nice visual effects and sound. Not available on OSI. This is the most popular game we have ever published.

> MARS by Rodger Olsen - Your ship crashed on the Red Planet and you have to get home. You will have to explore a Martian city, repair your ship and deal with possibly hostile aliens to get home again.

> Authors note to players — This is highly recommended as a first adventure. It is in no way simple—playing time normally runs from 30 to 50 hours—but it is constructed in a more "open" manner to let you try out adventuring and get used to the game before you hit the really tough problems.



ADVENTURE WRITING/DEATHSHIP by Rodger Olsen — This is a data sheet showing how we do it. It is about 14 pages of detailed instructions how to write your own adventures. It contains the entire text of Deathship. Data sheet - \$3.95. NOTE: Owners of OSI, TRS-80, TRS-80 Color, and Vic 20 computers can also get Deathship on tape for an additional \$5.00.

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October: Education Feature

- Three Faces of Apple LOGO An indepth discussion of the three available versions of the educational computer, LOGO.
- A Personal Computer System for **Untrained Users** — A look at Applesoft BASIC programs that assist untrained persons in using an unsupervised computer.
- One Teacher's PET The author, a fifth grade teacher, discusses the growing involvement of PET computers in her classroom.
- Introduction to Turtle Graphics This article describes the Turtle Graphics routines available with Apple Pascal. A graphics screen dump is also included.

Department Highlights

Data Sheet Apple Slices **PET Vet** New Publications Reviews in Brief Software and Hardware Catalogs

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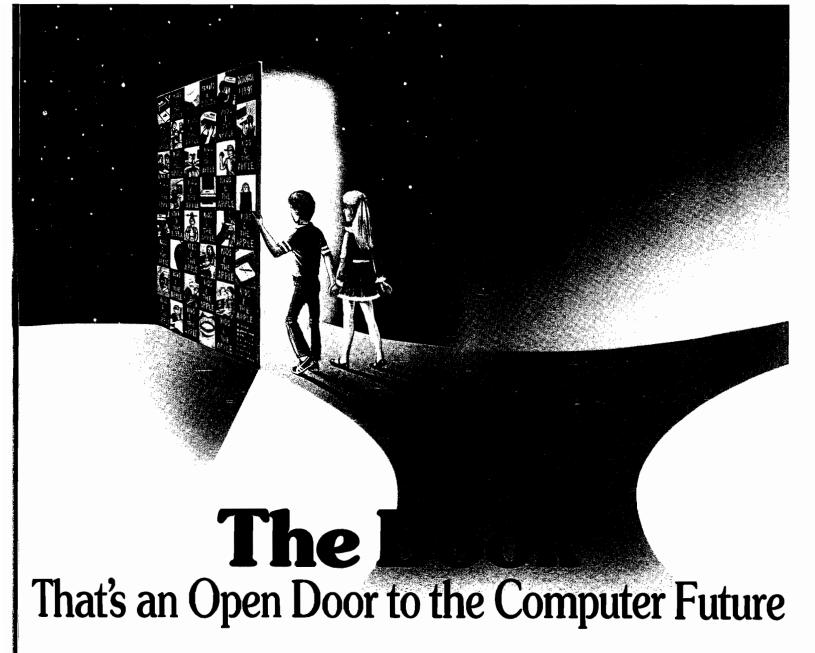
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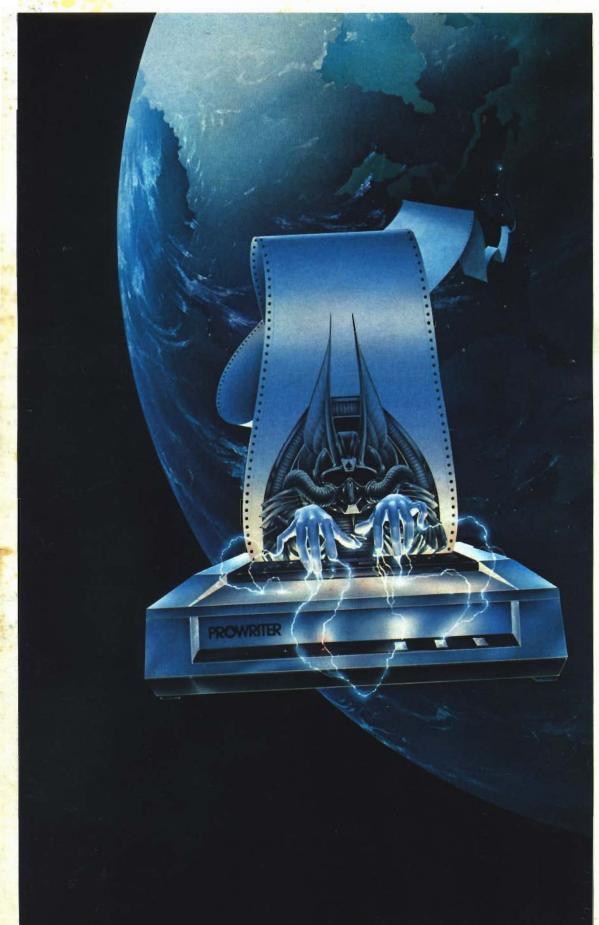
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